

**N-Channel Silicon Carbide MOSFET** 

Rev.01 - 23 April 2025

**Product data sheet** 

### 1. General description

WeEn Gen-2 Silicon Carbide MOSFET in a TO247 plastic package, designed for high frequency, high efficiency systems.

# 2. Features and benefits

- Low specific on-resistance
- Optimized dynamic performance
- Robust gate design
- 0V turn-off V<sub>GS</sub> for simple gate driving
- 100% UIS Tested
- Easy to parallel
- RoHS compliant

### **3. Applications**

- PC/server/telecom power supplies
- UPS & Energy storage system
- Battery formation instrument
- PV MPPT and inverters
- EV Chargers
- Motor Drives

# 4. Quick reference data

<mark>Γable 1. Qι</mark>	lick reference data						
Symbol	Parameter	Conditions	Notes	Values		Unit	
Absolute	maximum rating						
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C		650			V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C			68		А
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C		294		W	
Tj	junction temperature			-55 to 175			°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$R_{\text{DS(on)}}$	drain-source on-state resistance	$V_{GS}$ = 15 V; I <sub>D</sub> = 20 A; T <sub>j</sub> = 25 °C		-	55	-	mΩ
		$V_{GS}$ = 18 V; I <sub>D</sub> = 20 A; T <sub>j</sub> = 25 °C		-	44	57	mΩ
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge $I_D = 20 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V}$		-	59	-	nC	
$Q_{GD}$	gate-drain charge	T <sub>j</sub> = 25 °C		-	7.4	-	nC
Source-d	rain diode	·					
Q <sub>r</sub>	recovered charge	$I_{SD}$ = 20 A; di/dt = 500 A/µs; $V_{DS}$ = 400 V; $T_{j}$ = 25 °C		-	66.4	-	nC



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# 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	D	drain		
3	S	source		
mb	D	mounting base; connected to drain	TO247	sym300 S

# 6. Ordering information

Table 3. Ordering information								
	Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date	
	WNSC2M55065W	TO247	WNSC2M55065W6Q	Tube	30	TO247P	09-Mar-2023	

# 7. Marking

Table 4. Marking codes							
	Type number	Marking codes					
	WNSC2M55065W	WNSC2M					
		55065W					

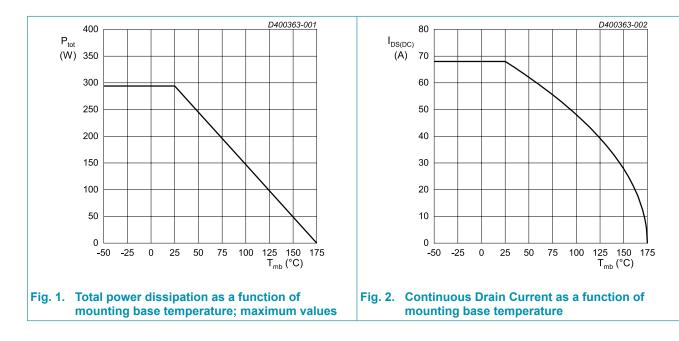
### **N-Channel Silicon Carbide MOSFET**

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C		650	V
$V_{\text{GS,max}}$	gate-source voltage	Absolute maximum values		-10 to 22	V
$V_{\text{GS,op}}$	gate-source voltage	Recommended operational values		-4 to 18	V
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C		294	W
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C		68	А
		V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 100 °C		48	А
I <sub>DM</sub>	peak drain current	pulse width $t_p$ limited by $T_{jmax}$	Fig.17	136	А
ls	continuous diode current	V <sub>GS</sub> = -4 V; T <sub>mb</sub> = 25 °C		52	А
I <sub>SM</sub>	pulse diode current	$V_{GS}$ = -4 V; pulse width $t_p$ limited by $T_{jmax}$		136	A
E <sub>as</sub>	single pulse drain-to- source avalanche	$I_{AS}$ = 15.3 A; L = 1 mH; V <sub>DD</sub> = 100 V; T <sub>j</sub> = 25 °C		117	mJ
T <sub>stg</sub>	storage temperature			-55 to 175	°C
T <sub>j</sub>	junction temperature			-55 to 175	°C
T <sub>sld(M)</sub>	peak soldering temperature			260	°C



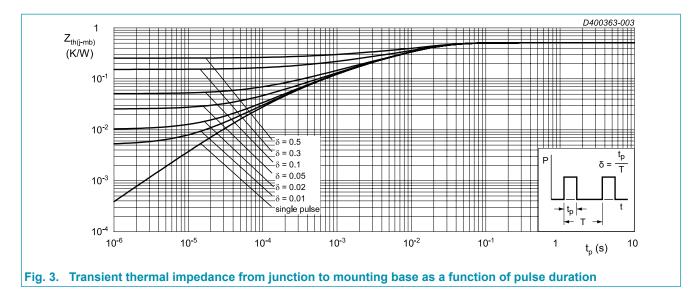
# 9. Thermal & Mechanical characteristics

#### Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
$R_{\text{th(j-mb)}}$	thermal resistance from junction to mounting base			-	0.51	-	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air		-	40	-	K/W
$M_{d}$	Mounting torque	M3 or 6 - 32 screw		-	-	0.6	Nm

Note: It is recommended that a metal washer is inserted between screw head and mounting tab. Do not use self-tapping screws.

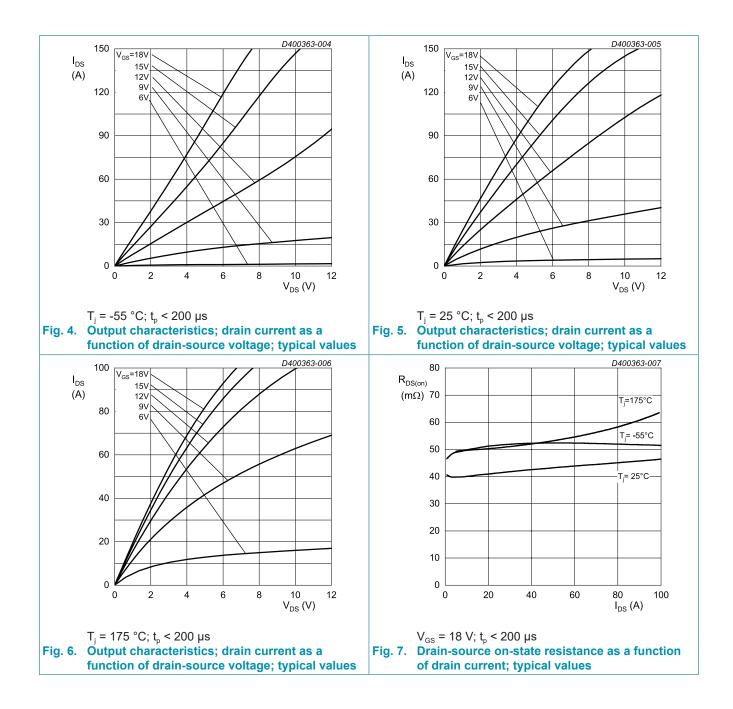
Device is ESD sensitive. Handling precautions are recommended.

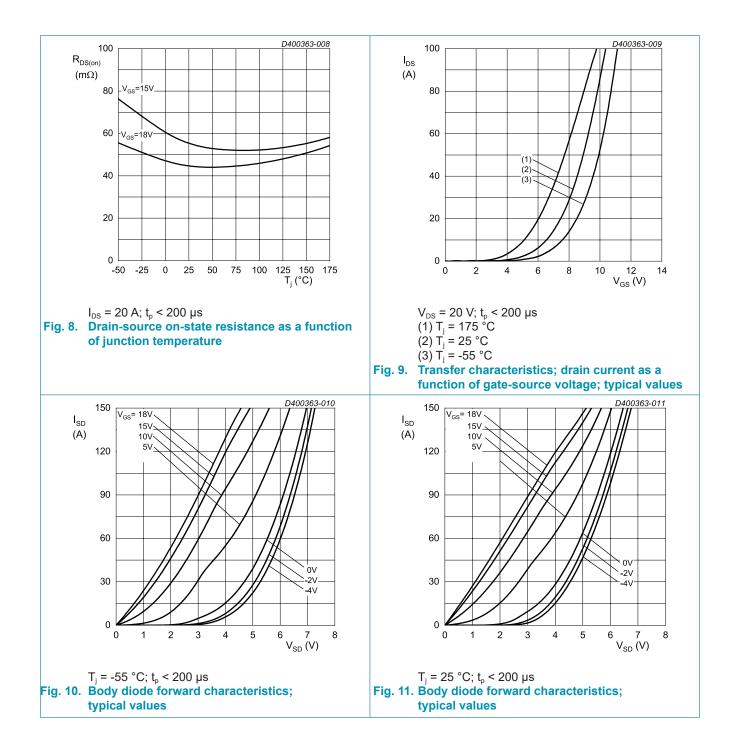


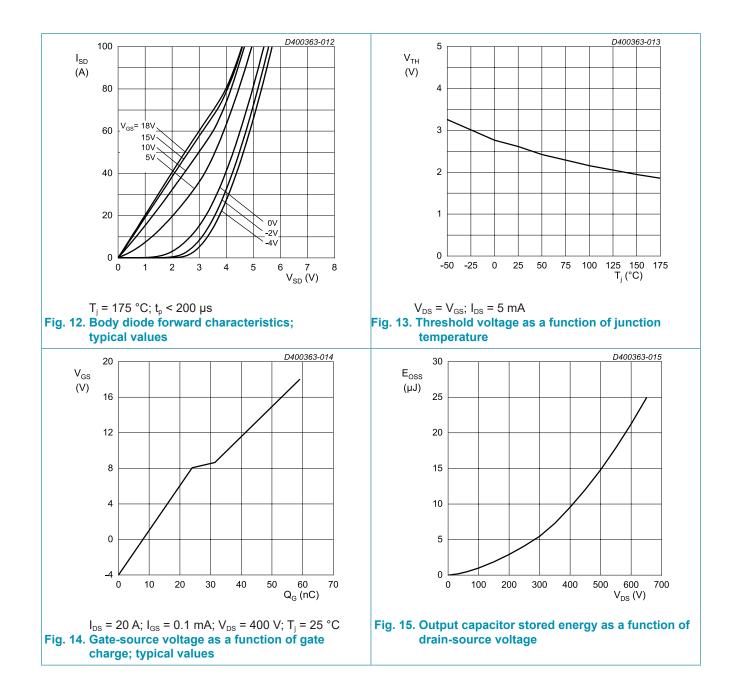
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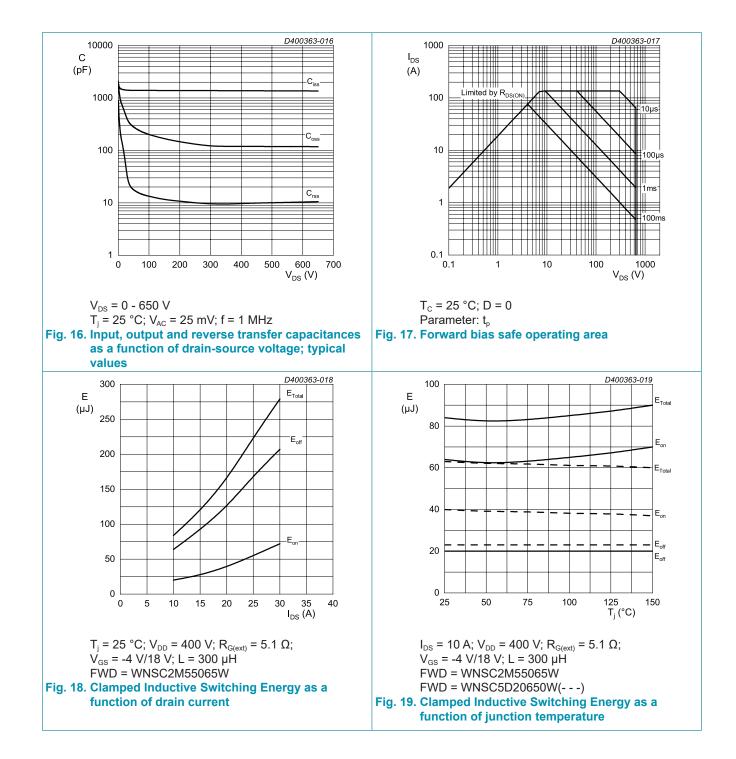
### **10. Characteristics**

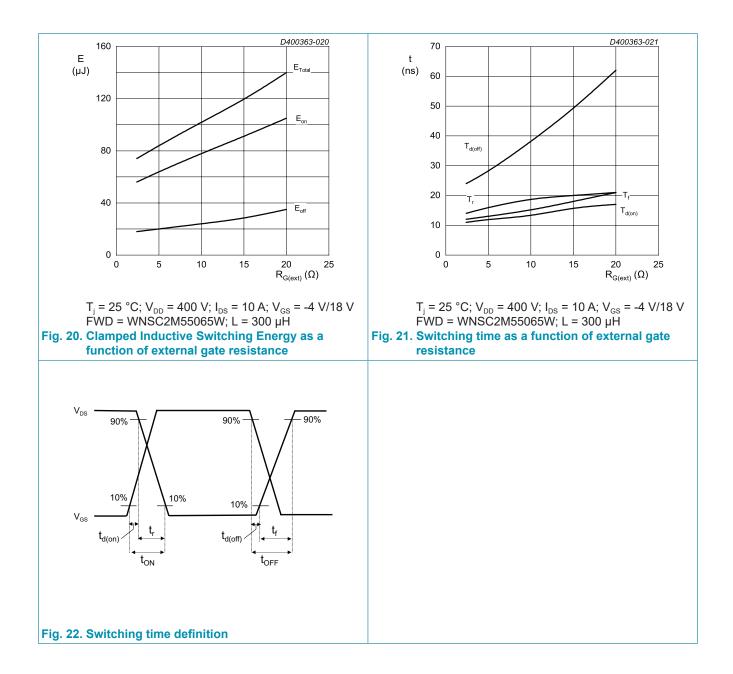
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
	aracteristics				- 76		
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_{D}$ = 100 µA; $V_{GS}$ = 0 V; $T_{j}$ = 25 °C		650	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold	I <sub>D</sub> = 5 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C		1.9	2.6	3.5	V
	voltage	I <sub>D</sub> = 5 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 175 °C		-	1.9	-	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$		-	0.1	50	μA
		V <sub>DS</sub> = 650 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C		-	5	-	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 22 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C		-	5	100	nA
		V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	5	100	nA
R <sub>DS(on)</sub>	drain-source on-state	V <sub>GS</sub> = 15 V; I <sub>D</sub> = 20 A; T <sub>j</sub> = 25 °C		-	55	-	mΩ
	resistance	V <sub>GS</sub> = 18 V; I <sub>D</sub> = 20 A; T <sub>j</sub> = 25 °C		-	44	57	mΩ
		V <sub>GS</sub> = 18 V; I <sub>D</sub> = 20 A; T <sub>j</sub> = 175 °C		-	53	-	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C		-	1.27	-	Ω
9 <sub>fs</sub>	transconductance	V <sub>DS</sub> = 20 V; I <sub>D</sub> = 20 A; T <sub>j</sub> = 25 °C		-	14	-	S
Dynamic	characteristics	I					
Q <sub>G(tot)</sub>	total gate charge	$I_{D}$ = 20 A; $V_{DS}$ = 400 V; $V_{GS}$ = -4 V/18 V;		-	59	-	nC
Q <sub>GS</sub>	gate-source charge	$T_j = 25 \ ^{\circ}C$		-	24	-	nC
$Q_{GD}$	gate-drain charge			-	7.4	-	nC
C <sub>iss</sub>	input capacitance	$V_{DS} = 400 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$ $T_j = 25 \text{ °C}$		-	1376	-	pF
C <sub>oss</sub>	output capacitance			-	119	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	10	-	pF
E <sub>oss</sub>	Coss stored energy			-	9.5	-	μJ
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 400 V; $V_{GS}$ = -4 V/18 V; $R_{G(ext)}$ = 5.1		-	12	-	ns
t,	rise time	Ω; I <sub>D</sub> = 10 A; L = 300 μH; T <sub>j</sub> = 25 °C		-	16	-	ns
t <sub>d(off)</sub>	turn-off delay time			-	28	-	ns
t <sub>f</sub>	fall time			-	13	-	ns
Eon	turn-on energy (SiC Diode FWD)		Fig.19	-	40	-	μJ
E <sub>off</sub>	turn-off energy (SiC Diode FWD)		Fig.19	-	23	-	μJ
Eon	turn-on energy (Body Diode FWD)		Fig.19	-	64	-	μJ
E <sub>off</sub>	turn-off energy (Body Diode FWD)		Fig.19	-	20	-	μJ
Source-d	rain diode				_		
V <sub>SD</sub>	source-drain voltage	$V_{GS}$ = 0 V; $I_{SD}$ = 20 A; $T_{j}$ = 25 °C		-	3.7	-	V
		$V_{GS}$ = -4 V; $I_{SD}$ = 20 A; $T_j$ = 25 °C		-	4.2	-	V
		$V_{GS}$ = -4 V; $I_{SD}$ = 20 A; $T_j$ = 175 °C		-	3.7	-	V
t <sub>rr</sub>	reverse recovery time	$I_{SD} = 20 \text{ A}; \text{ di/dt} = 500 \text{ A/}\mu\text{s}; \text{ V}_{DS} = 400 \text{ V};$		-	25.6	-	ns
Q <sub>r</sub>	recovered charge	T <sub>j</sub> = 25 °C		-	66.4	-	nC
l <sub>rrm</sub>	reverse recovery current			-	5.2	-	Α



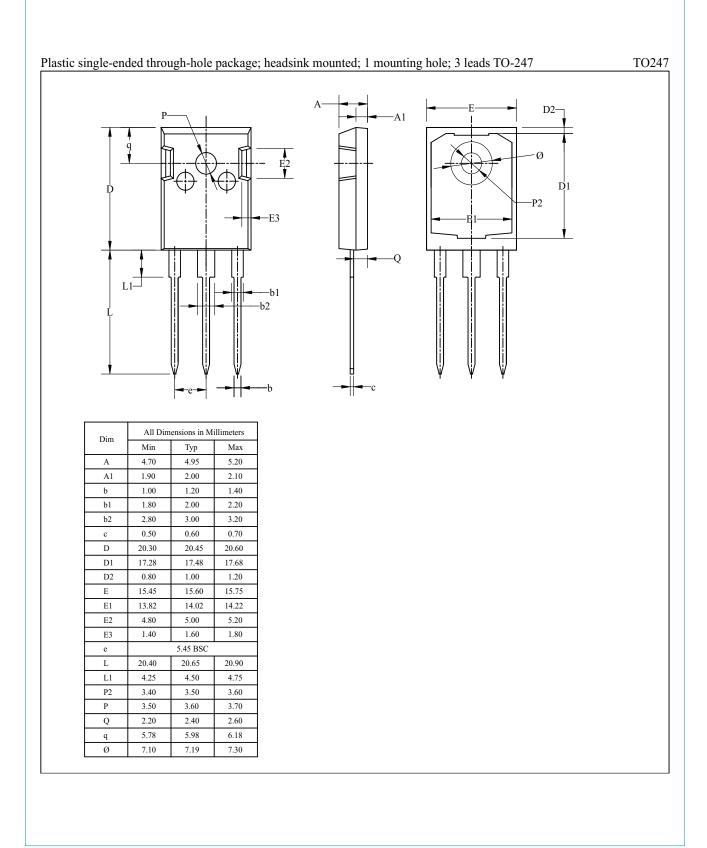








### **11. Package outline**



WNSC2M55065W Product data sheet

#### **N-Channel Silicon Carbide MOSFET**

# 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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