**Product data sheet** 

## 1. General description

WeEn Gen-2 Silicon Carbide MOSFET in a TO247 plastic package, designed for high frequency, high efficiency systems.



## 2. Features and benefits

- · Low specific on-resistance
- Optimized dynamic performance
- Robust gate design
- 0V turn-off V<sub>GS</sub> for simple gate driving
- 100% UIS Tested
- Easy to parallel
- RoHS compliant

## 3. Applications

- PC/server/telecom power supplies
- UPS & Energy storage system
- · Battery formation instrument
- PV MPPT and inverters
- EV Chargers
- Motor Drives

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit		
Absolute	Absolute maximum rating								
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C			650		V		
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C			81		А		
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C			326		W		
T <sub>j</sub>	junction temperature				-55 to 17	5	°C		
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit		
Static cha	racteristics								
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = 15 \text{ V}; I_D = 25 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	43	-	mΩ		
		$V_{GS}$ = 18 V; $I_{D}$ = 25 A; $T_{j}$ = 25 °C		-	34.5	45	mΩ		
Dynamic	characteristics		,						
Q <sub>G(tot)</sub>	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	73	-	nC		
$Q_{GD}$	gate-drain charge	T <sub>j</sub> = 25 °C		-	8.6	-	nC		
Source-di	Source-drain diode								
Q <sub>r</sub>	recovered charge	$I_{SD}$ = 25 A; di/dt = 500 A/µs; $V_{DS}$ = 400 V; $T_{j}$ = 25 °C		-	77.4	-	nC		

# 5. Pinning information

## **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	D	drain		
3	S	source		G
mb	D	mounting base; connected to drain	TO247	sym300 S

# 6. Ordering information

**Table 3. Ordering information** 

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSC2M43065W	TO247	WNSC2M43065W6Q	Tube	30	TO247P	09-Mar-2023

# 7. Marking

## Table 4. Marking codes

Type number	Marking codes
WNSC2M43065W	WNSC2M 43065W

# 8. Limiting values

### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C		650	V
$V_{\rm GS,max}$	gate-source voltage	Absolute maximum values		-10 to 22	V
$V_{GS,op}$	gate-source voltage	Recommended operational values		-4 to 18	V
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C		326	W
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C		81	Α
		V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 100 °C		58	Α
I <sub>DM</sub>	peak drain current	pulse width t <sub>p</sub> limited by T <sub>jmax</sub>	Fig.17	163	А
Is	continuous diode current	V <sub>GS</sub> = -4 V; T <sub>mb</sub> = 25 °C		59	А
I <sub>SM</sub>	pulse diode current	$V_{GS}$ = -4 V; pulse width $t_p$ limited by $T_{jmax}$		163	А
E <sub>as</sub>	single pulse drain-to- source avalanche	$I_{AS}$ = 17.2 A; L = 1 mH; $V_{DD}$ = 100 V; $T_j$ = 25 °C		147	mJ
T <sub>stg</sub>	storage temperature			-55 to 175	°C
T <sub>j</sub>	junction temperature			-55 to 175	°C
$T_{sld(M)}$	peak soldering temperature			260	°C

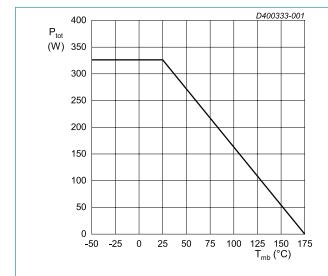


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

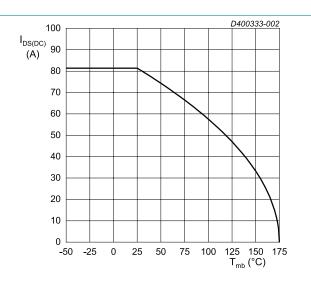


Fig. 2. Continuous Drain Current as a function of mounting base temperature

## 9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base			-	0.46	-	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air		-	40	-	K/W
M <sub>d</sub>	Mounting torque	M3 or 6 - 32 screw		-	-	0.6	Nm

Note: It is recommended that a metal washer is inserted between screw head and mounting tab.

Do not use self-tapping screws.

Device is ESD sensitive. Handling precautions are recommended.

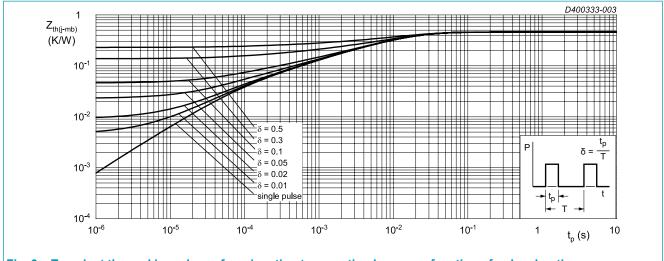


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_j = 25 °C$		650	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold	$I_D = 6 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$		1.9	2.6	3.5	V
	voltage	$I_D = 6 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C}$		-	1.9	-	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	0.1	50	μA
		V <sub>DS</sub> = 650 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C		-	5	-	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS} = 22 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$		-	5	100	nA
		$V_{GS} = -10 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	5	100	nA
R <sub>DS(on)</sub>	drain-source on-state	V <sub>GS</sub> = 15 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C		-	43	-	mΩ
	resistance	V <sub>GS</sub> = 18 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C		-	34.5	45	mΩ
		V <sub>GS</sub> = 18 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 175 °C		-	41	-	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C		-	1.15	-	Ω
g <sub>fs</sub>	transconductance	$V_{DS}$ = 20 V; $I_{D}$ = 25 A; $T_{j}$ = 25 °C		-	18	-	S
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	73	-	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C		-	30	-	nC
$Q_{GD}$	gate-drain charge			-	8.6	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 400 V; V <sub>GS</sub> = 0 V; f = 1 MHz;		-	1745	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C		-	149	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	11	-	pF
E <sub>oss</sub>	Coss stored energy			-	11.9	-	μJ
$t_{d(on)}$	turn-on delay time	$V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V}; R_{G(ext)} = 5.1$		-	13	-	ns
t <sub>r</sub>	rise time	$\Omega$ ; $I_D = 12.5 \text{ A}$ ; $L = 100 \mu\text{H}$ ; $T_J = 25 \text{ °C}$		-	17	-	ns
$t_{d(off)}$	turn-off delay time			-	31	-	ns
t <sub>f</sub>	fall time			-	13	-	ns
E <sub>on</sub>	turn-on energy (SIC Diode FWD)		Fig.19	-	72	-	μJ
E <sub>off</sub>	turn-off energy (SIC Diode FWD)		Fig.19	-	30	-	μJ
E <sub>on</sub>	turn-on energy (Body Diode FWD)		Fig.19	-	82	-	μJ
E <sub>off</sub>	turn-off energy (Body Diode FWD)		Fig.19	-	30	-	μJ
Source-d	rain diode						
V <sub>SD</sub>	source-drain voltage	$V_{GS} = 0 \text{ V}; I_{SD} = 25 \text{ A}; T_j = 25 \text{ °C}$		-	3.7	-	V
		V <sub>GS</sub> = -4 V; I <sub>SD</sub> = 25 A; T <sub>j</sub> = 25 °C		-	4.2	-	V
		$V_{GS} = -4 \text{ V}; I_{SD} = 25 \text{ A}; T_j = 175 ^{\circ}\text{C}$		-	3.7	-	V
t <sub>rr</sub>	reverse recovery time	$I_{SD} = 25 \text{ A}$ ; di/dt = 500 A/ $\mu$ s; $V_{DS} = 400 \text{ V}$ ;		-	27.7	-	ns
Q <sub>r</sub>	recovered charge	T <sub>j</sub> = 25 °C		-	77.4	-	nC
I <sub>rrm</sub>	reverse recovery current			-	5.6	-	Α

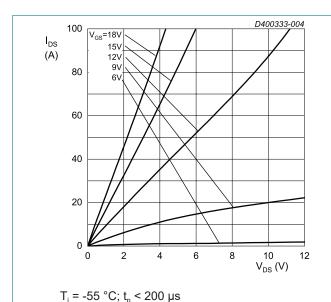
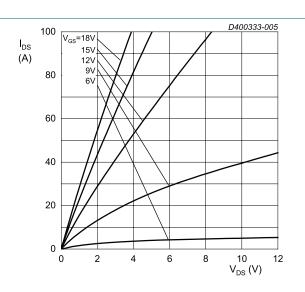
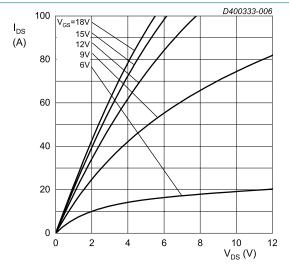


Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values

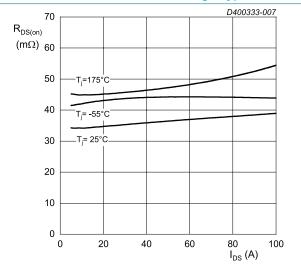


 $T_j = 25 \text{ °C}; t_p < 200 \text{ µs}$ 

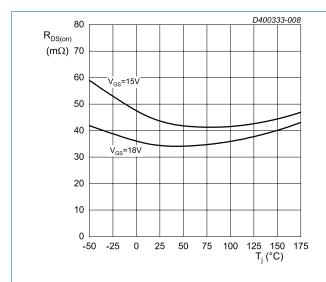
Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



T<sub>j</sub> = 175 °C; t<sub>p</sub> < 200 μs Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

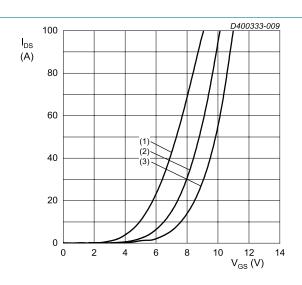


 $V_{GS}$  = 18 V;  $t_p$  < 200 µs Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



 $I_{DS}$  = 25 A;  $t_p$  < 200  $\mu s$ 

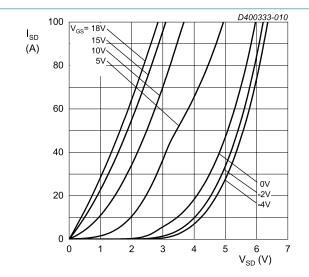
Fig. 8. Drain-source on-state resistance as a function of junction temperature



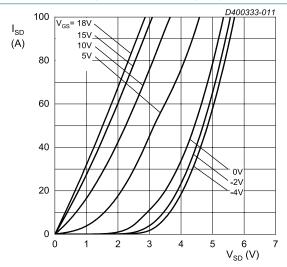
 $V_{DS} = 20 \text{ V; } t_p < 200 \text{ } \mu \text{s}$ (1)  $T_j = 175 \text{ }^{\circ}\text{C}$ (2)  $T_j = 25 \text{ }^{\circ}\text{C}$ 

(3)  $T_i = -55 \,^{\circ}C$ 

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values

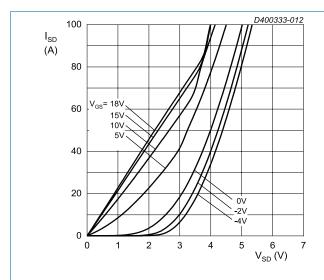


 $T_{j} = -55 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$ Fig. 10. Body diode forward characteristics; typical values

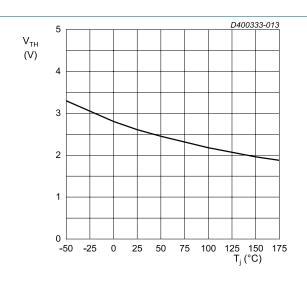


 $T_{j} = 25 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$ 

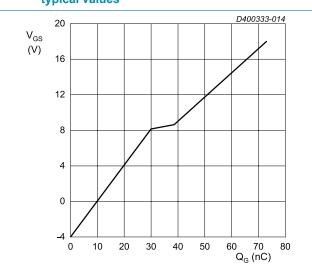
Fig. 11. Body diode forward characteristics; typical values



 $T_j$  = 175 °C;  $t_p$  < 200 µs Fig. 12. Body diode forward characteristics; typical values



V<sub>DS</sub> = V<sub>GS</sub>; I<sub>DS</sub> = 6 mA Fig. 13. Threshold voltage as a function of junction temperature



I<sub>DS</sub> = 25 A; I<sub>GS</sub> = 0.1 mA; V<sub>DS</sub> = 400 V; T<sub>j</sub> = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values

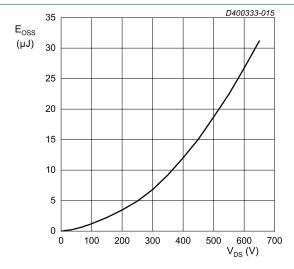
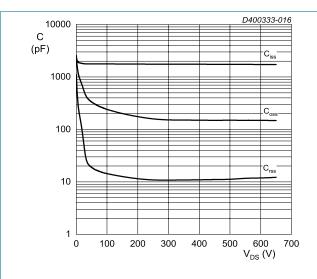
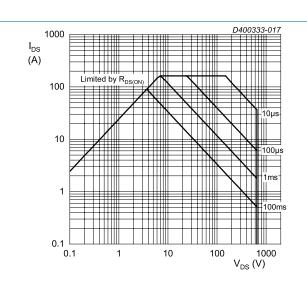


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



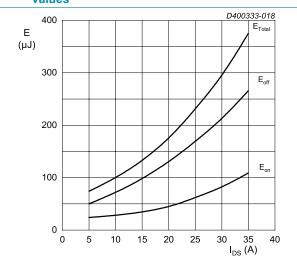
 $V_{DS}$  = 0 - 650 V T<sub>i</sub> = 25 °C;  $V_{AC}$  = 25 mV; f = 1 MHz

Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



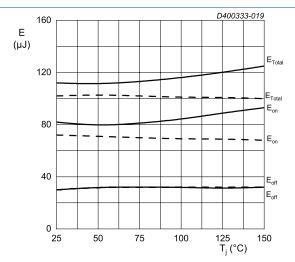
 $T_j = 25 \, ^{\circ}\text{C}; D = 0$ Parameter:  $t_p$ 

Fig. 17. Forward bias safe operating area



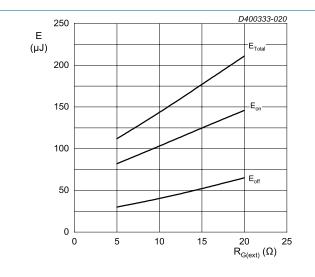
 $T_{j}$  = 25 °C;  $V_{DD}$  = 400 V;  $R_{G(ext)}$  = 5.1  $\Omega$ ;  $V_{GS}$  = -4 V/18 V; L = 100  $\mu$ H FWD = WNSC2M43065W

Fig. 18. Clamped Inductive Switching Energy as a function of drain current



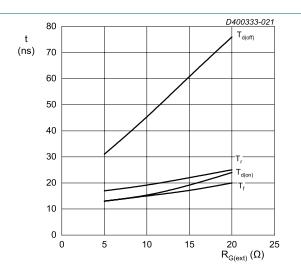
 $I_{DS}$  = 12.5 A;  $V_{DD}$  = 400 V;  $R_{G(ext)}$  = 5.1 Ω;  $V_{GS}$  = -4 V/18 V; L = 100 μH FWD = WNSC2M43065W FWD = WNSC6D30650W(- - -)

Fig. 19. Clamped Inductive Switching Energy as a function of junction temperature



 $T_{j}$  = 25 °C;  $V_{DD}$  = 400 V;  $I_{DS}$  = 12.5 A;  $V_{GS}$  = -4 V/18 V FWD = WNSC2M43065W; L = 100  $\mu H$ 

Fig. 20. Clamped Inductive Switching Energy as a function of external gate resistance



 $T_{j}$  = 25 °C;  $V_{DD}$  = 400 V;  $I_{DS}$  = 12.5 A;  $V_{GS}$  = -4 V/18 V FWD = WNSC2M43065W; L = 100  $\mu H$ 

Fig. 21. Switching time as a function of external gate resistance

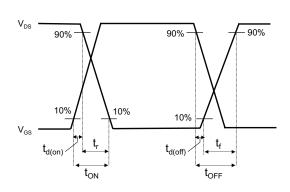
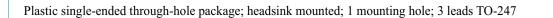
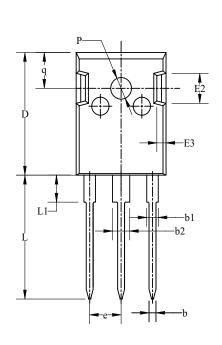


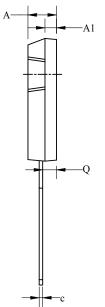
Fig. 22. Switching time definition

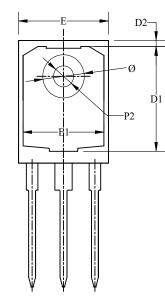
# 11. Package outline



TO247







Dim	All Dim	ensions in M	illimeters
Dilli	Min	Тур	Max
A	4.70	4.95	5.20
A1	1.90	2.00	2.10
ь	1.00	1.20	1.40
b1	1.80	2.00	2.20
b2	2.80	3.00	3.20
с	0.50	0.60	0.70
D	20.30	20.45	20.60
D1	17.28	17.48	17.68
D2	0.80	1.00	1.20
Е	15.45	15.60	15.75
E1	13.82	14.02	14.22
E2	4.80	5.00	5.20
E3	1.40	1.60	1.80
e		5.45 BSC	
L	20.40	20.65	20.90
L1	4.25	4.50	4.75
P2	3.40	3.50	3.60
P	3.50	3.60	3.70
Q	2.20	2.40	2.60
q	5.78	5.98	6.18
Ø	7.10	7.19	7.30

## 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.ween-semi.com">http://www.ween-semi.com</a>.

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