Product data sheet

1. General description

WeEn Gen-2 Silicon Carbide MOSFET in a TO247-4L plastic package, designed for high frequency, high efficiency systems.



2. Features and benefits

- · Kelvin source configuration
- Low specific on-resistance
- Optimized dynamic performance
- Robust gate design
- 0V turn-off V_{GS} for simple gate driving
- 100% UIS Tested
- Easy to parallel
- RoHS compliant

3. Applications

- PC/server/telecom power supplies
- UPS & Energy storage system
- Battery formation instrument
- PV MPPT and inverters
- EV Chargers
- Motor Drives

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit	
Absolute maximum rating								
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C			650		V	
I _D	drain current	V _{GS} = 18 V; T _{mb} = 25 °C			77		Α	
P _{tot}	total power dissipation	T _{mb} = 25 °C, T _j = 175 °C			288		W	
T _j	junction temperature				-55 to 17	5	°C	
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit	
Static cha	racteristics							
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = 15 \text{ V}; I_D = 25 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	43	-	mΩ	
		V_{GS} = 18 V; I_{D} = 25 A; T_{j} = 25 °C		-	34.5	45	mΩ	
Dynamic	characteristics							
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	73	-	nC	
Q_{GD}	gate-drain charge	T _j = 25 °C		-	8.6	-	nC	
Source-di	rain diode					,		
Q _r	recovered charge	I_{SD} = 25 A; di/dt = 500 A/µs; V_{DS} = 400 V; T_{j} = 25 °C		-	77.4	-	nC	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain		D
2	S	source		
3	SS	source sense		$G \longrightarrow A$
4	G	gate		SS
mb	D	mounting base; connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSC2M43065R	TO247-4L	WNSC2M43065R6Q	Tube	30	TO247N-4L	17-Dec-2021

7. Marking

Table 4. Marking codes

Type number	Marking codes
WNSC2M43065R	WNSC2M
	43065R

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		650	V
$V_{\rm GS,max}$	gate-source voltage	Absolute maximum values		-10 to 22	V
$V_{GS,op}$	gate-source voltage	Recommended operational values		-4 to 18	V
P _{tot}	total power dissipation	T _{mb} = 25 °C, T _j = 175 °C		288	W
I _D	drain current	V _{GS} = 18 V; T _{mb} = 25 °C		77	Α
		V _{GS} = 18 V; T _{mb} = 100 °C		54	Α
I _{DM}	peak drain current	pulse width t _p limited by T _{jmax}	Fig.17	153	А
Is	continuous diode current	V _{GS} = -4 V; T _{mb} = 25 °C		54	Α
I _{SM}	pulse diode current	V_{GS} = -4 V; pulse width t_p limited by T_{jmax}		153	А
E _{as}	single pulse drain-to- source avalanche	I_{AS} = 17.2 A; L = 1 mH; V_{DD} = 100 V; T_j = 25 °C		147	mJ
T _{stg}	storage temperature			-55 to 175	°C
T _j	junction temperature			-55 to 175	°C
$T_{sld(M)}$	peak soldering temperature			260	°C

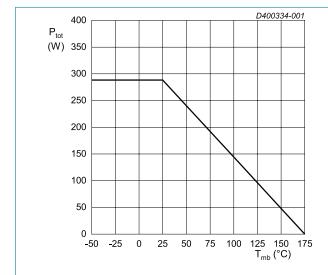


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

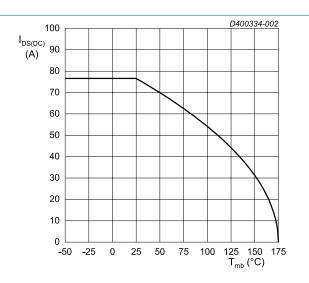


Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base			-	0.52	-	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air		-	40	-	K/W
M _d	Mounting torque	M3 or 6 - 32 screw		-	-	0.6	Nm

Note: It is recommended that a metal washer is inserted between screw head and mounting tab.

Do not use self-tapping screws.

Device is ESD sensitive. Handling precautions are recommended.

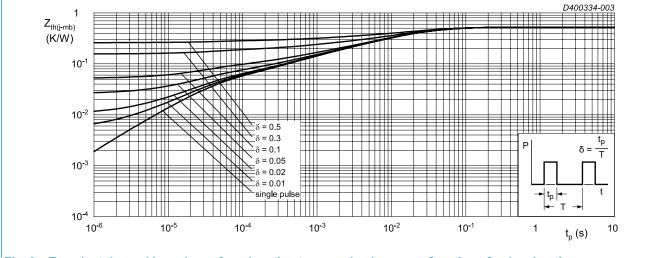


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_j = 25 °C$		650	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold	$I_D = 6 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$		1.9	2.6	3.5	V
	voltage	$I_D = 6 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C}$		-	1.9	-	V
I _{DSS}	drain leakage current	$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	0.1	50	μA
		$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$		-	5	-	μA
I_{GSS}	gate leakage current	$V_{GS} = 22 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	5	100	nA
		$V_{GS} = -10 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	5	100	nA
R _{DS(on)}	drain-source on-state	$V_{GS} = 15 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$		-	43	-	mΩ
	resistance	$V_{GS} = 18 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$		-	34.5	45	mΩ
		V _{GS} = 18 V; I _D = 25 A; T _j = 175 °C		-	41	-	mΩ
R _G	gate resistance	f = 1 MHz; T _j = 25 °C		-	1.15	-	Ω
g _{fs}	transconductance	$V_{DS} = 20 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$		-	18	-	S
Dynamic	characteristics						
$Q_{G(tot)}$	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	73	-	nC
Q _{GS}	gate-source charge	$T_j = 25 ^{\circ}\text{C}$		-	30	-	nC
Q_{GD}	gate-drain charge			-	8.6	-	nC
C _{iss}	input capacitance	$V_{DS} = 400 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$		-	1745	-	pF
C _{oss}	output capacitance	T _j = 25 °C		-	149	-	pF
C _{rss}	reverse transfer capacitance			-	11	-	pF
E _{oss}	Coss stored energy			-	11.9	-	μJ
t _{d(on)}	turn-on delay time	$V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V}; R_{G(ext)} = 5.1$		-	19	-	ns
t _r	rise time	$Ω$; $I_D = 12.5 A$; $L = 100 \mu H$; $T_j = 25 °C$		-	10	-	ns
$t_{\text{d(off)}}$	turn-off delay time			-	40	-	ns
t _f	fall time			-	12	-	ns
E _{on}	turn-on energy (SIC Diode FWD)		Fig.19	-	65	-	μJ
E _{off}	turn-off energy (SIC Diode FWD)		Fig.19	-	4	-	μJ
E _{on}	turn-on energy (Body Diode FWD)		Fig.19	-	78	-	μJ
E _{off}	turn-off energy (Body Diode FWD)		Fig.19	-	5	-	μJ
Source-d	rain diode						
V _{SD}	source-drain voltage	$V_{GS} = 0 \text{ V}; I_{SD} = 25 \text{ A}; T_j = 25 \text{ °C}$		-	3.7	-	V
		V _{GS} = -4 V; I _{SD} = 25 A; T _j = 25 °C		-	4.2	-	V
		$V_{GS} = -4 \text{ V}; I_{SD} = 25 \text{ A}; T_j = 175 \text{ °C}$		-	3.7	-	V
t _{rr}	reverse recovery time	$I_{SD} = 25 \text{ A}$; di/dt = 500 A/ μ s; $V_{DS} = 400 \text{ V}$;		-	27.7	-	ns
Q_r	recovered charge	T _j = 25 °C		-	77.4	-	nC
I _{rrm}	reverse recovery current			-	5.6	-	Α

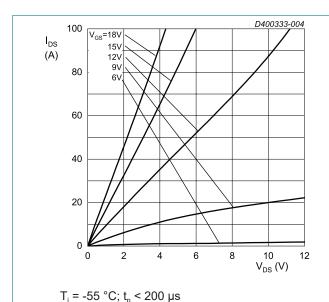


Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values

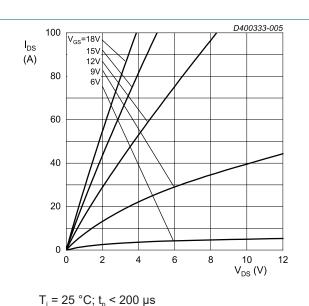
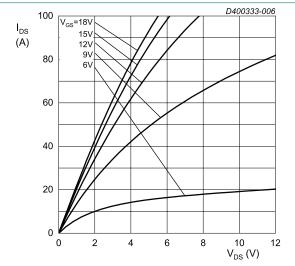
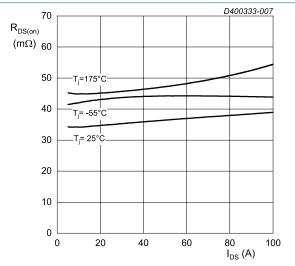


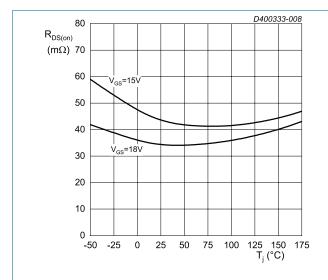
Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



 $T_{j}=175~^{\circ}C;~t_{p}<200~\mu s$ Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

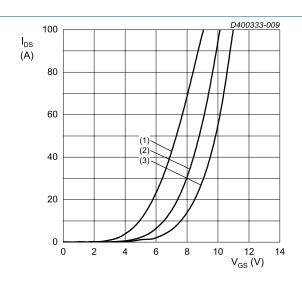


 V_{GS} = 18 V; t_p < 200 µs Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



 I_{DS} = 25 A; t_p < 200 μs

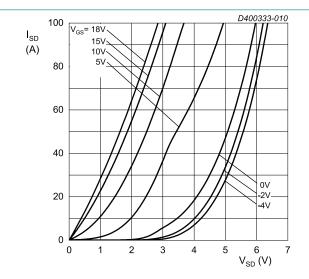
Fig. 8. Drain-source on-state resistance as a function of junction temperature



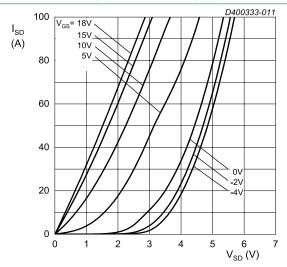
 $V_{DS} = 20 \text{ V; } t_p < 200 \text{ } \mu \text{s}$ (1) $T_j = 175 \text{ }^{\circ}\text{C}$ (2) $T_j = 25 \text{ }^{\circ}\text{C}$

(3) $T_i = -55 \,^{\circ}C$

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values

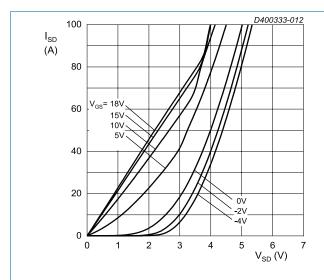


 $T_{j} = -55 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$ Fig. 10. Body diode forward characteristics; typical values

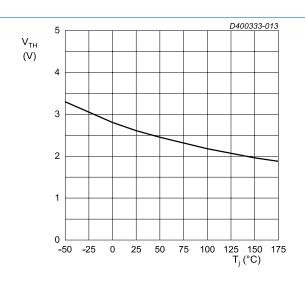


 $T_{j} = 25 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$

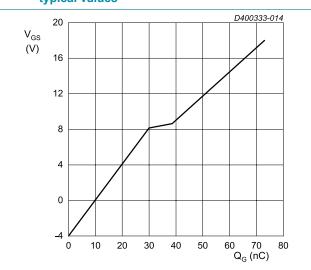
Fig. 11. Body diode forward characteristics; typical values



 T_j = 175 °C; t_p < 200 µs Fig. 12. Body diode forward characteristics; typical values



V_{DS} = V_{GS}; I_{DS} = 6 mA Fig. 13. Threshold voltage as a function of junction temperature



I_{DS} = 25 A; I_{GS} = 0.1 mA; V_{DS} = 400 V; T_j = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values

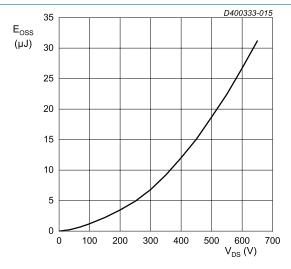
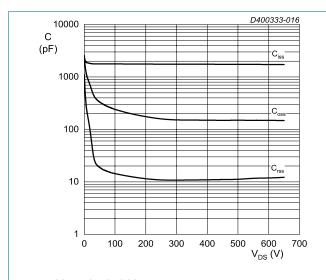
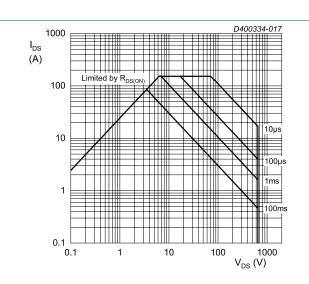


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



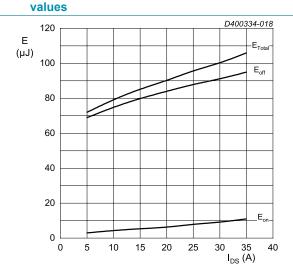
 V_{DS} = 0 - 650 V T_i = 25 °C; V_{AC} = 25 mV; f = 1 MHz

Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical



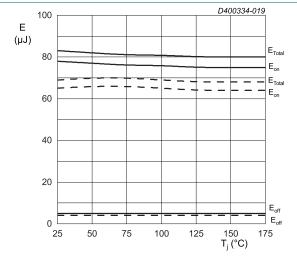
 $T_j = 25 \,^{\circ}\text{C}; D = 0$ Parameter: t_p

Fig. 17. Forward bias safe operating area



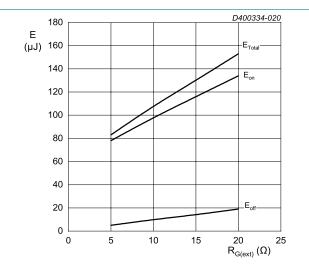
 $T_{\rm j}$ = 25 °C; $V_{\rm DD}$ = 400 V; $R_{\rm G(ext)}$ = 5.1 Ω; $V_{\rm GS}$ = -4 V/18 V; L = 100 μH FWD = WNSC2M43065R

Fig. 18. Clamped Inductive Switching Energy as a function of drain current



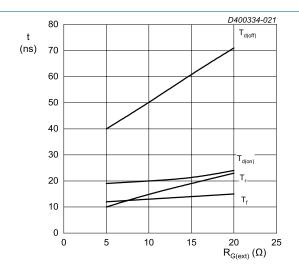
 I_{DS} = 12.5 A; V_{DD} = 400 V; $R_{G(ext)}$ = 5.1 Ω; V_{GS} = -4 V/18 V; L = 100 μH FWD = WNSC2M43065R FWD = WNSC6D20650W(- - -)

Fig. 19. Clamped Inductive Switching Energy as a function of junction temperature



 T_{j} = 25 °C; V_{DD} = 400 V; I_{DS} = 12.5 A; V_{GS} = -4 V/18 V FWD = WNSC2M43065R; L = 100 μH

Fig. 20. Clamped Inductive Switching Energy as a function of external gate resistance



 $T_{\rm j}$ = 25 °C; $V_{\rm DD}$ = 400 V; $I_{\rm DS}$ = 12.5 A; $V_{\rm GS}$ = -4 V/18 V FWD = WNSC2M43065R; L = 100 μH

Fig. 21. Switching time as a function of external gate resistance

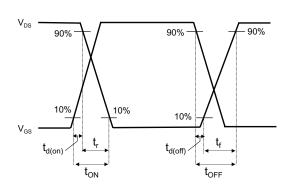
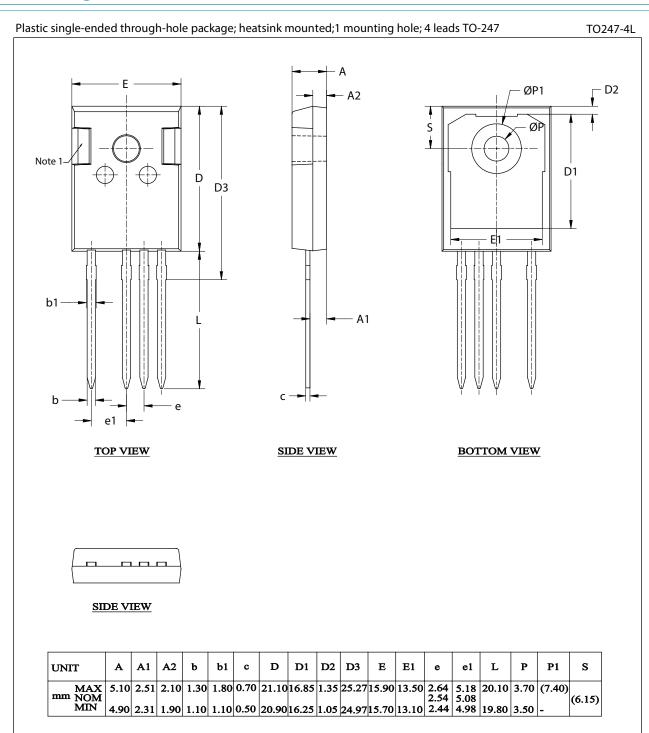


Fig. 22. Switching time definition

11. Package outline



Note:

- Metal exposed with Sn plating.
 All dimensions do not include mold flash & gate remain

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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