Product data sheet

1. General description

WSJM80R200 is a high voltage N-channel MOSFET in TO220 package, which utilizes the advanced super-junction technology to provide superior FOM $R_{\rm DS(on)}{}^{\star}Q_{\rm g}$ among silicon based MOSFETs. It is particularly suitable for applications require extreme high efficiency and power density.





2. Features and benefits

- Superior FOM R_{DS(on)} * Q_g
- Extremely low switching loss
- 100% avalanche tested

3. Applications

- Solar
- LED power
- · Flyback topologies for high efficiency power supplies

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit				
Absolute	Absolute maximum rating										
V _{DS}	drain-source voltage				800		V				
V_{GS}	gate-source voltage				±30		V				
I _D	continuous drain current	T _{mb} = 25 °C			22		Α				
P _{tot}	power dissipation	T _{mb} = 25 °C			272		W				
T _j	junction temperature			-	·55 to 15	0	°C				
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit				
Static cha	aracteristics		,								
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}, I_{D} = 10.5 \text{ A}$		-	180	200	mΩ				
Dynamic	Dynamic characteristics										
Q _{G(tot)}	total gate charge	I _D = 10.5 A; V _{DS} = 640 V; V _{GS} = 10 V		-	52	-	nC				
E _{oss}	coss stored erergy	V _{GS} = 0 V; V _{DS} = 0 to 640 V		-	11	-	μJ				

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D
2	D	drain	1 7 4	
3	S	source		$G \left(\stackrel{\longleftarrow}{\bowtie} \overline{A} \right)$
mb	D	mounting base; connected to drain		sym300 S

6. Ordering information

Table 3. Ordering information

Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WSJM80R200	TO220	WSJM80R200Q	Tube	50	SOT78	13-Jun-2008

7. Marking

Table 4. Marking codes

Type number	Marking codes
WSJM80R200	WSJM 80R200

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V _{DS}	drain-source voltage			800	V
V _{GS}	gate-source voltage			±30	V
I _D	continuous drain current	T _{mb} = 25 °C		22	Α
		T _{mb} = 100 °C		14	Α
I _{DM}	pulsed drain current	T _{mb} = 25 °C		88	Α
P _{tot}	power dissipation	T _{mb} = 25 °C		272	W
E _{AS}	single pulse drain-to- source avalanche	$I_{AS} = 3.5 \text{ A}; R_{GS} = 25 \Omega; V_{DD} = 50 \text{ V};$ $T_j = 25 \text{ °C}$		61	mJ
E _{AR}	repetitive avalanche energy	$I_{AS} = 3.5 \text{ A}; R_{GS} = 25 \Omega; V_{DD} = 50 \text{ V};$ $T_j = 25 \text{ °C}$		0.13	mJ
I _{AS}	avalanche current, single pulse			3.5	А
dv/dt	MOSFET dv/dt ruggedness			50	V/ns
dv/dt	reverse diode dv/dt			15	V/ns
dl _F /dt	maximum diode commutation speed			50	A/µs
T _{stg}	storage temperature			-55 to 150	°C
T _j	junction temperature			-55 to 150	°C

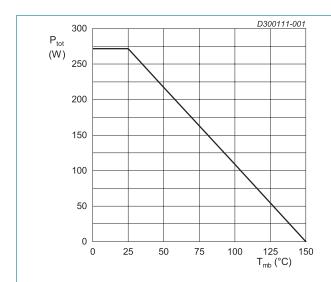


Fig. 1. Total power dissipation as a function of mounting base temperature

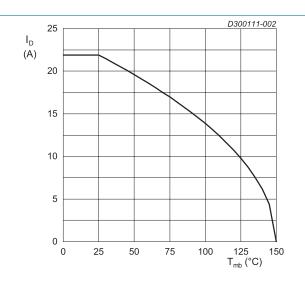


Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base			-	0.35	0.46	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air		-	60	-	K/W

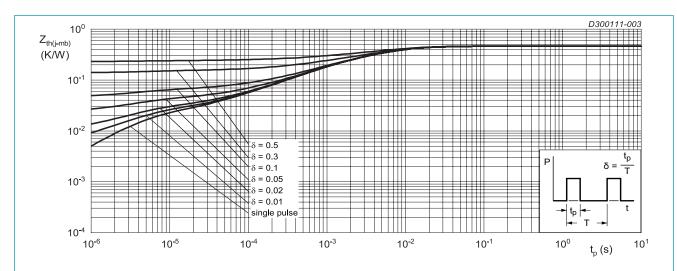


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration; maximum values

10. Characteristics

Table 7. Characteristics

T_i = 25 °C unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \ \mu A; \ V_{GS} = 0 \ V$		800	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	$I_D = 250 \ \mu A; \ V_{DS} = V_{GS}$		2.5	-	4.5	V
I _{DSS}	drain leakage current	$V_{DS} = 800 \text{ V}; V_{GS} = 0 \text{ V}$		-	-	1	μA
		$V_{DS} = 800 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 ^{\circ}\text{C}$		-	-	10	μA
I _{GSS}	gate leakage current	$V_{GS} = \pm 30 \text{ V}; V_{DS} = 0 \text{ V}$		-	-	±100	nA
$R_{\text{DS(on)}}$	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 10.5 \text{ A}$		-	180	200	mΩ
R_G	gate resistance	f = 1 MHz		-	1.8	-	Ω
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	$I_D = 10.5 \text{ A}; V_{DS} = 640 \text{ V}; V_{GS} = 10 \text{ V}$		-	52	-	nC
Q _{GS}	gate-source charge			-	12	-	nC
Q_{GD}	gate-drain charge			-	19	-	nC
C _{iss}	input capacitance	$V_{DS} = 640 \text{ V}; V_{GS} = 0 \text{ V}; f = 250 \text{ kHz}$		-	2413	-	pF
C _{oss}	output capacitance			-	46	-	pF
C _{rss}	reverse transfer capacitance			-	0.7	-	pF
$C_{o(er)}$	effective output capacitance, energy related	$V_{GS} = 0 \text{ V}; V_{DS} = 0 \text{ to 640 V}$		-	52	-	pF
$C_{o(tr)}$	effective output capacitance, time related			-	221	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 400 \text{ V}; V_{GS} = 10 \text{ V}; R_G = 3 \Omega;$		-	16	-	ns
t _r	rise time	I _D = 10.5 A		-	11	-	ns
$t_{d(off)}$	turn-off delay time			-	45	-	ns
t _f	fall time			-	8.5	-	ns
Source-d	rain diode						
V _{SD}	source-drain voltage	V _{GS} = 0 V; I _S = 10.5 A		-	0.8	1.1	V
I _s	body-diode continuous current	T _{mb} = 25 °C		-	-	22	А
t _{rr}	reverse recovery time	$V_R = 400 \text{ V}; I_F = 10.5 \text{ A}; dI_F/dt = 100 \text{ A}/$		-	377	-	ns
Q _{rr}	reverse recovered charge	μs		-	6.4	-	μC
I _{rrm}	reverse recovery current			_	31	-	Α

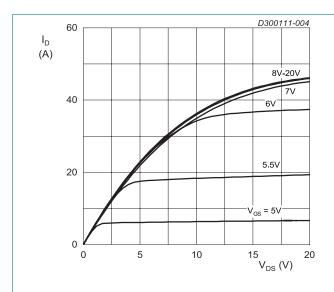
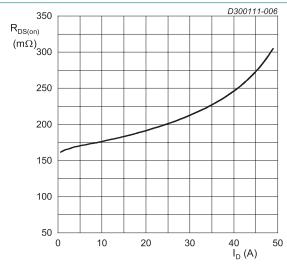
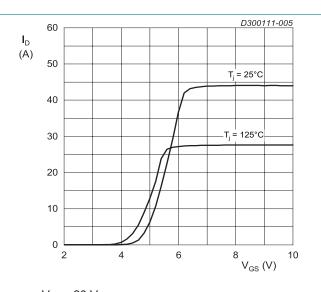


Fig. 4. Drain current as a function of drain-source voltage; typical values

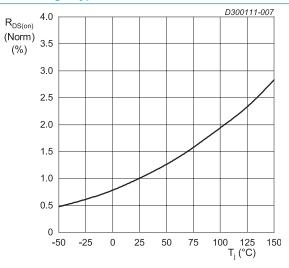


V_{GS} = 10 V
Fig. 6. Drain-source on-state resistance as a function of drain current; typical values



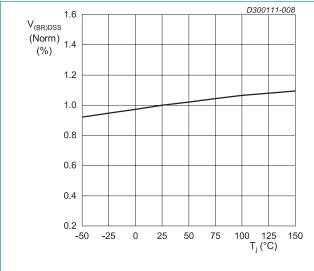
V_{DS} = 20 V

Fig. 5. Drain current as a function of gate-source voltage; typical values



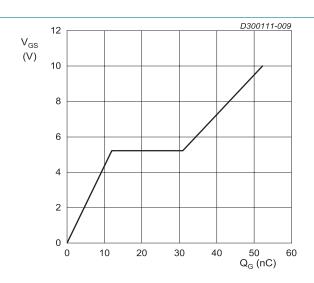
V_{GS} = 10 V; I_D = 10.5 A

Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature



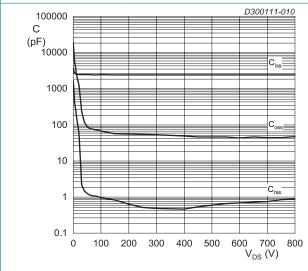
 $I_D = 250 \mu A$

Fig. 8. Normalized drain-source breakdown voltage as a function of junction temperature

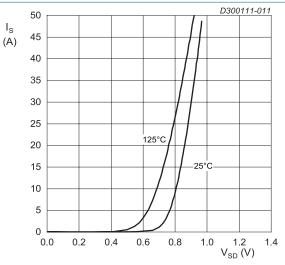


 $I_D = 10.5 A; V_{DS} = 640 V$

Fig. 9. Gate-source voltage as a function of gate charge; typical values



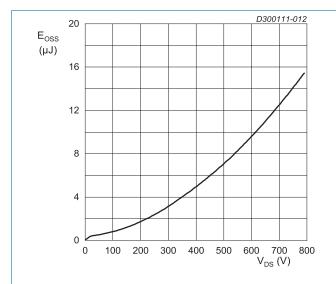
 $V_{GS} = 0 V; f = 250 kHz$ Fig 10. Capacitances as a function of drain-source voltage; typical values



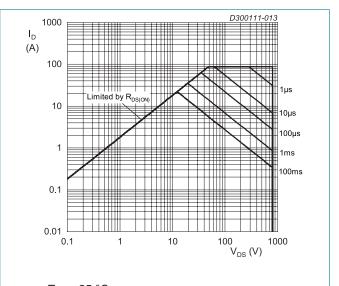
 $V_{GS} = 0 V$

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Super-Junction Power MOSFET





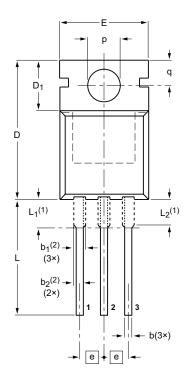


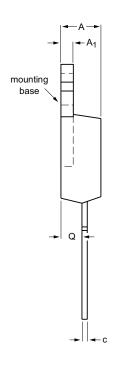
 T_{mb} = 25 °C Fig. 13. Safe operating area

11. Package outline



SOT78





DIMENSIONS (mm are the original dimensions)

UNIT	Α	A ₁	b	b ₁ ⁽²⁾	b ₂ ⁽²⁾	C	D	D ₁	E	е	L	L ₁ ⁽¹⁾	L ₂ ⁽¹⁾ max.	р	q	Q
mm	4.7 4.1	1.40 1.25	0.9 0.6	1.6 1.0	1.3 1.0	0.7 0.4	16.0 15.2	6.6 5.9	10.3 9.7	2.54	15.0 12.8	3.30 2.79	3.0	3.8 3.5	3.0 2.7	2.6 2.2

- Lead shoulder designs may vary.
 Dimension includes excess dambar.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT78		3-lead TO-220AB	SC-46		08-04-23 08-06-13

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 24 July 2024

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