Product data sheet

1. General description

WeEnPACK-B1 module with WeEn 1200V Gen2 SiC MOSFET and PressFit pin type. Intergrated with NTC temperature sensor.





2. Features and benefits

- Dual-boost topology
- Press-fit pin configuration
- Low ON resistance
- Low switching losses
- Reduced Q_g and C_{rss}
- Minimized circuit impedance
- Robust product design

3. Applications

Solar power MPPT

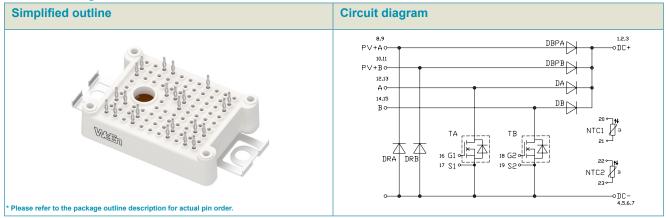
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit
Absolute	maximum rating		,				
V _{DS}	drain-source voltage	T _j = 25 °C			1200		V
I _D	drain current	V _{GS} = 18 V; T _h = 25 °C			45		Α
P _{tot}	total power dissipation	T _h = 25 °C			86		W
T _j	junction temperature				-40 to 15	50	°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics				•		
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = 15 \text{ V}; I_D = 40 \text{ A}; T_j = 25 \text{ °C}$		-	30	-	mΩ
		V _{GS} = 18 V; I _D = 40 A; T _j = 25 °C		-	24	40	mΩ
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	$I_D = 40 \text{ A}$; $V_{DS} = 800 \text{ V}$; $V_{GS} = -4 \text{ V}/18 \text{ V}$;		-	151	-	nC
Q_{GD}	$T_j = 25 \text{ °C}$			-	21	-	nC
Source-d	rain diode		1		1		
Q _r	recovered charge	I_{SD} = 40 A; di/dt = 500 A/µs; V_{DS} = 400 V; T_{j} = 25 °C		-	129	-	nC

5. Pinning information

Table 2. Pinning information



6. Ordering information

Table 3. Ordering information

Type number	_	Orderable part number	_		. •	Package
	Name		method	quantity	version	issue date
WMSC030B12B1P-C	WeEnPACK-B1	WMSC030B12B1P-C6T	Tray	24	WeEnPACK-	28-Jun-2024
					B1PBT-B	

7. Marking

Table 4. Marking codes

Type number	Marking codes
WMSC030B12B1P-C	WMSC030B12B1P-C

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
T _{stg}	storage temperature			-40 to 125	°C
$T_{j.op}$	operating junction temperature			-40 to 150	°C
$T_{j.max}$	maximum junction temperature	Intermittent condition with shortened lifetime		-40 to 175	°C
V_{ISOL}	RMS isolation voltage	T_j = 25 °C; all terminals shorted; f = 50 Hz; t = 1 s		3500	V
MOSFET					
V_{DS}	drain-source voltage	T _j = 25 °C		1200	V
$V_{GS,max}$	gate-source voltage	Absolute maximum values		-12 to 24	V
$V_{GS,op}$	gate-source voltage	Recommended operational values		-4 to 18	V
P _{tot}	total power dissipation	T _h = 25 °C		86	W
I _D	drain current	V _{GS} = 18 V; T _h = 25 °C		45	Α
		V _{GS} = 18 V; T _h = 100 °C		28	Α
I _{DM} I	peak drain current	pulse width t_p limited by T_{jmax}		90	Α
E _{as}	single pulse drain-to- source avalanche	I_{AS} = 20 A; L = 1 mH; V_{DD} = 100 V; $T_{j(init)}$ = 25 °C; per MOSFET		200	mJ
Body Dioc	de				
I _{SD}	DC body diode forward current	V _{GS} = -4 V; T _h = 25 °C		20	Α
SD,pulse	Pulse body diode current	verified by design, t_p limited by T_{jmax}		90	Α
Inverse-po	plarity Protection Diode				
V_{RRM}	repetitive peak reverse voltage			1600	V
I _{F(AV)}	average forward current	δ = 0.5; square-wave pulse; $T_h \le 97$ °C		45	А
I _{FSM}	non-repetitive peak forward current	t_p = 10 ms; $T_{j(init)}$ = 25 °C; sine-wave pulse		530	А
		t_p = 8.3 ms; $T_{j(init)}$ = 25 °C; sine-wave pulse		582	А
Boost Dio	de				
V_{RRM}	repetitive peak reverse voltage			1200	V
$I_{F(AV)}$	average forward current	δ = 0.5; square-wave pulse; $T_h \le 125 °C$		40	А
I _{FRM}	repetitive peak forward current	δ = 0.5; t_p = 25 μ s; square-wave pulse		80	А
I _{FSM}	non-repetitive peak forward current	t_p = 10 ms; $T_{j(init)}$ = 25 °C; sine-wave pulse		350	А
		t_p = 10 µs; $T_{j(init)}$ = 25 °C; sine-wave pulse		2100	А

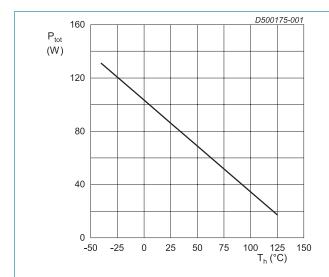


Fig. 1. Power dissipation as a function of heatsink temperature; maximum values

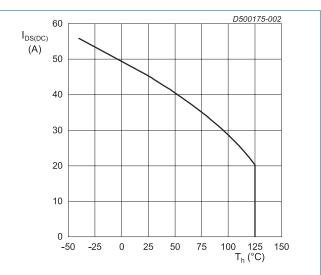


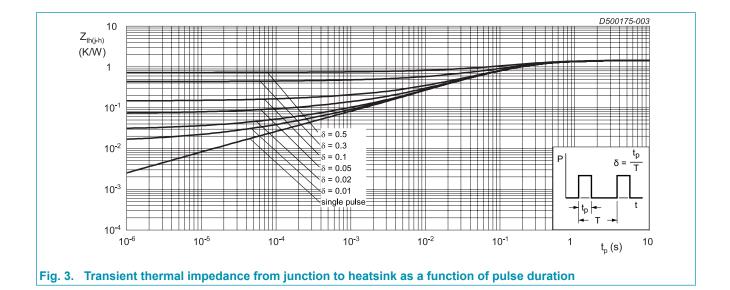
Fig. 2. Continuous Drain Current as a function of heatsink temperature

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
$R_{\text{th(j-c)}}$	thermal resistance from junction to case	per MOSFET		-	0.6	-	K/W
$R_{\text{th(j-h)}}$	thermal resistance from junction to heatsink	per MOSFET, $\lambda_{grease} = 1 \text{ W/(m·K)}$ thick _{grease} = 50 um		-	1.45	-	K/W
Internal Is	solation	basic insulation (class 1, IEC 61140)			Al_2O_3		
d _{Creep}	Creepage distance	terminal to heatsink		-	11.5	-	mm
		terminal to terminal		-	6.3	-	mm
d _{Clear}	Clearance	terminal to heatsink		-	10	-	mm
		terminal to terminal		-	5	-	mm
CTI	Comperative tracking index				>200		
F	Mounting force per clamp			20	-	50	N
G	Approximate Weight			-	20	-	g

Note: Module is ESD sensitive. Handling precautions are recommended.



10. Characteristics

Table 7. Characteristics

MOSFET					1		
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
	aracteristics				1	1	
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_j = 25 °C$		1200	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	$I_D = 12 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 25 \text{ °C}$		1.9	2.5	3.5	V
		I _D = 12 mA; V _{DS} = 10 V; T _j = 175 °C		-	1.9	-	V
I _{DSS}	drain leakage current	V _{DS} = 1200 V; V _{GS} = 0 V; T _j = 25 °C		-	0.2	100	μA
I _{GSS}	gate leakage current	V _{GS} = 24 V; V _{DS} = 0 V; T _j = 25 °C		-	10	100	nA
	(absolute value)	V _{GS} = -12 V; V _{DS} = 0 V; T _j = 25 °C		-	10	100	nA
R _{DS(on)}	drain-source on-state	V _{GS} = 15 V; I _D = 40 A; T _j = 25 °C		-	30	-	mΩ
	resistance	V _{GS} = 18 V; I _D = 40 A; T _j = 25 °C		-	24	40	mΩ
		V _{GS} = 18 V; I _D = 40 A; T _j = 125 °C		-	37	-	mΩ
		V _{GS} = 18 V; I _D = 40 A; T _j = 150 °C		-	42	-	mΩ
		V _{GS} = 18 V; I _D = 40 A; T _j = 175 °C		-	48	-	mΩ
R_G	gate resistance, each side	f = 1 MHz; T _j = 25 °C, per MOSFET		-	8.0	-	Ω
g _{fs}	transconductance	V _{DS} = 20 V; I _D = 40 A; T _j = 25 °C		-	27	-	S
Dynamic	characteristics			,			
Q _{G(tot)}	total gate charge	$I_D = 40 \text{ A}$; $V_{DS} = 800 \text{ V}$; $V_{GS} = -4 \text{ V}/18 \text{ V}$;		-	151	-	nC
Q _{GS}	gate-source charge	T _j = 25 °C		-	63	-	nC
Q_{GD}	gate-drain charge			-	21	-	nC
C _{iss}	input capacitance	V _{DS} = 1000 V; V _{GS} = 0 V; f = 1 MHz;		-	3305	-	pF
C _{oss}	output capacitance	T _j = 25 °C		-	139	-	pF
C _{rss}	reverse transfer capacitance			-	12	-	pF
E _{oss}	Coss stored energy			-	70	-	μJ
$t_{d(on)}$	turn-on delay time	V _{DS} = 800 V; V _{GS} = -4 V/18 V;		-	32	-	ns
t _r	rise time	$R_{G(ext)}$ = 5.1 Ω; I_D = 40 A; L = 100 μH; T_i = 25 °C		-	30	-	ns
$t_{d(off)}$	turn-off delay time	,		-	52	-	ns
t _f	fall time			-	11	-	ns
E _{on}	turn-on energy			-	597	-	μJ
E _{off}	turn-off energy	1		-	110	-	μJ

Body dic	ode						
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics						
V_{SD}	source-drain voltage	$V_{GS} = -4 \text{ V}; I_{SD} = 40 \text{ A}; T_j = 25 \text{ °C}$		-	5.5	-	V
		$V_{GS} = -4 \text{ V}; I_{SD} = 40 \text{ A}; T_j = 150 \text{ °C}$		-	5.0	-	V
Dynamic	characteristics						
I _{rrm}	reverse recovery current	I_{SD} = 40 A; di/dt = 500 A/µs; V_{DS} = 400 V;		-	6.9	-	А
t _{rr}	reverse recovery time	T _j = 25 °C		-	33.4	-	ns
Q _r	recovered charge			-	129	-	nC
Inverse-p	olarity Protection Diode						
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
V _F	forward voltage	I _F = 45 A; T _i = 25 °C		-	1.20	1.40	V
		I _F = 45 A; T _j = 150 °C		-	1.10	1.30	V
I _R	reverse current	V _R = 1600 V; T _j = 25 °C		-	-	50	μA
		V _R = 1600 V; T _j = 150 °C		-	-	1.5	mA
V_R	reverse voltage	DC		-	1600	-	V
Boost Did	ode						'
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
V _F	forward voltage	I _F = 40 A; T _j = 25 °C		-	1.42	1.60	V
		I _F = 40 A; T _j = 150 °C		-	1.90	2.30	V
I _R	reverse current	V _R = 1200 V; T _j = 25 °C		-	1	200	μA
V_R	reverse voltage	DC		-	1200	-	V
Q_r	recovered charge	$I_F = 40 \text{ A}$; $V_R = 400 \text{ V}$; $dI_F/dt = 500 \text{ A}/\mu\text{s}$; $T_j = 25 ^{\circ}\text{C}$		-	99	-	nC
NTC ther	mistor						
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R ₂₅	Rated resistance	$T_{NTC} = 25 ^{\circ}C$		-	5000	-	Ω
R ₁₀₀		T _{NTC} = 100 °C		493±5%		Ω	
B _{25/50}	B-value	$R_2 = R_{25} \exp[B_{25/50}(1/T_2 - 1/(298.15K))]$			3380		K
	Maximum operating temperature			-	200	-	°C
	Dissipation costant			-	2	-	mW/k
	Thermal time constant			-	≤10	-	s

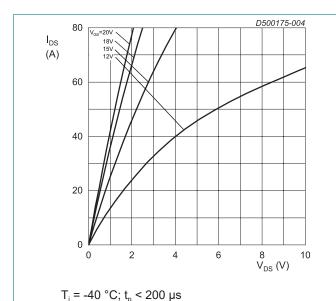
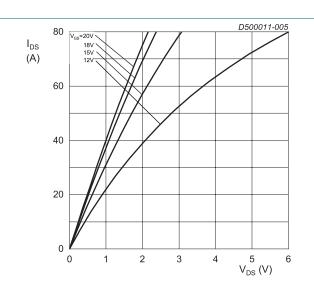
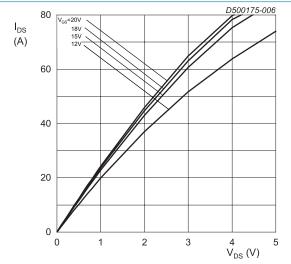


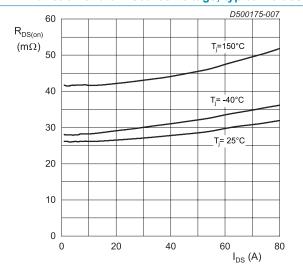
Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values



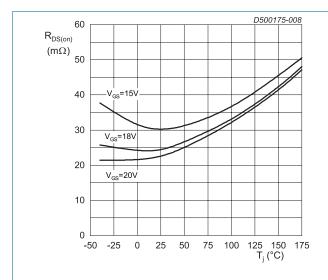
T_j = 25 °C; t_p < 200 μs Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



T_j = 150 °C; t_p < 200 μs Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

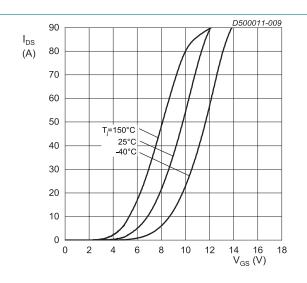


 V_{GS} = 18 V; t_p < 200 µs Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



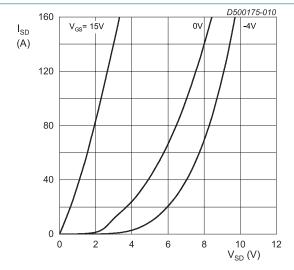
 I_{DS} = 40 A; t_p < 200 µs

Fig. 8. Drain-source on-state resistance as a function of junction temperature



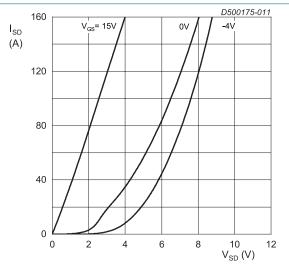
 $V_{DS} = 20 \text{ V}; t_p < 200 \text{ }\mu\text{s}$

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values



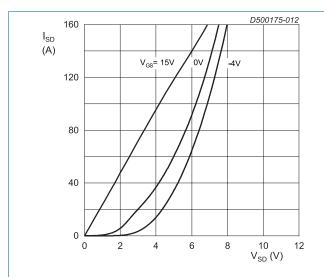
 $T_j = -40 \, ^{\circ}\text{C}; \, t_p < 200 \, \mu\text{s}$

Fig. 10. Body diode forward characteristics; typical values



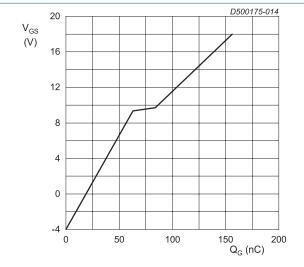
 $T_j = 25 \, ^{\circ}C; t_p < 200 \, \mu s$

Fig. 11. Body diode forward characteristics; typical values

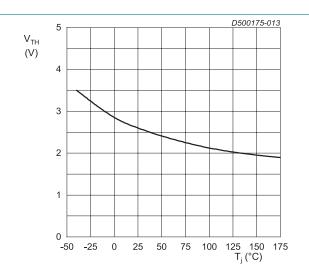


 T_j = 150 °C; t_p < 200 µs

Fig. 12. Body diode forward characteristics; typical values



 I_{DS} = 40 A; I_{GS} = 0.1 mA; V_{DS} = 800 V; T_j = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values



V_{DS} = 10 V; I_{DS} = 12 mA Fig. 13. Threshold voltage as a function of junction temperature

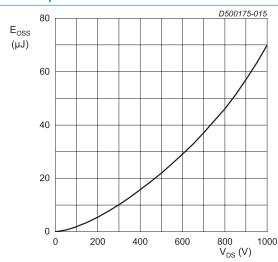
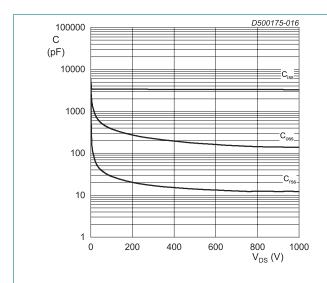
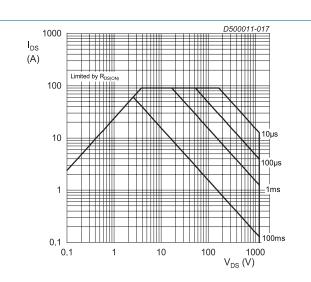


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



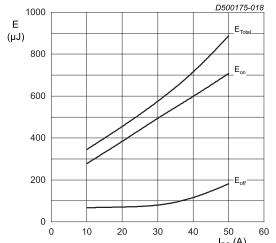
 $V_{DS} = 0 - 1000 V$ $T_i = 25 \, ^{\circ}\text{C}; \, V_{AC} = 25 \, \text{mV}; \, f = 1 \, \text{MHz}$

Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



 $T_i = 25 \,^{\circ}C; D = 0$ Parameter: t_p

Fig. 17. Forward bias safe operating area



 T_{j} = 25 °C; V_{DD} = 800 V; $R_{G(ext)}$ = 5.1 $\Omega;$ $R_{G(on)}$ = 5.1 $\Omega;$ V_{GS} = -4 V/18 V V; L = 100 μH Fig. 18. Clamped Inductive Switching Energy as a

function of drain current

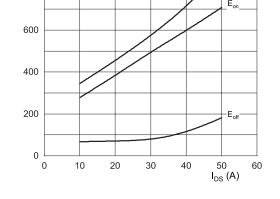
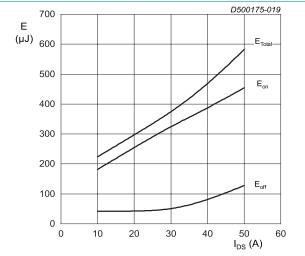
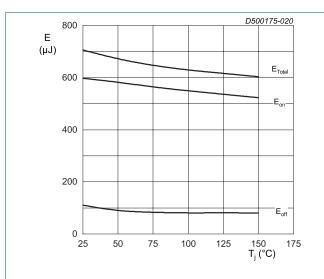


Fig. 19. Clamped Inductive Switching Energy as a function of drain current

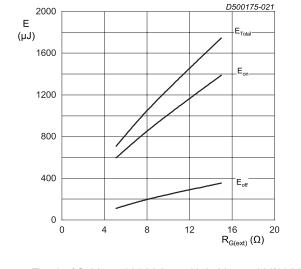


 T_{j} = 25 °C; V_{DD} = 600 V; $R_{G(off)}$ = 5.1 $\Omega;$ $R_{G(on)}$ = 5.1 $\Omega;$ V_{GS} = -4 V/18 V; L = 100 μH



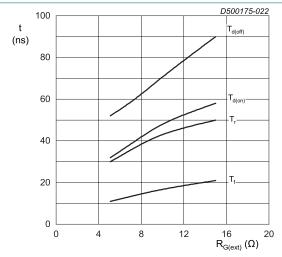
 I_{DS} = 40 A; V_{DD} = 800 V; $R_{G(off)}$ = 5.1 $\Omega;$ $R_{G(on)}$ = 5.1 $\Omega;$ V_{GS} = -4 V/18 V; L = 100 μH

Fig. 20. Clamped Inductive Switching Energy as a function of junction temperature



 T_{j} = 25 °C; V_{DD} = 800 V; I_{DS} = 40 A; V_{GS} = -4 V/18 V; L = 100 μH

Fig. 21. Clamped Inductive Switching Energy as a function of external gate resistance



 $T_{\rm j}$ = 25 °C; $V_{\rm DD}$ = 800 V; $I_{\rm DS}$ = 40 A; $V_{\rm GS}$ = -4 V/18 V; L = 100 μH

Fig. 22. Switching time as a function of external gate resistance

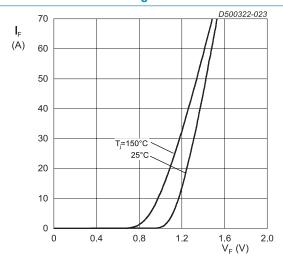
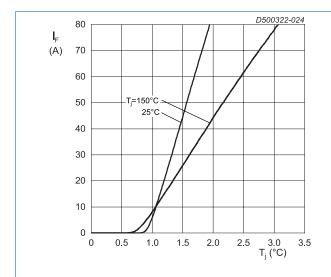


Fig. 23. By-pass and inverse-polarity protection diode forward characteristic; typical values



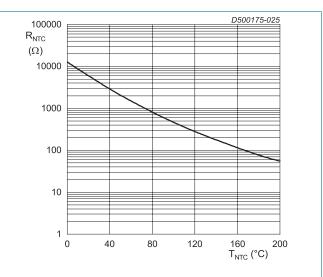
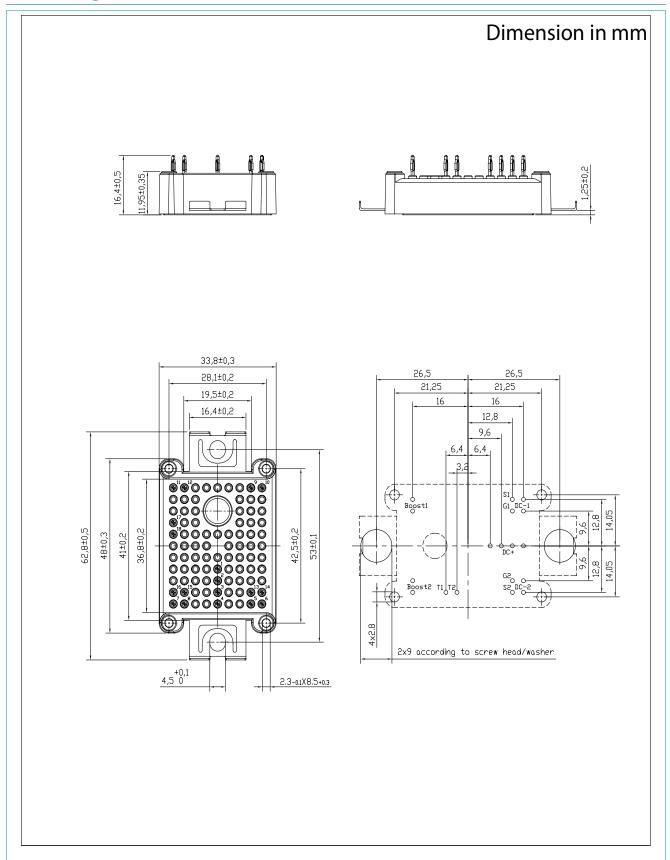


Fig. 24. Boost diode forward characteristic; typical values

Fig. 25. NTC thermistor resistance as a function of NTC temperature

11. Package outline



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.ween-semi.com.

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Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

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WMSC030B12B1P-C

N-Channel Silicon Carbide MOSFET Module

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Date of release: 09 December 2024

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