Product data sheet

1. General description

WeEnPACK-B2 module with WeEn 1200V Gen2 SiC MOSFET and Press-fit pin type. Integrated with NTC temperature sensor. Featured with extremely low V_{SD} with special internal configuration.



2. Features and benefits

- · Half bridge topology
- Press-fit pin configuration
- Low R_{DSon}-T_j coefficient
- Low Switching Losses
- Low Q_a and C_{rss}
- Mimimized circuit impedance
- · Improved chip synchronization performance

3. Applications

- Power inverters
- AC-DC converters
- · DC-DC converters
- · Active power factor correctors
- Motor drives

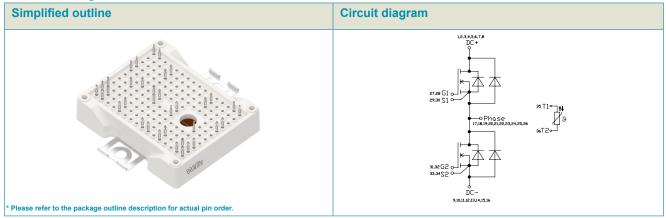
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit
Absolute	maximum rating		,				
V _{DS}	drain-source voltage	T _j = 25 °C			1200		V
I _D	drain current	V _{GS} = 18 V; T _h = 25 °C			233		Α
P _{tot}	total power dissipation	T _h = 25 °C			272		W
$T_{j.op}$	operating junction temperature				-40 to 15	0	°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics						
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = 15 \text{ V}; I_D = 250 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	4.0	-	mΩ
		V _{GS} = 18 V; I _D = 250 A; T _j = 25 °C		-	3.2	6	mΩ
Dynamic	characteristics				·		
Q _{G(tot)}	total gate charge	$I_D = 250 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = 0 \text{ V}/18 \text{ V};$		-	945	-	nC
Q_{GD}	gate-drain charge	T _j = 25 °C		-	205	-	nC
Source-d	rain diode		ı			1	
Q _r	recovered charge	I_{SD} = 250 A; V_{GS} = -4 V/18 V; V_{R} = 600 V; di/dt =3730 A/ μ s; R_{g} = 3 Ω		-	1685	-	nC

5. Pinning information

Table 2. Pinning information



6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	•	Package issue date
WMSC004H12B2P-D	WeEnPACK-B2	WMSC004H12B2P-D6T	Tray	14	WeEnPACK- B2PHB-D	19-Feb-2025

7. Marking

Table 4. Marking codes

Type number	Marking codes
WMSC004H12B2P-D	WMSC004H12B2P-D

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
T _{stg}	storage temperature			-40 to 125	°C
$T_{j.op}$	operating junction temperature			-40 to 150	°C
$T_{j.max}$	maximum junction temperature	Intermittent condition with shortened lifetime		-40 to 175	°C
V _{ISOL}	RMS isolation voltage	T _j = 25 °C; all terminals shorted; f = 50 Hz; t = 1 s		3500	V
MOSFET					
V_{DS}	drain-source voltage	T _j = 25 °C		1200	V
$V_{\rm GS,max}$	gate-source voltage	Absolute maximum values		-12 to 24	V
$V_{GS,op}$	gate-source voltage	Recommended operational values		-4 to 18	V
P _{tot}	total power dissipation	T _h = 25 °C		272	W
I _D	drain current	V _{GS} = 18 V; T _h = 25 °C		223	Α
		V _{GS} = 18 V; T _h = 100 °C		147	А
I_{DM}	peak drain current	pulsed; tp \leq 10 us; T _h = 25 °C		450	Α
E _{as}	single pulse drain-to- source avalanche	$I_{AS} = 30 \text{ A}; L = 1 \text{ mH}; V_{DD} = 100 \text{ V};$ $T_{j(init)} = 25 \text{ °C}; each die$		450	mJ
Body Diod	е				
I _{SD}	DC body diode forward current	$T_h = 25 ^{\circ}C; V_{GS} = -4 V$		100	Α
I _{SD,pulse}	Pulse body diode current	verified by design, tp limited by T_{jmax}		450	А

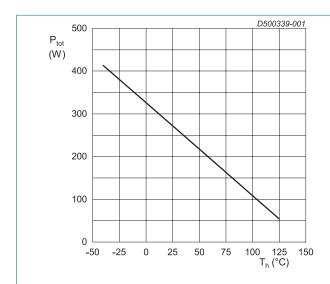


Fig. 1. Power dissipation as a function of heatsink temperature; maximum values

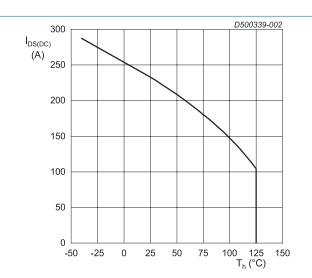


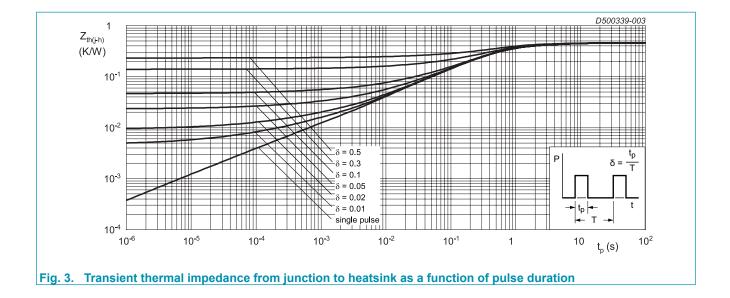
Fig. 2. Continuous Drain Current as a function of heatsink temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-c)}	thermal resistance from junction to case	per MOSFET		-	0.1	-	K/W
R _{th(j-h)}	thermal resistance from junction to heatsink	per MOSFET, $\lambda_{grease} = 3 \text{ W/(m·K)}$, thick _{grease} = 50 um		-	0.46	-	K/W
Internal Is	solation	basic insulation (class 1, IEC 61140)			Al_2O_3		
d _{Creep}	Creepage distance	terminal to heatsink		-	11.5	-	mm
		terminal to terminal		-	6.3	-	mm
d _{Clear}	Clearance	terminal to heatsink		-	10	-	mm
		terminal to terminal		-	5	-	mm
СТІ	Comperative tracking index				>200		
F	Mounting force per clamp			40	-	80	N
G	Approximate Weight			-	36	-	g

Note: Module is ESD sensitive. Handling precautions are recommended.

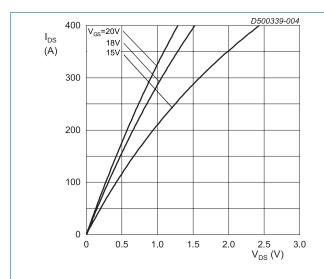


10. Characteristics

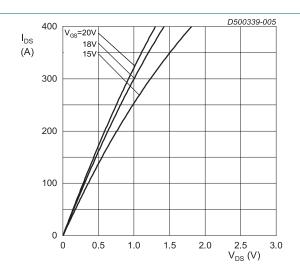
Table 7. Characteristics

MOSFET							
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 500 \mu A; V_{GS} = 0 V; T_j = 25 °C$		1200	-	-	V
$V_{GS(th)}$	gate-source threshold	$I_D = 100 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 25 \text{ °C}$		1.9	2.5	3.5	V
	voltage	I _D = 100 mA; V _{DS} = 10 V; T _j = 175 °C		-	1.9	-	V
I _{DSS}	drain leakage current	V _{DS} = 1200 V; V _{GS} = 0 V; T _j = 25 °C		-	1	500	μA
I _{GSS}	gate leakage current	V _{GS} = 24 V; V _{DS} = 0 V; T _j = 25 °C		-	50	500	nA
	(absolute value)	V _{GS} = -12 V; V _{DS} = 0 V; T _j = 25 °C		-	50	500	nA
R _{DS(on)}	drain-source on-state	V _{GS} = 15 V; I _D = 250 A; T _j = 25 °C		-	4.0	-	mΩ
	resistance	V _{GS} = 18 V; I _D = 250 A; T _j = 25 °C		-	3.2	6	mΩ
		V _{GS} = 18 V; I _D = 250 A; T _j = 125 °C		-	4.7	-	mΩ
		V _{GS} = 18 V; I _D = 250 A; T _j = 150 °C		-	5.2	-	mΩ
		V _{GS} = 18 V; I _D = 250 A; T _j = 175 °C		-	5.5	-	mΩ
R_{G}	gate resistance	$f = 1 \text{ MHz}$; $T_j = 25 \text{ °C}$; each die with 4.7Ω R_{Grext} in series		-	1.08	-	Ω
g _{fs}	transconductance	V _{DS} = 20 V; I _D = 250 A; T _j = 25 °C		-	84	-	S
Dynamic	characteristics						
Q _{G(tot)}	total gate charge $I_D = 250 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = 0 \text{ V}/18 \text{ V};$			-	945	-	nC
Q _{GS}	gate-source charge	T _j = 25 °C		-	240	-	nC
Q_{GD}	gate-drain charge			-	205	-	nC
C _{iss}	input capacitance	V _{DS} = 1000 V; V _{GS} = 0 V; f = 100 KHz;		-	23	-	nF
C _{oss}	output capacitance	T _j = 25 °C		-	1181	-	pF
C _{rss}	reverse transfer capacitance			-	71	-	pF
E _{oss}	Coss stored energy			-	590	-	μJ
$t_{d(on)}$	turn-on delay time	V _{DS} = 800 V; V _{GS} = -4 V/18 V;		-	76	-	ns
t _r	rise time	$R_{G(ext)}$ = 5.1 Ω; I_D = 250 A; L = 100 μH; T_i = 25 °C		-	62	-	ns
$t_{d(off)}$	turn-off delay time	-, -5 5		-	246	-	ns
t _f	fall time			-	54	-	ns
E _{on}	turn-on energy			-	13510	-	μJ
E _{off}	turn-off energy			-	8310	-	μJ

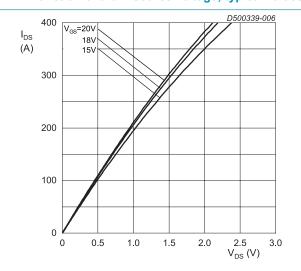
Body dic	ode						
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics		,				
V_{SD}	source-drain voltage	$V_{GS} = -4 \text{ V}; I_{SD} = 250 \text{ A}; T_j = 25 \text{ °C}$		-	3.2	-	V
		$V_{GS} = -4 \text{ V}; I_{SD} = 250 \text{ A}; T_j = 150 \text{ °C}$		-	3.9	-	V
Dynamic	characteristics						
I _{rrm}	reverse recovery current	$I_{SD} = 250 \text{ A}; V_{GS} = -4 \text{ V}/18 \text{ V}; V_{R} = 600 \text{ V};$		-	89	-	Α
t _{rr}	reverse recovery time	di/dt = 3730 A/ μ s; R _g = 3 Ω; T _i = 25 °C		-	32	-	ns
Q _r	recovered charge	J		-	1685	-	nC
E _{rec}	reverse recovery energy			-	304	-	μJ
NTC ther	mistor						
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R ₂₅	Rated resistance	T _{NTC} = 25 °C		-	5000	-	Ω
R ₁₀₀		T _{NTC} = 100 °C		493±5%)	Ω
B _{25/50}	B-value	$R_2 = R_{25} \exp[B_{25/50}(1/T_2 - 1/(298.15K))]$		3380		K	
	Maximum operating temperature			-	200	-	°C
	Dissipation costant			-	2	-	mW/K
	Thermal time constant			-	≤10	-	S



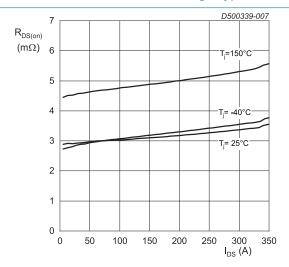
T_j = -40 °C; t_p < 200 μs Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values



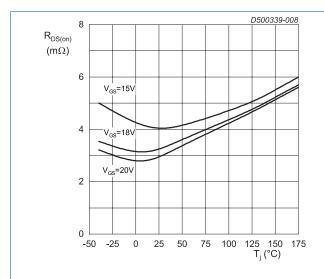
 $T_j = 25 \, ^{\circ}\text{C}; \, t_p < 200 \, \mu\text{s}$ Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



 $T_j = 150~^{\circ}\text{C}; \ t_p < 200~\mu\text{s}$ Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

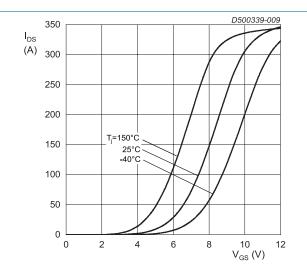


 V_{GS} = 18 V; t_p < 200 µs Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



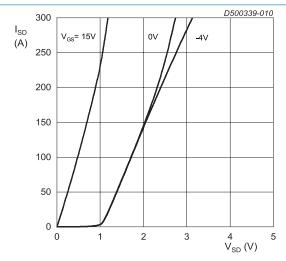
 I_{DS} = 250 A; t_p < 200 μ s

Fig. 8. Drain-source on-state resistance as a function of junction temperature



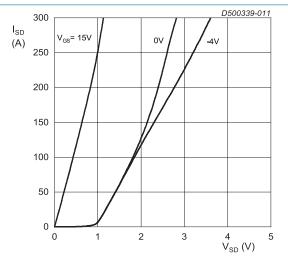
 V_{DS} = 20 V; t_p < 200 μ s

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values



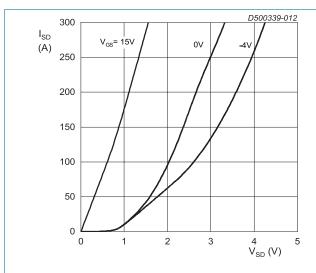
 T_j = -40 °C; t_p < 200 µs

Fig. 10. Body diode forward characteristics; typical values

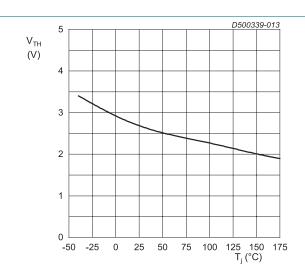


 $T_i = 25 \, ^{\circ}C; t_p < 200 \, \mu s$

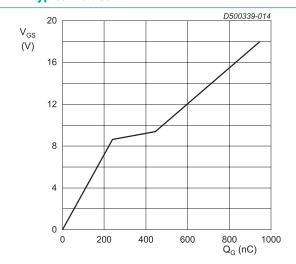
Fig. 11. Body diode forward characteristics; typical values



T_j = 150 °C; t_p < 200 μs Fig. 12. Body diode forward characteristics; typical values



V_{DS} = 10 V; I_{DS} = 100 mA Fig. 13. Threshold voltage as a function of junction temperature



 I_{DS} = 250 A; I_{GS} = 0.1 mA; V_{DS} = 800 V; T_j = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values

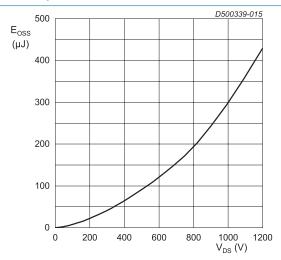
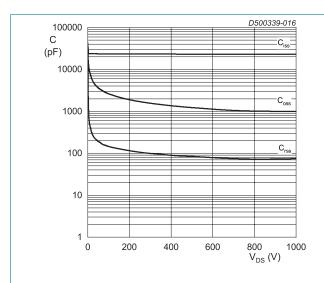
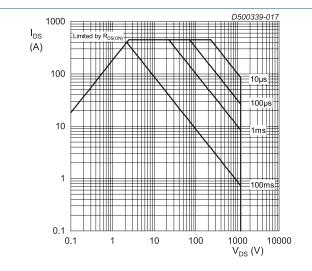


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



 $V_{DS} = 0 - 1000 V$ $T_i = 25 \text{ °C}; V_{AC} = 25 \text{ mV}; f = 100 \text{ KHz}$

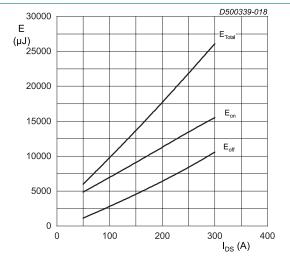


 $T_i = 25 \,^{\circ}C; D = 0$ Parameter: t_p

Fig. 17. Forward bias safe operating area

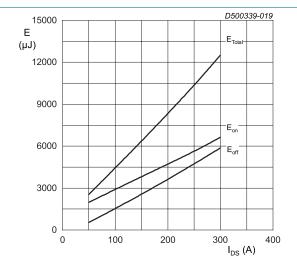
Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical





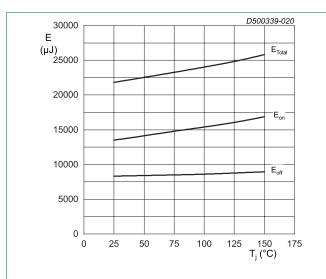
 T_{j} = 25 °C; V_{DD} = 800 V; $R_{G(off)}$ = 5.1 $\Omega;$ $R_{G(on)}$ = 5.1 $\Omega;$ V_{GS} = -4 V/18 V; L = 100 μH

Fig. 18. Clamped Inductive Switching Energy as a function of drain current



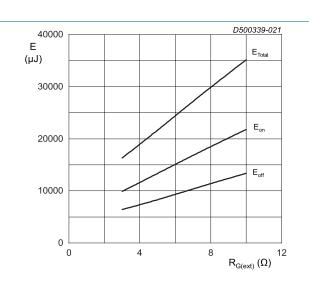
 $T_{j} = 25~^{\circ}\text{C};~V_{DD} = 600~V;~R_{G(off)} = 3~\Omega;~R_{G(on)} = 3~\Omega;$ $V_{GS} = -4 \text{ V}/18 \text{ V}; L = 100 \mu\text{H}$

Fig. 19. Clamped Inductive Switching Energy as a function of drain current



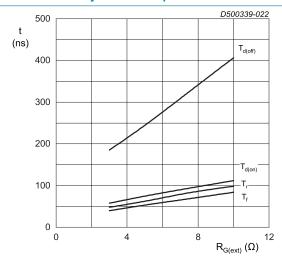
 I_{DS} = 250 A; V_{DD} = 800 V; $R_{G(off)}$ = 5.1 $\Omega;$ $R_{G(on)}$ = 5.1 $\Omega;$ V_{GS} = -4 V/18 V; L = 100 μH

Fig. 20. Clamped Inductive Switching Energy as a function of junction temperature



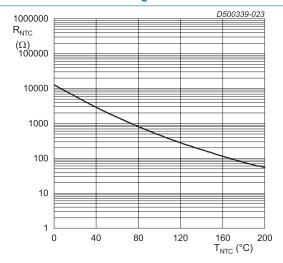
 $T_{\rm j}$ = 25 °C; $V_{\rm DD}$ = 800 V; $I_{\rm DS}$ = 250 A; $V_{\rm GS}$ = -4 V/18 V; $L = 100 \mu H$

Fig. 21. Clamped Inductive Switching Energy as a function of external gate resistance



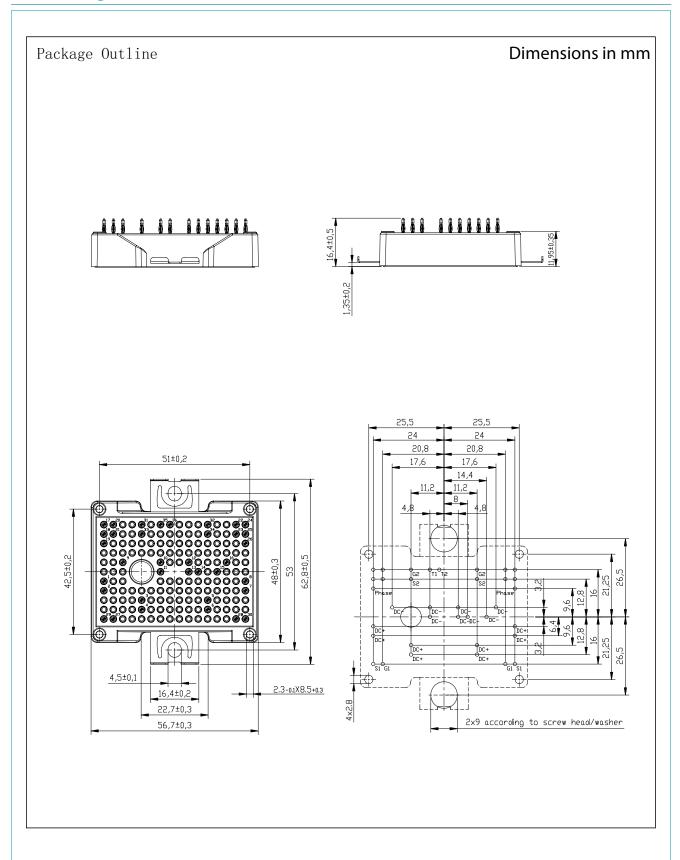
 $T_i = 25$ °C; $V_{DD} = 800$ V; $I_{DS} = 250$ A; $V_{GS} = -4$ V/18 V; Fig. 23. NTC thermistor resistance as a function of $L = 100 \mu H$

Fig. 22. Switching time as a function of external gate resistance



NTC temperature

11. Package outline



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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WMSC004H12B2P-D

N-Channel Silicon Carbide MOSFET Module

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