Product data sheet

1. General description

WSJT65R075DW is a high voltage N-channel MOSFET in TO247 package, which utilizes the advanced super-junction technology to provide superior FOM RDS(on)*Qg among silicon based MOSFETs. It is particularly suitable for applications require extreme high efficiency and power density.





2. Features and benefits

- Superior FOM RDS(on)*Qg
- · Extremely low switching loss
- Integrated ultrafast body diode
- 100% avalanche tested

3. Applications

- EV charger
- High efficiency power supplies
- On board charger
- Inverters

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit
Absolute	maximum rating						
V _{DS}	drain-source voltage				650		V
V_{GS}	gate-source voltage				±30		V
I _D	continuous drain current	T _{mb} = 25 °C			50		Α
P _{tot}	power dissipation	T _{mb} = 25 °C			481		W
T _j	junction temperature			-55 to 150		°C	
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 10 \text{ V}, I_{D} = 25 \text{ A}$		-	55	70	mΩ
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = 10 \text{ V}$		-	70	-	nC
E _{oss}	coss stored erergy	V _{GS} = 0 V; V _{DS} = 0 to 400 V		-	10.7	-	μJ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	D	drain	ЩОЩ	
3	S	source		G (EX)
mb	D	mounting base; connected to drain	1 2 3	sym300 S

6. Ordering information

Table 3. Ordering information

Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WSJT65R075DW	TO247	WSJT65R075DWQ	Tube	30	TO247N	20-July-2016

7. Marking

Table 4. Marking codes

Type number	Marking codes
WSJT65R075DW	WSJT 65R075DW

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V _{DS}	drain-source voltage			650	V
V _{GS}	gate-source voltage			±30	V
I _D	continuous drain current	T _{mb} = 25 °C		50	Α
		T _{mb} = 100 °C		32	Α
I _{DM}	pulsed drain current	T _{mb} = 25 °C		140	Α
P _{tot}	power dissipation	T _{mb} = 25 °C		481	W
E _{AS}	single pulse drain-to- source avalanche	$I_{AS} = 10 \text{ A}; R_{GS} = 25 \Omega; V_{DD} = 50 \text{ V};$ $T_j = 25 \text{ °C}$		500	mJ
E _{AR}	repetitive avalanche energy	$I_{AS} = 10 \text{ A}; R_{GS} = 25 \Omega; V_{DD} = 50 \text{ V};$ $T_j = 25 \text{ °C}$		1.5	mJ
I _{AS}	avalanche current, single pulse			10	А
dv/dt	MOSFET dv/dt ruggedness			50	V/ns
dv/dt	reverse diode dv/dt			50	V/ns
dl _F /dt	maximum diode commutation speed			850	A/µs
T _{stg}	storage temperature			-55 to 150	°C
T _j	junction temperature			-55 to 150	°C

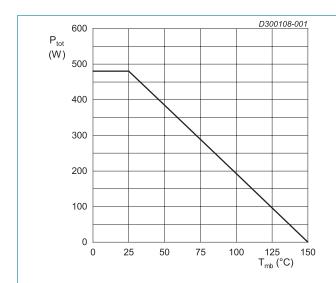


Fig. 1. Total power dissipation as a function of mounting base temperature

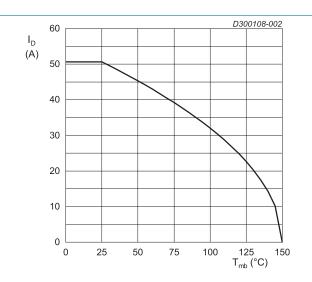


Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base			-	0.20	0.26	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air		-	45	-	K/W

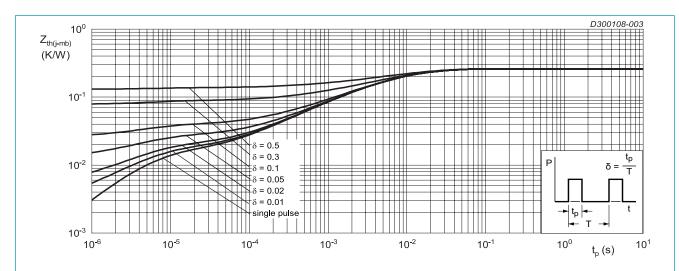


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration; maximum values

10. Characteristics

Table 7. Characteristics

T_i = 25 °C unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 1 \text{ mA}; V_{GS} = 0 \text{ V}$		650	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	$I_D = 250 \ \mu A; \ V_{DS} = V_{GS}$		3.0	-	5.0	V
I _{DSS}	drain leakage current	$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}$		-	-	10	μA
		$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 ^{\circ}\text{C}$		-	300	-	μA
I _{GSS}	gate leakage current	$V_{GS} = \pm 30 \text{ V}; V_{DS} = 0 \text{ V}$		-	-	±100	nA
$R_{\text{DS(on)}}$	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}$		-	55	70	mΩ
R_G	gate resistance	f = 1 MHz		-	1.0	-	Ω
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = 10 \text{ V}$		-	70	-	nC
Q _{GS}	gate-source charge			-	26	-	nC
Q_{GD}	gate-drain charge			-	19	-	nC
C _{iss}	input capacitance	V _{DS} = 400 V; V _{GS} = 0 V; f = 250 KHz		-	4241	-	pF
C _{oss}	output capacitance			-	84	-	pF
C _{rss}	reverse transfer capacitance			-	2.3	-	pF
$C_{o(er)}$	effective output capacitance, energy related	V _{GS} = 0 V; V _{DS} = 0 to 400 V		-	133	-	pF
$C_{o(tr)}$	effective output capacitance, time related			-	831	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = 400 \text{ V}; V_{GS} = 10 \text{ V}; R_G = 5.1 \Omega;$		-	65	-	ns
t _r	rise time	I _D = 25 A		-	12	-	ns
$t_{\text{d(off)}}$	turn-off delay time			-	54	-	ns
t _f	fall time			-	2.1	-	ns
Source-d	rain diode						
V _{SD}	source-drain voltage	V _{GS} = 0 V; I _S = 25 A		-	0.87	1.2	V
I _s	body-diode continuous current	T _{mb} = 25 °C		-	-	50	А
t _{rr}	reverse recovery time	$V_R = 400 \text{ V}; I_F = 25 \text{ A}; dI_F/dt = 100 \text{ A/}\mu\text{s}$		-	180	-	ns
Q _{rr}	reverse recovered charge			-	1.6	-	μC
I _{rrm}	reverse recovery current			-	17	-	Α

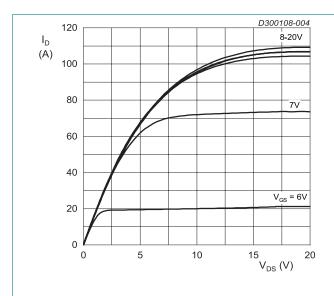
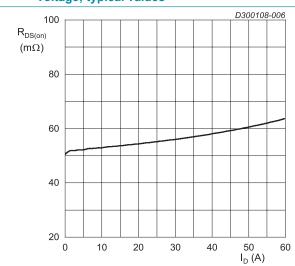


Fig. 4. Drain current as a function of drain-source voltage; typical values



V_{GS} = 10 V
Fig. 6. Drain-source on-state resistance as a function of drain current; typical values

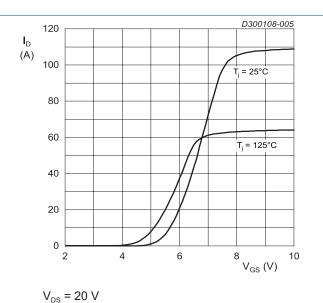
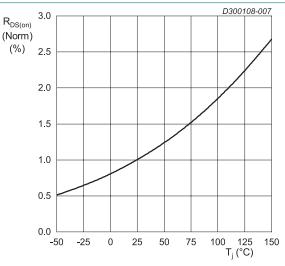


Fig. 5. Drain current as a function of gate-source voltage; typical values



V_{GS} = 10 V; I_D = 25 A

Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature

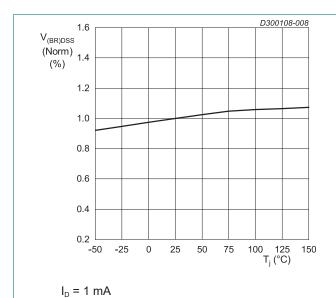
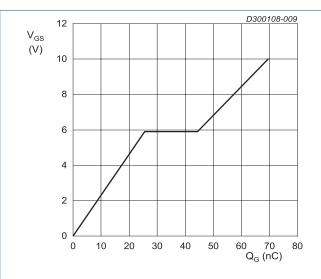
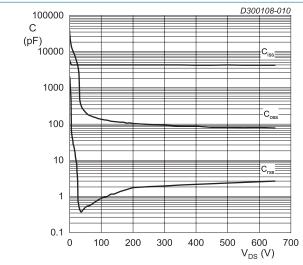


Fig. 8. Normalized drain-source breakdown voltage as a function of junction temperature

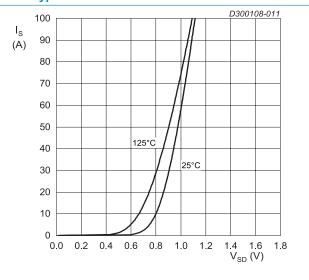


 $I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}$

Fig. 9. Gate-source voltage as a function of gate charge; typical values



V_{GS} = 0 V; f = 250 KHz Fig 10. Capacitances as a function of drain-source voltage; typical values



V_{GS} = 0 V Fig 11. Source current as a function of source-drain voltage; typical values

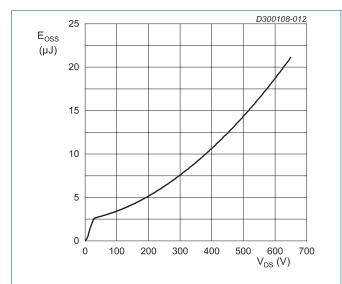
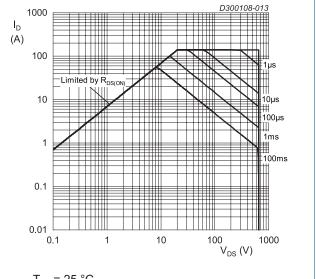
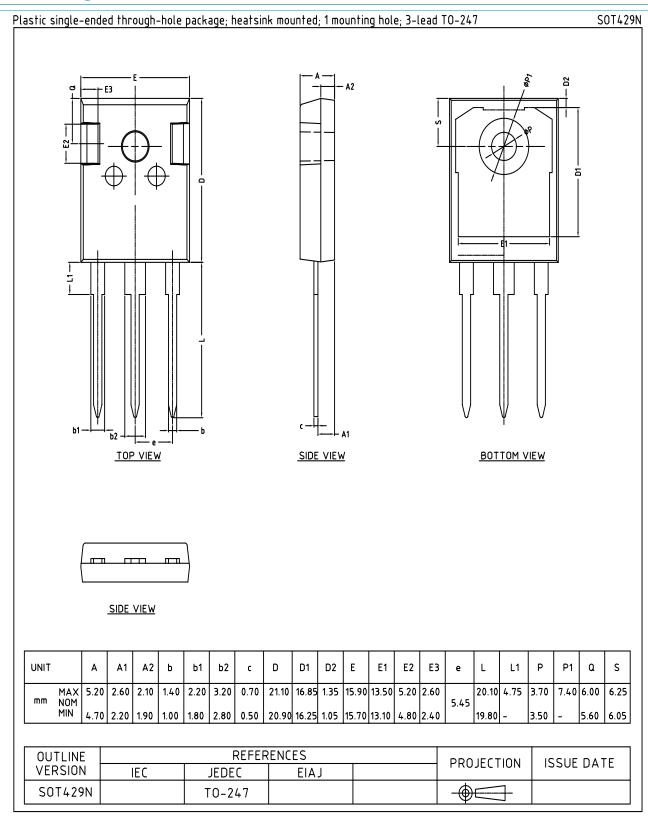


Fig. 12. Output capacitance stored energy as a function of drain-source voltage



 T_{mb} = 25 °C Fig. 13. Safe operating area

11. Package outline



12. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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