**Product data sheet** 

## 1. General description

WSJM80R200X is a high voltage N-channel MOSFET in TO220F package, which utilizes the advanced super-junction technology to provide superior FOM  $R_{\rm DS(on)} \, ^{\star} \, Q_{\rm g}$  among silicon based MOSFETs. It is particularly suitable for applications require extreme high efficiency and power density.



### 2. Features and benefits

- Superior FOM  $R_{DS(on)} * Q_g$
- Extremely low switching loss
- 100% avalanche tested

## 3. Applications

- EV charger
- High efficiency power supplies
- On board charger
- Inverters

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit	
Absolute	Absolute maximum rating							
V <sub>DS</sub>	drain-source voltage				800		V	
$V_{GS}$	gate-source voltage				±30		V	
I <sub>D</sub>	continuous drain current	T <sub>h</sub> = 25 °C	[1]		22		Α	
P <sub>tot</sub>	power dissipation	T <sub>h</sub> = 25 °C			37		W	
T <sub>j</sub>	junction temperature			-	·55 to 15	0	°C	
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit	
Static cha	aracteristics							
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 10 \text{ V}, I_{D} = 10.5 \text{ A}$		-	180	200	mΩ	
Dynamic	Dynamic characteristics							
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 10.5 A; V <sub>DS</sub> = 640 V; V <sub>GS</sub> = 10 V		-	52	-	nC	
E <sub>oss</sub>	coss stored erergy	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 0 to 640 V		-	11	-	μJ	

# 5. Pinning information

#### **Table 2. Pinning information**

mb	
	$G \longrightarrow A$
ed	svm300 S
Ď Ď Ď	
	ed

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WSJM80R200X	TO220F	WSJM80R200XQ	Tube	50	SOT186A	14-Nov-2013

## 7. Marking

#### **Table 4. Marking codes**

Type number	Marking codes
WSJM80R200X	WSJM 80R200X

# 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V <sub>DS</sub>	drain-source voltage			800	V
V <sub>GS</sub>	gate-source voltage			±30	V
I <sub>D</sub>	continuous drain current	T <sub>mb</sub> = 25 °C	[1]	22	Α
		T <sub>mb</sub> = 100 °C	[1]	14	Α
I <sub>DM</sub>	pulsed drain current	T <sub>mb</sub> = 25 °C		88	Α
P <sub>tot</sub>	power dissipation	T <sub>mb</sub> = 25 °C		37	W
E <sub>AS</sub>	single pulse drain-to- source avalanche	$I_{AS} = 13.5 \text{ A}; R_{GS} = 25 \Omega; V_{DD} = 50 \text{ V};$ $T_j = 25 \text{ °C}$		911	mJ
E <sub>AR</sub>	repetitive avalanche energy	$I_{AS} = 13.5 \text{ A}; R_{GS} = 25 \Omega; V_{DD} = 50 \text{ V};$ $T_j = 25 \text{ °C}$		0.64	mJ
I <sub>AS</sub>	avalanche current, single pulse			13.5	А
dv/dt	MOSFET dv/dt ruggedness			50	V/ns
dv/dt	reverse diode dv/dt			15	V/ns
dl <sub>F</sub> /dt	maximum diode commutation speed			500	A/µs
T <sub>stg</sub>	storage temperature			-55 to 150	°C
T <sub>j</sub>	junction temperature			-55 to 150	°C

[1] Limited by maximum junction temperature, equivalent to TO220.

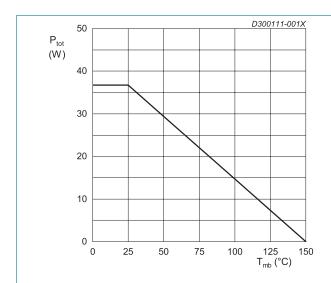


Fig. 1. Total power dissipation as a function of heatsink temperature

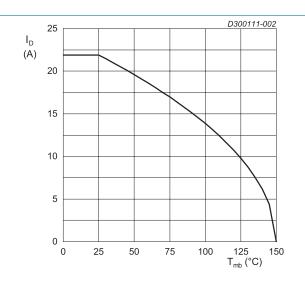


Fig. 2. Continuous Drain Current as a function of heatsink temperature

### 9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
$R_{\text{th(j-h)}}$	thermal resistance from junction to heatsink			-	2.6	3.4	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air		-	60	-	K/W

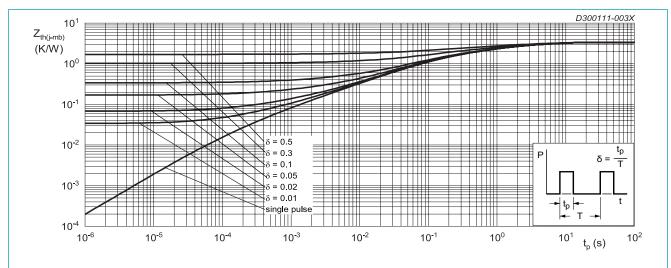


Fig. 3. Transient thermal impedance from junction to heatsink as a function of pulse duration; maximum values

## 10. Characteristics

### **Table 7. Characteristics**

T<sub>i</sub> = 25 °C unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$V_{(\text{BR})\text{DSS}}$	drain-source breakdown voltage	$I_D = 250 \ \mu A; \ V_{GS} = 0 \ V$		800	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	$I_D = 250 \ \mu A; \ V_{DS} = V_{GS}$		2.5	-	4.5	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 800 \text{ V}; V_{GS} = 0 \text{ V}$		-	-	1	μA
		$V_{DS} = 800 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 ^{\circ}\text{C}$		-	-	10	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS} = \pm 30 \text{ V}; V_{DS} = 0 \text{ V}$		-	-	±100	nA
$R_{\text{DS(on)}}$	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 10.5 \text{ A}$		-	180	200	mΩ
$R_G$	gate resistance	f = 1 MHz		-	1.8	-	Ω
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 10.5 A; V <sub>DS</sub> = 640 V; V <sub>GS</sub> = 10 V		-	52	-	nC
Q <sub>GS</sub>	gate-source charge			-	12	-	nC
$Q_{GD}$	gate-drain charge			-	19	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 640 V; V <sub>GS</sub> = 0 V; f = 250 kHz		-	2413	-	pF
C <sub>oss</sub>	output capacitance			-	46	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	0.7	-	pF
$C_{o(er)}$	effective output capacitance, energy related	$V_{GS} = 0 \text{ V}; V_{DS} = 0 \text{ to 640 V}$		-	52	-	pF
$C_{o(tr)}$	effective output capacitance, time related			-	221	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS} = 400 \text{ V}; V_{GS} = 10 \text{ V}; R_G = 3 \Omega;$		-	16	-	ns
t <sub>r</sub>	rise time	I <sub>D</sub> = 10.5 A		-	11	-	ns
$t_{d(off)}$	turn-off delay time			-	45	-	ns
t <sub>f</sub>	fall time			-	8.5	-	ns
Source-d	rain diode						
V <sub>SD</sub>	source-drain voltage	V <sub>GS</sub> = 0 V; I <sub>S</sub> = 10.5 A		-	0.8	1.1	V
I <sub>s</sub>	body-diode continuous current	T <sub>h</sub> = 25 °C		-	-	22	А
t <sub>rr</sub>	reverse recovery time	$V_R = 400 \text{ V}; I_F = 10.5 \text{ A}; dI_F/dt = 100 \text{ A}/$		-	377	-	ns
Q <sub>rr</sub>	reverse recovered charge	μs		-	6.4	-	μC
I <sub>rrm</sub>	reverse recovery current			_	31	_	Α

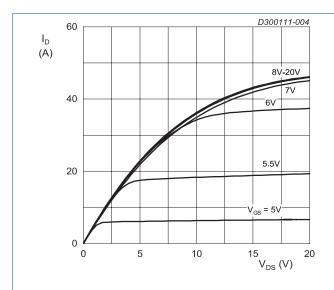
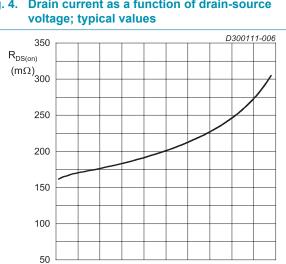


Fig. 4. Drain current as a function of drain-source



 $V_{GS}$  = 10 VFig. 6. Drain-source on-state resistance as a function of drain current; typical values

20

30

40

Ι<sub>D</sub> (Α)

50

10

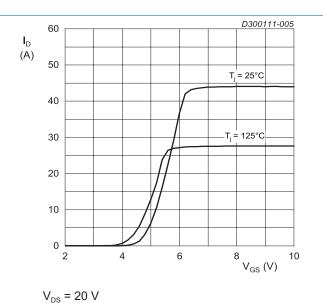
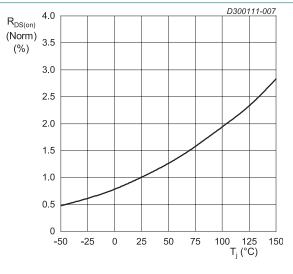


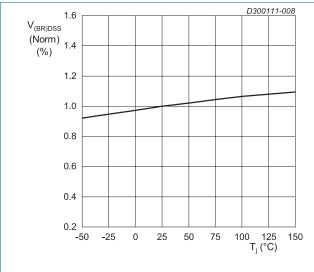
Fig. 5. Drain current as a function of gate-source voltage; typical values



 $V_{GS} = 10 \text{ V; } I_D = 10.5 \text{ A} \\$  Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature

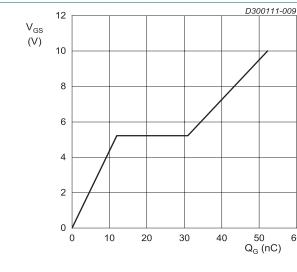
60

#### **Super-Junction Power MOSFET**



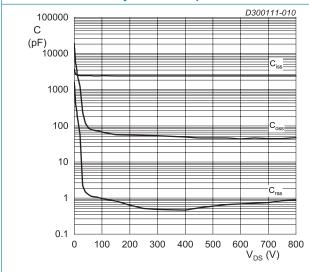
 $I_D = 250 \mu A$ 

Fig. 8. Normalized drain-source breakdown voltage as a function of junction temperature

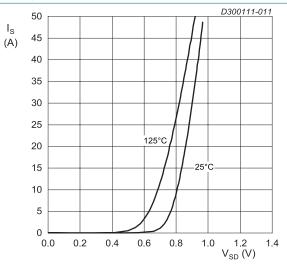


 $I_D = 10.5 A; V_{DS} = 640 V$ 

Fig. 9. Gate-source voltage as a function of gate charge; typical values

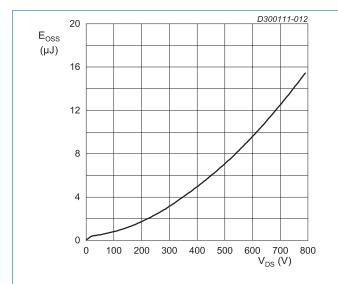


 $V_{GS} = 0 V; f = 250 kHz$ Fig 10. Capacitances as a function of drain-source voltage; typical values

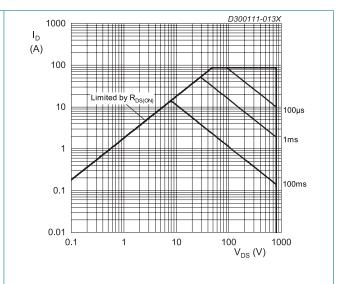


 $V_{GS} = 0 V$ 

Fig 11. Source current as a function of source-drain voltage; typical values

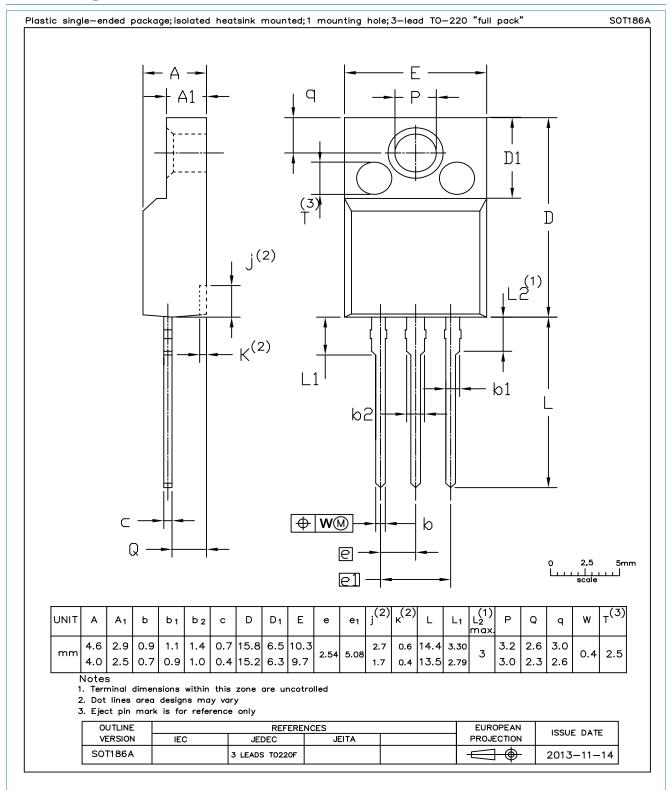






 $T_h = 25 \, ^{\circ}\text{C}$  Fig. 13. Safe operating area

# 11. Package outline



### 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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