

N-Channel Silicon Carbide MOSFET

Rev.02 - 22 November 2023

**Product data sheet** 

alogen-Free

ead-Free

### **1. General description**

Silicon Carbide MOSFET in a TO263-7L plastic package, designed for high frequency, high efficiency systems.

### 2. Features and benefits

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- Low on-resistance
  - Fast switching speed
  - 0V turn-off gate voltage for simple gate drive
  - 100% UIS Tested
  - · Easy to parallel
  - Controllable dV/dt for optimized EMI
  - Reduced cooling requirements
  - RoHS compliant

### 3. Applications

- Switch Mode Power Supplies
- UPS
- Solar string inverter and solar optimizer
- EV Charger
- Motor Drives

### 4. Quick reference data

Table 1. Qu	lick reference data							
Symbol	Parameter	Conditions	Notes		Values		Unit	
Absolute	maximum rating							
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C			1200		V	
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C			58		А	
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C			375		W	
Tj	junction temperature			-55 to 175		°C		
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit	
Static cha	aracteristics							
$R_{\text{DS(on)}}$	drain-source on-state resistance	V <sub>GS</sub> = 15 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C		-	60	-	mΩ	
Dynamic	characteristics							
Q <sub>G(tot)</sub>	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	83	-	nC	
$Q_{GD}$	gate-drain charge	T <sub>j</sub> = 25 °C		-	15	-	nC	
Source-d	Source-drain diode							
Q <sub>r</sub>	recovered charge	$I_{SD}$ = 25 A; di/dt = 500 A/µs; V <sub>DS</sub> = 400 V; T <sub>j</sub> = 25 °C		-	64	-	nC	

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## 5. Pinning information

Table	2	Pinning	information
lable	Ζ.	Pinning	mormation

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D
2	SS	source sense		
3-7	S	source		G (E)
mb	D	mounting base; connected to drain	TO263-7L	SS sym301 S

## 6. Ordering information

Table 3. Ordering information								
Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date		
WNSC2M60120B7	TO263-7L	WNSC2M60120B76J	Reel	800	TO263P-7L	12-Jun-2023		

## 7. Marking

Table 4. Marking codes	
Type number	Marking codes
WNSC2M60120B7	WNSC2M 60120B7

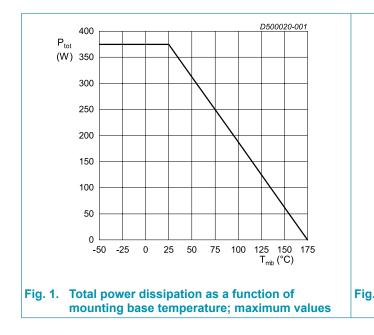
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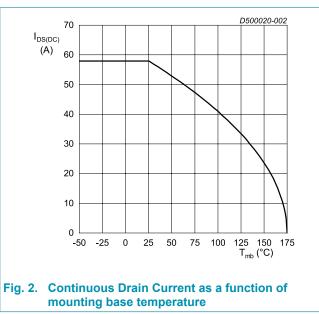
## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C		1200	V
$V_{GS,max}$	gate-source voltage			-12 to 24	V
V <sub>GS,op</sub>	gate-source voltage			-4 to 18	V
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C		375	W
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C		58	А
		V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 100 °C		41	А
I <sub>DM</sub>	peak drain current	pulse width $t_p$ limited by $T_{jmax}$	Fig.17	116	А
l <sub>s</sub>	continuous diode current	V <sub>GS</sub> = -4 V; T <sub>mb</sub> = 25 °C		48.6	А
I <sub>SM</sub>	pulse diode current	$V_{GS}$ = -4 V; pulse width $t_p$ limited by $T_{jmax}$		116	A
E <sub>as</sub>	single pulse drain-to- source avalanche	$I_{AS} = 15 \text{ A}; \text{ L} = 1 \text{ mH}; \text{ V}_{DD} = 100 \text{ V};$ $T_j = 25 \text{ °C}$		112.5	mJ
T <sub>stg</sub>	storage temperature			-55 to 175	°C
T <sub>j</sub>	junction temperature			-55 to 175	°C
$T_{sld(M)}$	peak soldering temperature			260	°C





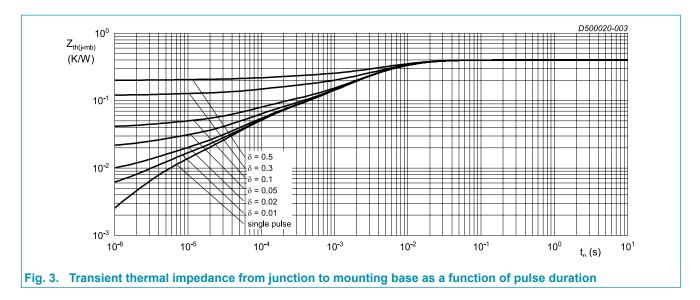
### 9. Thermal & Mechanical characteristics

#### Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
$R_{\text{th(j-mb)}}$	thermal resistance from junction to mounting base			-	0.4	-	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air		-	40	-	K/W
$M_{d}$	Mounting torque	M3 or 6 - 32 screw		-	-	0.6	Nm

Note: It is recommended that a metal washer is inserted between screw head and mounting tab. Do not use self-tapping screws.

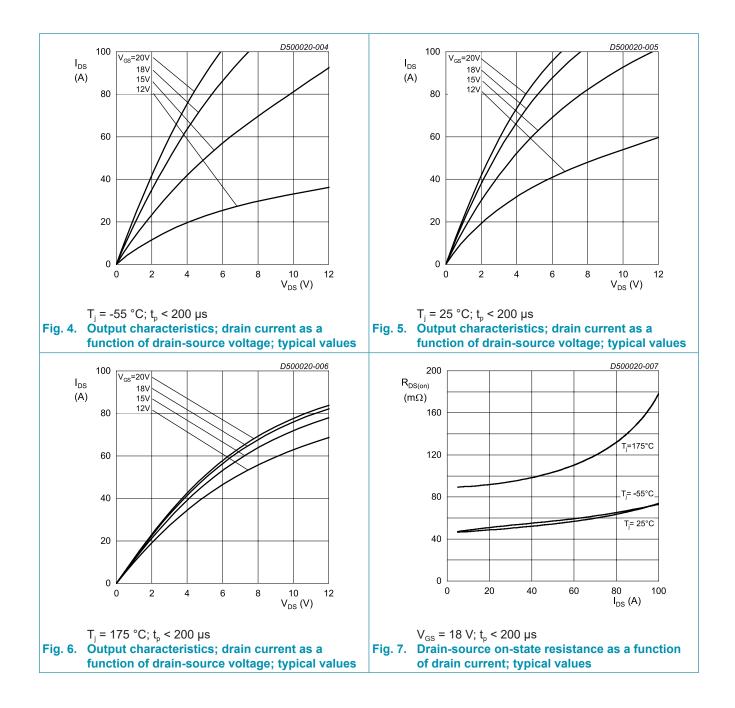
Device is ESD sensitive. Handling precautions are recommanded.

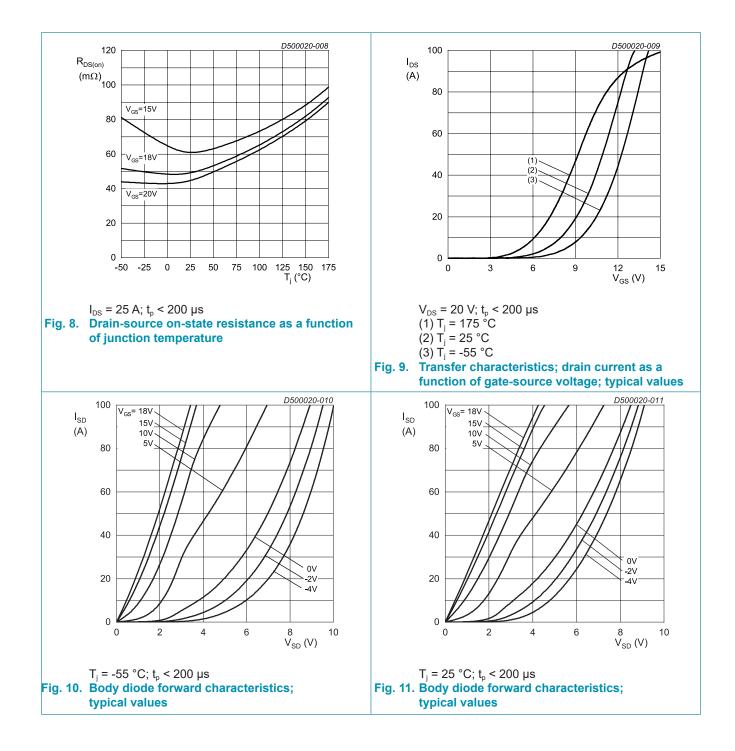


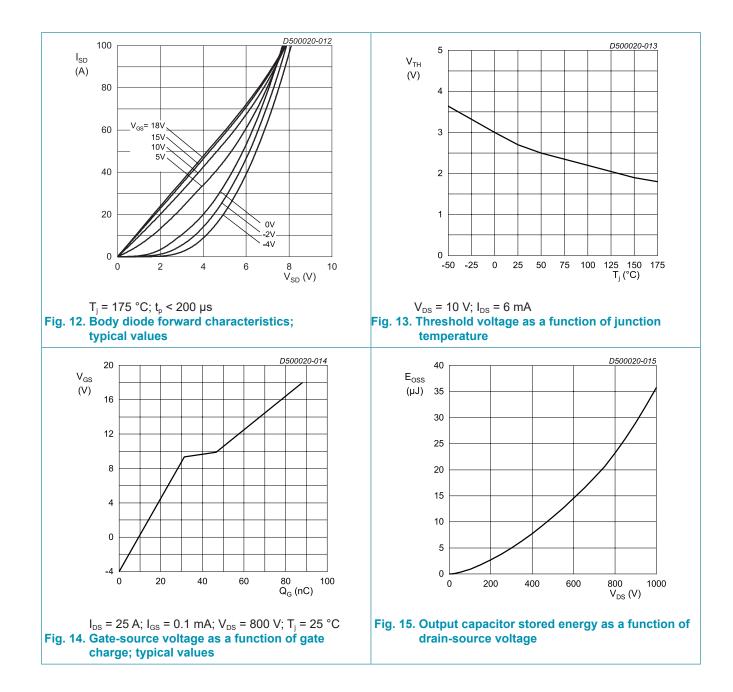
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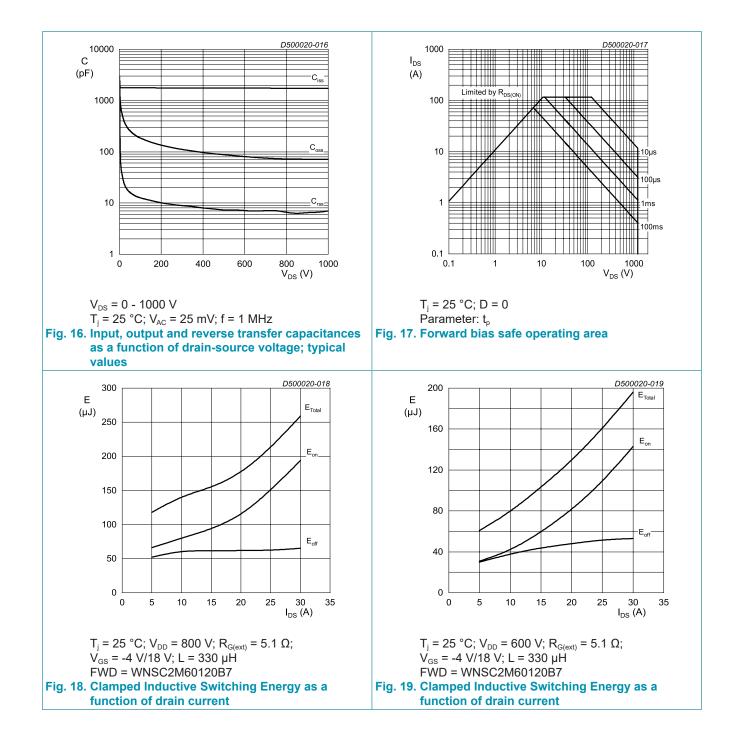
### **10. Characteristics**

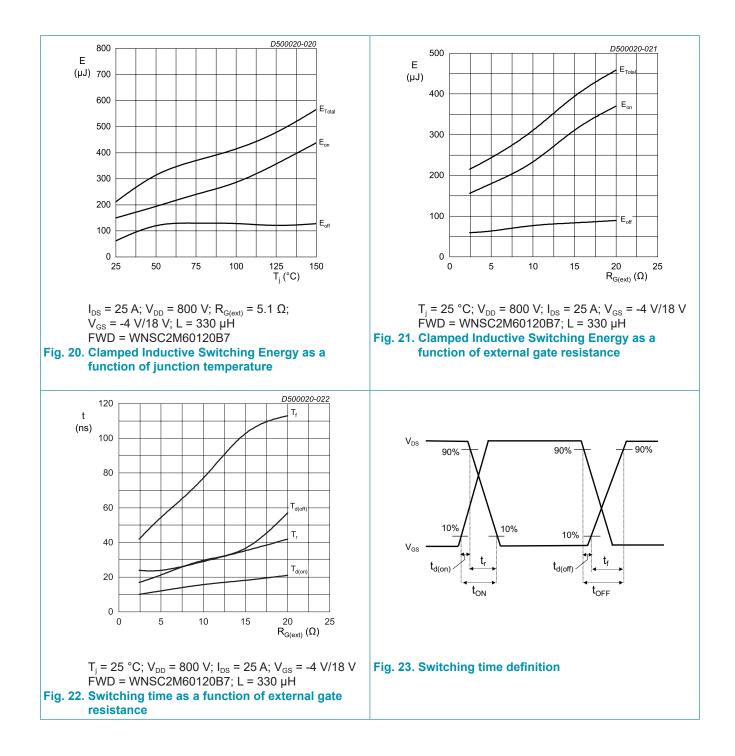
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_{D}$ = 100 µA; $V_{GS}$ = 0 V; $T_{j}$ = 25 °C		1200	-	-	V
V <sub>GS(th)</sub>	gate-source threshold	I <sub>D</sub> = 6 mA; V <sub>DS</sub> = 10 V; T <sub>j</sub> = 25 °C		1.9	2.6	3.5	V
	voltage	I <sub>D</sub> = 6 mA; V <sub>DS</sub> = 10 V; T <sub>j</sub> = 175 °C		-	1.9	-	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 1200 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	0.2	100	μA
		V <sub>DS</sub> = 1200 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C		-	2	-	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 24 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	10	100	nA
		V <sub>GS</sub> = -12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	10	100	nA
R <sub>DS(on)</sub>	drain-source on-state	V <sub>GS</sub> = 15 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C		-	60	-	mΩ
	resistance	V <sub>GS</sub> = 18 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C		-	49	68	mΩ
		V <sub>GS</sub> = 18 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 175 °C		-	93	-	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C		-	2.2	-	Ω
<b>g</b> <sub>fs</sub>	transconductance	V <sub>DS</sub> = 20 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C		-	14	-	S
Dynamic	characteristics				1	1	1
Q <sub>G(tot)</sub>	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	83	-	nC
Q <sub>GS</sub>	gate-source charge	$T_j = 25 \ ^{\circ}C$		-	31	-	nC
Q <sub>GD</sub>	gate-drain charge			-	15	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 1000 V; V <sub>GS</sub> = 0 V; f = 1 MHz;		-	1731	-	pF
C <sub>oss</sub>	output capacitance	$T_j = 25 \ ^{\circ}C$		-	71	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	7	-	pF
E <sub>oss</sub>	Coss stored energy			-	35.5	-	μJ
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 800 V; $V_{GS}$ = -4 V/18 V; $R_{G(ext)}$ = 5.1		-	12	-	ns
t <sub>r</sub>	rise time	Ω; I <sub>D</sub> = 25 A; L = 330 μH; T <sub>j</sub> = 25 °C		-	23	-	ns
t <sub>d(off)</sub>	turn-off delay time			-	21	-	ns
t <sub>r</sub>	fall time			-	55	-	ns
Eon	turn-on energy (Body Diode FWD)		Fig.20	-	150	-	μJ
E <sub>off</sub>	turn-off energy (Body Diode FWD)		Fig.20	-	62	-	μJ
Source-di	ain diode	1					
V <sub>SD</sub>	source-drain voltage	V <sub>GS</sub> = 0 V; I <sub>SD</sub> = 12.5 A; T <sub>j</sub> = 25 °C		-	3.4	-	V
		V <sub>GS</sub> = -4 V; I <sub>SD</sub> = 12.5 A; T <sub>j</sub> = 25 °C		-	5.1	-	V
		V <sub>GS</sub> = -4 V; I <sub>SD</sub> = 12.5 A; T <sub>j</sub> = 175 °C		-	4.4	-	V
t <sub>rr</sub>	reverse recovery time	$I_{sD} = 25 \text{ A}; \text{ di/dt} = 500 \text{ A/}\mu\text{s}; \text{ V}_{DS} = 400 \text{ V};$		-	23.8	-	ns
Q <sub>r</sub>	recovered charge	T <sub>j</sub> = 25 °C		-	64	-	nC
I <sub>rrm</sub>	reverse recovery current			-	4.7	-	А





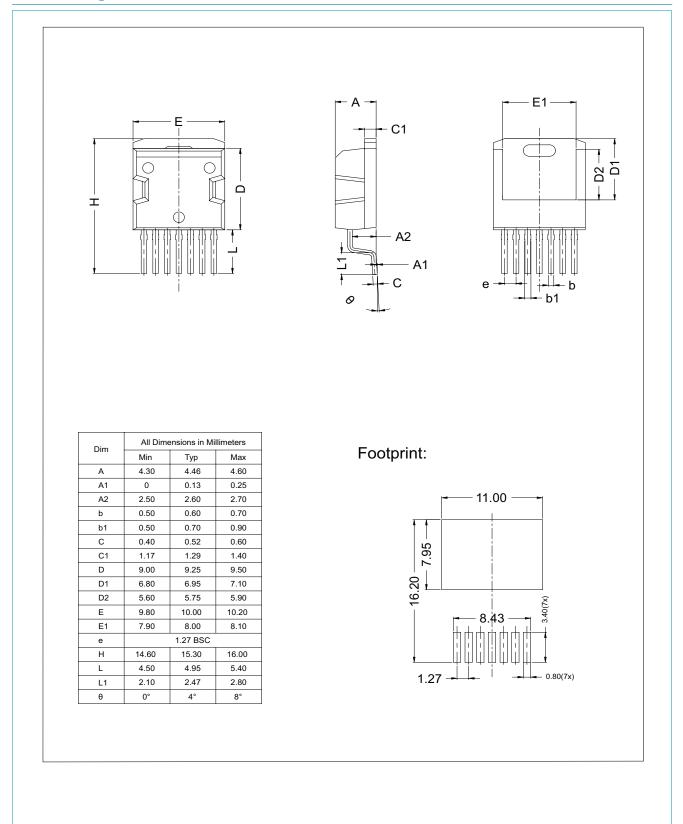






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## 11. Package outline



#### **N-Channel Silicon Carbide MOSFET**

## 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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