Product data sheet

1. General description

Silicon Carbide MOSFET in a TO263-7L plastic package, designed for high frequency, high efficiency systems.



2. Features and benefits

- · Low on-resistance
- Fast switching speed
- 0V turn-off gate voltage for simple gate drive
- 100% UIS Tested
- Easy to parallel
- Controllable dV/dt for optimized EMI
- Reduced cooling requirements
- RoHS compliant

3. Applications

- Switch Mode Power Supplies
- UPS
- · Battery formation instrument
- · Solar string inverter and solar optimizer
- EV Charger
- Motor Drives

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit	
Absolute maximum rating								
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C			650		V	
I _D	drain current	V _{GS} = 18 V; T _{mb} = 25 °C			91		Α	
P _{tot}	total power dissipation	T _{mb} = 25 °C, T _j = 175 °C			484		W	
T _j	junction temperature			-55 to 175 °C		°C		
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit	
Static cha	racteristics							
R _{DS(on)}	drain-source on-state	$V_{GS} = 15 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$		-	45	58	mΩ	
	resistance	V_{GS} = 18 V; I_{D} = 25 A; T_{j} = 25 °C		-	33	43	mΩ	
Dynamic	characteristics		,					
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	87	-	nC	
Q_{GD}	gate-drain charge	T _j = 25 °C		-	9	-	nC	
Source-di	rain diode				•	•		
Q _r	recovered charge	I_{SD} = 25 A; di/dt = 500 A/µs; V_{DS} = 400 V; T_{j} = 25 °C		-	94	-	nC	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D
2	SS	source sense		
3-7	S	source		$G \longrightarrow A$
mb	D	mounting base; connected to drain	TO263-7L	SS Sym301 S

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSC2M45065B7	TO263-7L	WNSC2M45065B76J	Reel	800	TO263P-7L	12-Jun-2023

7. Marking

Table 4. Marking codes

Type number	Marking codes
WNSC2M45065B7	WNSC2M
	45065B7

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		650	V
$V_{\rm GS,max}$	gate-source voltage			-10 to 22	V
$V_{GS,op}$	gate-source voltage			-4 to 18	V
P _{tot}	total power dissipation	T _{mb} = 25 °C, T _j = 175 °C		484	W
I _D	drain current	V _{GS} = 18 V; T _{mb} = 25 °C		91	Α
		V _{GS} = 18 V; T _{mb} = 100 °C		64	Α
I _{DM}	peak drain current	pulse width t _p limited by T _{jmax}	Fig.17	181	Α
Is	continuous diode current	V _{GS} = -4 V; T _{mb} = 25 °C		88	А
I _{SM}	pulse diode current	V_{GS} = -4 V; pulse width t_p limited by T_{jmax}		181	А
E _{as}	single pulse drain-to- source avalanche	I_{AS} = 20 A; L = 1 mH; V_{DD} = 100 V; T_j = 25 °C		200	mJ
T _{stg}	storage temperature			-55 to 175	°C
T _j	junction temperature			-55 to 175	°C
$T_{sld(M)}$	peak soldering temperature			260	°C

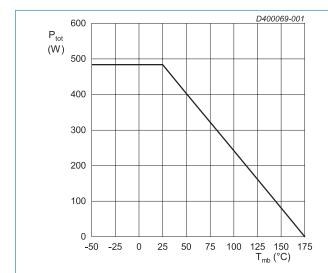


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

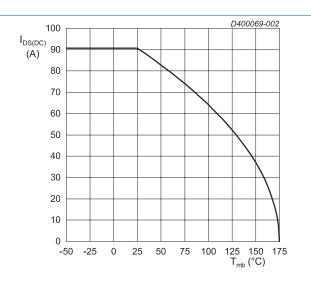


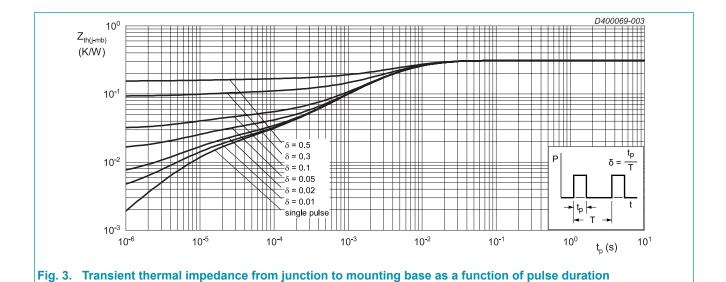
Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base			-	0.31	-	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air		-	40	-	K/W

Note: Device is ESD sensitive. Handling precautions are recommanded.



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_j = 25 °C$		650	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold	$I_D = 6 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$		1.9	2.6	3.5	V
voitage	voltage	$I_D = 6 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C}$		-	1.9	-	V
I _{DSS}	drain leakage current	$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	0.1	50	μA
		V _{DS} = 650 V; V _{GS} = 0 V; T _j = 175 °C		-	5	-	μA
I _{GSS}	gate leakage current	V _{GS} = 22 V; V _{DS} = 0 V; T _j = 25 °C		-	5	100	nA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C		-	5	100	nA
R _{DS(on)}	drain-source on-state	V _{GS} = 15 V; I _D = 25 A; T _j = 25 °C		-	45	58	mΩ
	resistance	V _{GS} = 18 V; I _D = 25 A; T _j = 25 °C		-	33	43	mΩ
		V _{GS} = 18 V; I _D = 25 A; T _j = 175 °C		-	49	-	mΩ
R _G	gate resistance	f = 1 MHz; T _j = 25 °C		-	3.1	-	Ω
g _{fs}	transconductance	V _{DS} = 20 V; I _D = 25 A; T _j = 25 °C		-	15	-	S
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	87	-	nC
Q _{GS}	gate-source charge	T _j = 25 °C		-	35	-	nC
Q_{GD}	gate-drain charge			-	9	-	nC
C _{iss}	input capacitance	V _{DS} = 400 V; V _{GS} = 0 V; f = 1 MHz;		-	2167	-	pF
C _{oss}	output capacitance	T _j = 25 °C		-	191	-	pF
C _{rss}	reverse transfer capacitance			-	8	-	pF
E _{oss}	Coss stored energy			-	95.5	-	μJ
t _{d(on)}	turn-on delay time	$V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V}; R_{G(ext)} = 5.1$		-	11	-	ns
t _r	rise time	$Ω$; $I_D = 12.5 A$; $L = 100 \mu H$; $T_j = 25 °C$		-	8	-	ns
$t_{\text{d(off)}}$	turn-off delay time			-	30	-	ns
t _f	fall time			-	22	-	ns
E _{on}	turn-on energy (Body Diode FWD)		Fig.19	-	50	-	μJ
E _{off}	turn-off energy (Body Diode FWD)		Fig.19	-	24	-	μJ
Source-d	rain diode						
V _{SD}	source-drain voltage	$V_{GS} = 0 \text{ V; } I_{SD} = 25 \text{ A; } T_j = 25 \text{ °C}$		-	3.4	-	V
		V _{GS} = -4 V; I _{SD} = 25 A; T _j = 25 °C		-	3.9	-	V
		V _{GS} = -4 V; I _{SD} = 25 A; T _j = 175 °C		-	3.4	-	V
t _{rr}	reverse recovery time	$I_{SD} = 25 \text{ A}$; di/dt = 500 A/ μ s; $V_{DS} = 400 \text{ V}$;		-	32	-	ns
Q _r	recovered charge	T _j = 25 °C		-	94	-	nC
I _{rrm}	reverse recovery current			-	5.9	-	Α

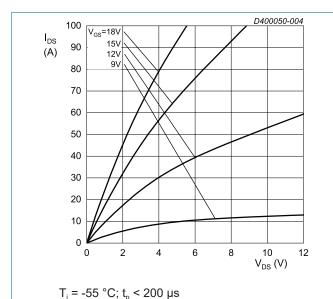


Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values

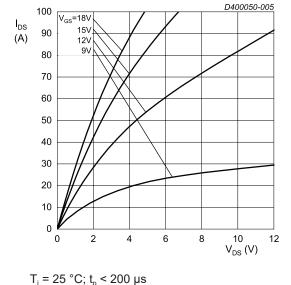
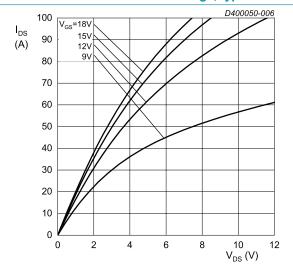
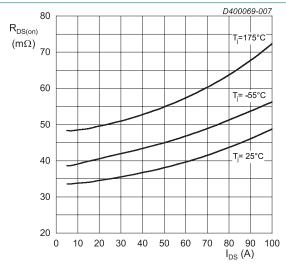


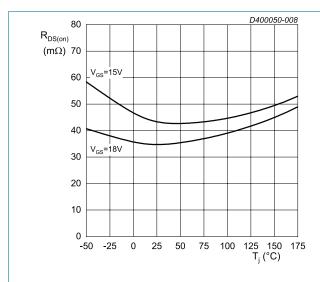
Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



T_j = 175 °C; t_p < 200 μs Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

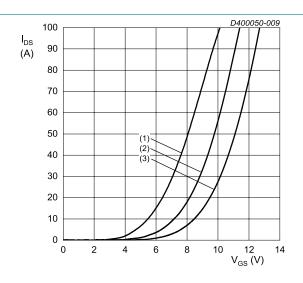


 V_{GS} = 18 V; t_p < 200 µs Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



 I_{DS} = 25 A; t_p < 200 μs

Fig. 8. Drain-source on-state resistance as a function of junction temperature

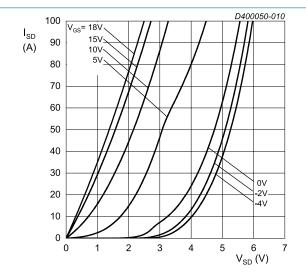


 V_{DS} = 20 V; t_p < 200 μs

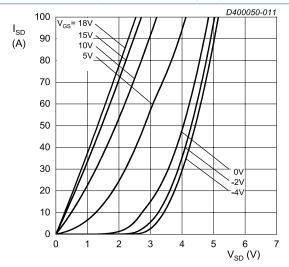
(1) $T_j = 175 \,^{\circ}C$ (2) $T_j = 25 \,^{\circ}C$

(3) $T_i = -55 \,^{\circ}C$

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values

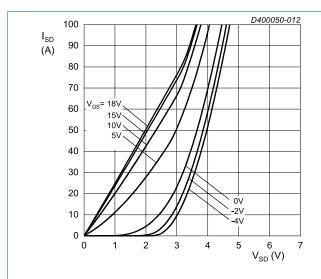


 $T_{j} = -55 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$ Fig. 10. Body diode forward characteristics; typical values

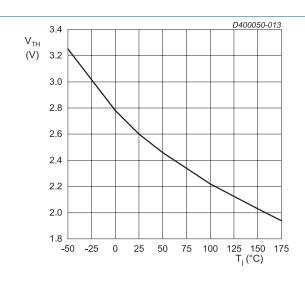


 $T_{j} = 25 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$

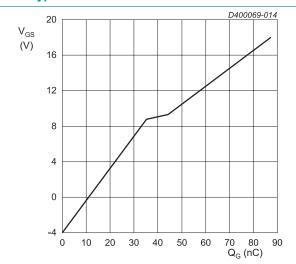
Fig. 11. Body diode forward characteristics; typical values



 T_j = 175 °C; t_p < 200 µs Fig. 12. Body diode forward characteristics; typical values



 $V_{DS} = V_{GS}$; $I_{DS} = 6$ mA Fig. 13. Threshold voltage as a function of junction temperature



I_{DS} = 25 A; I_{GS} = 0.1 mA; V_{DS} = 400 V; T_j = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values

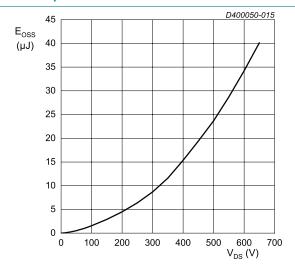
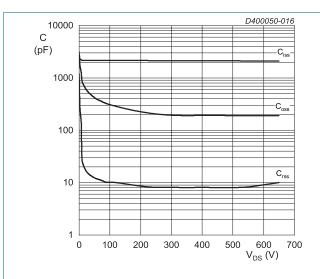
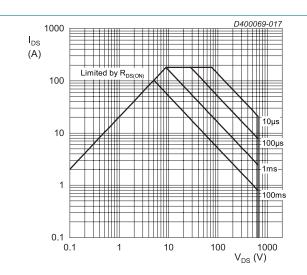


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



 $V_{DS} = 0 - 650 \text{ V}$

 $T_i = 25 \, ^{\circ}C; \, V_{AC} = 25 \, \text{mV}; \, f = 1 \, \text{MHz}$

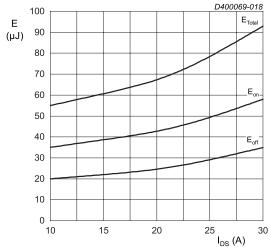


 $T_i = 25 \,^{\circ}C; D = 0$ Parameter: t₀

Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical

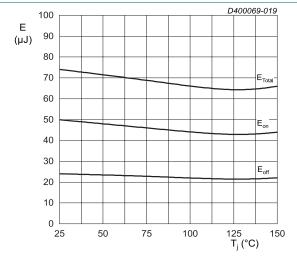
values





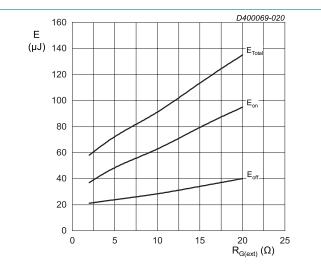
$$\begin{split} T_{j} = 25~^{\circ}C;~V_{DD} = 400~V;~R_{G(ext)} = 2~\Omega;\\ V_{GS} = -4~V/18~V;~L = 100~\mu H \end{split}$$
FWD = WNSC2M45065B7

Fig. 18. Clamped Inductive Switching Energy as a function of drain current



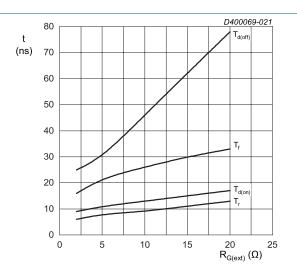
 I_{DS} = 12.5 A; V_{DD} = 400 V; $R_{G(ext)}$ = 5.1 Ω ; $V_{GS} = -4 \text{ V}/18 \text{ V}; L = 100 \mu\text{H}$ FWD = WNSC2M45065B7

Fig. 19. Clamped Inductive Switching Energy as a function of junction temperature



 T_{j} = 25 °C; V_{DD} = 400 V; I_{DS} = 12.5 A; V_{GS} = -4 V/18 V FWD = WNSC2M45065B7; L = 100 μH

Fig. 20. Clamped Inductive Switching Energy as a function of external gate resistance



 $T_{\rm j}$ = 25 °C; $V_{\rm DD}$ = 400 V; $I_{\rm DS}$ = 12.5 A; $V_{\rm GS}$ = -4 V/18 V FWD = WNSC2M45065B7; L = 100 μH

Fig. 21. Switching time as a function of external gate resistance

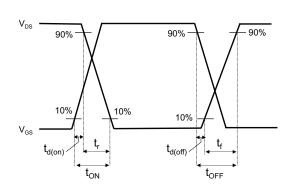
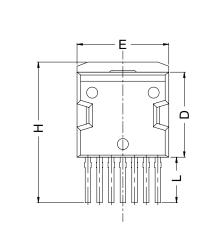
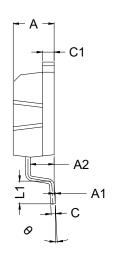
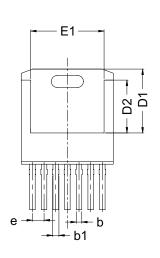


Fig. 22. Switching time definition

11. Package outline

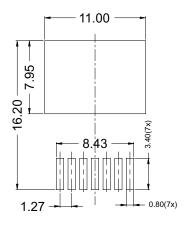






Dim	All Dime	ensions in Mi	llimeters
Dilli	Min	Тур	Max
Α	4.30	4.46	4.60
A1	0	0.13	0.25
A2	2.50	2.60	2.70
b	0.50	0.60	0.70
b1	0.50	0.70	0.90
С	0.40	0.52	0.60
C1	1.17	1.29	1.40
D	9.00	9.25	9.50
D1	6.80	6.95	7.10
D2	5.60	5.75	5.90
E	9.80	10.00	10.20
E1	7.90	8.00	8.10
е		1.27 BSC	
Н	14.60	15.30	16.00
L	4.50	4.95	5.40
L1	2.10	2.47	2.80
θ	0°	4°	8°

Footprint:



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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For more information, please visit: http://www.ween-semi.com
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