

N-Channel Silicon Carbide MOSFET Module

Rev.01 - 5 February 2024

Product data sheet

1. General description

WeEnPACK-B1 module with WeEn 1200V Gen2 SiC MOSFET and PressFit pin type. Intergrated with NTC temperature sensor.



2. Features and benefits

- Half bridge topology
- PressFit pins technology
- Low R_{DSon}
- Low Switching Losses
- Low Q_g and C_{rss}
- Low Inductive Design

3. Applications

- Power inverters
- AC-DC converters
- DC-DC converters
- Active power factor correctors
- Motor drivers

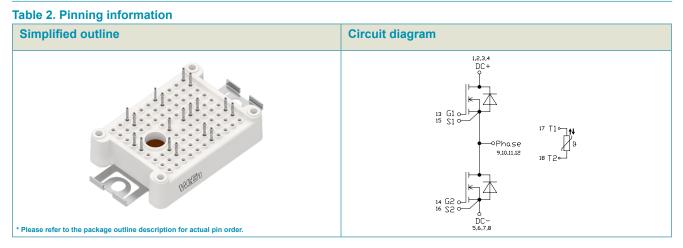
4. Quick reference data

Table 1 Quick reference data

Table 1. Q	uick reference data						
Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute	maximum rating						
V _{DS}	drain-source voltage	T _j = 25 °C			1200		V
I _D	drain current	V _{GS} = 18 V; T _h = 25 °C			85		А
P _{tot}	total power dissipation	T _h = 25 °C			146		W
Tj	junction temperature			-40 to 150			°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$R_{\text{DS(on)}}$	drain-source on-state	V_{GS} = 15 V; I_{D} = 75 A; T_{j} = 25 °C		-	16	-	mΩ
	resistance	V _{GS} = 18 V; I _D = 75 A; T _j = 25 °C		-	12.9	-	mΩ
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	$I_D = 50 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	300	-	nC
Q_{GD}	gate-drain charge	T _j = 25 °C		-	51	-	nC
Source-d	rain diode						
Q _r	recovered charge	I_{SD} = 80 A; V _{GS} = -4 V; di/dt = 6000 A/µs; V _R = 600 V; T _j = 25 °C		-	828	-	nC

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5. Pinning information



6. Ordering information

Table 3. Ordering information									
Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date			
WMSC016H12B1P	WeEnPACK-B1	WMSC016H12B1P6T	Tray	16	WeEnPACK- B1PHB-A	14-Dec-2023			

7. Marking

Table 4. Marking codes								
Type number	Marking codes							
WMSC016H12B1P	WMSC016H12B1P							

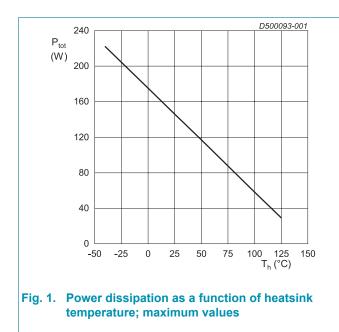
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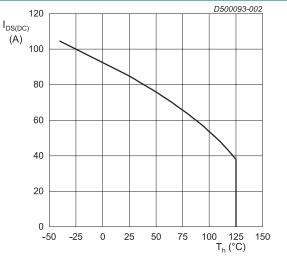
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
T _{stg}	storage temperature			-40 to 150	°C
T _{j.op}	operating junction temperature			-40 to 150	°C
V _{ISOL}	RMS isolation voltage	T _j = 25 °C; all terminals shorted; f = 50 Hz; t = 1 s		3500	V
MOSFET		·			
V _{DS}	drain-source voltage	T _j = 25 °C		1200	V
V _{GS,max}	gate-source voltage	Absolute maximum values		-12 to 24	V
V _{GS,op}	gate-source voltage	Recommended operational values		-4 to 18	V
P _{tot}	total power dissipation	T _h = 25 °C		146	W
I _D	drain current	V _{GS} = 18 V; T _h = 25 °C		85	А
		V _{GS} = 18 V; T _h = 100 °C		54	А
I _{DM}	peak drain current	pulse width t_p limited by T_{jmax}	Fig.17	170	А
E _{as}	single pulse drain-to- source avalanche	I_{AS} = 20 A; L = 1 mH; V _{DD} = 100 V; $T_{j(init)}$ = 25 °C; per MOSFET		200	mJ
Body Diod	le	·			
I _{SD}	DC body diode forward current	V _{GS} = -4 V; T _h = 25 °C		36	А
I _{SD,pulse}	Pulse body diode current	verified by design, t_p limited by T_{jmax}		170	А





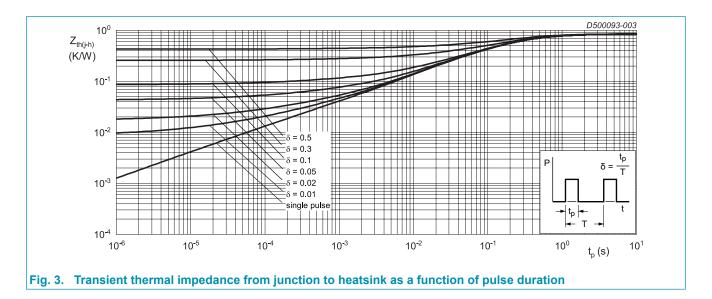


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9. Thermal characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-c)}	thermal resistance from junction to case	per MOSFET		-	0.26	-	K/W
$R_{th(j-h)}$	thermal resistance from junction to heatsink	per MOSFET, $\lambda_{grease} = 1 \text{ W/(m·K)}$		-	0.86	-	K/W
Internal l	solation	basic insulation (class 1, IEC 61140)		AI2O3			
d_{Creep}	Creepage distance	terminal to heatsink		-	11.5	-	mm
		terminal to terminal		-	6.3	-	mm
d _{Clear}	Clearance	terminal to heatsink		-	10	-	mm
		terminal to terminal		-	5	-	mm
СТІ	Comperative tracking index			>200			
F	Mounting force per clamp			20	-	50	Ν
G	Approximate Weight			-	20	-	g

Note: Module is ESD sensitive. Handling precautions are recommanded.



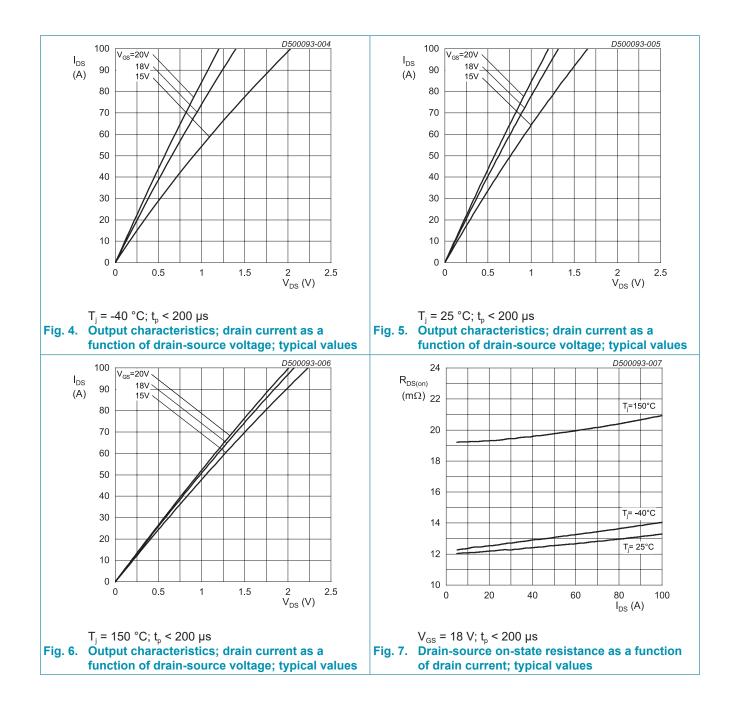
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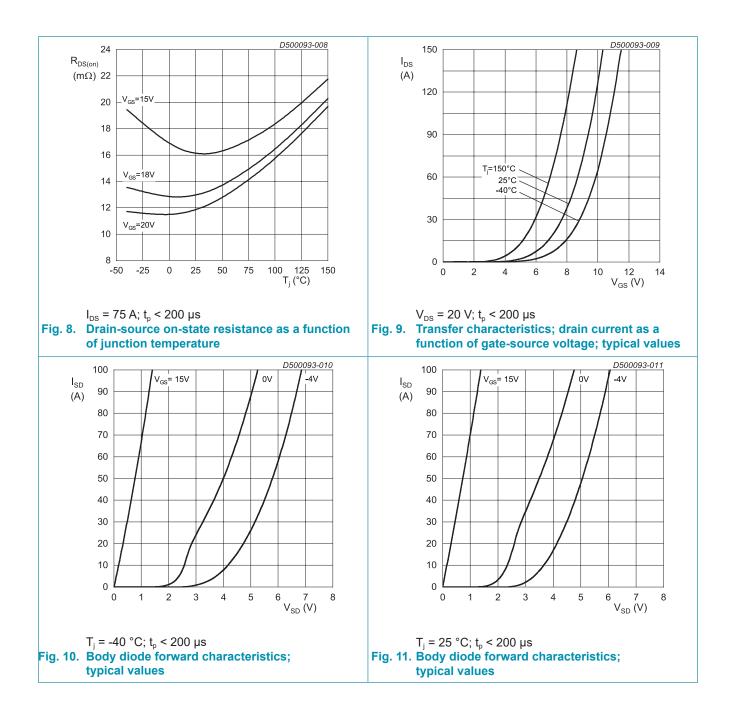
10. Characteristics

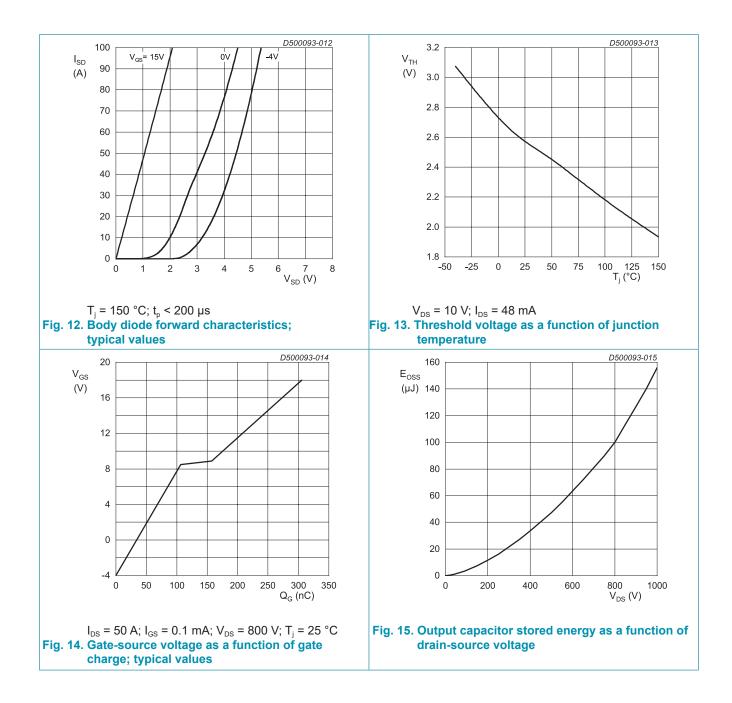
Table 7. Characteristics

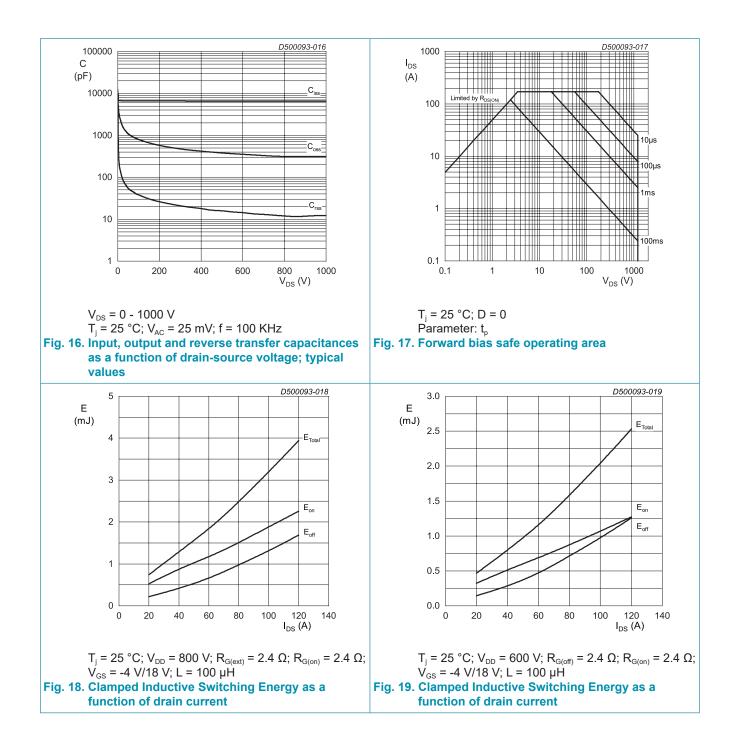
MOSFET							
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	I_{D} = 200 µA; V_{GS} = 0 V; T_{j} = 25 °C		1200	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	I_{D} = 24 mA; V_{DS} = 10 V; T_{j} = 25 °C		1.9	2.5	3.5	V
I _{DSS}	drain leakage current	V _{DS} = 1200 V; V _{GS} = 0 V; T _j = 25 °C		-	0.4	200	μA
I _{GSS}	gate leakage current	$V_{GS} = 24 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	20	200	nA
	(absolute value)	V _{GS} = -12 V; V _{DS} = 0 V; T _j = 25 °C		-	20	200	nA
$R_{\text{DS(on)}}$	drain-source on-state	V _{GS} = 15 V; I _D = 75 A; T _j = 25 °C		-	16	-	mΩ
	resistance	V _{GS} = 18 V; I _D = 75 A; T _j = 25 °C		-	12.9	-	mΩ
		V _{GS} = 18 V; I _D = 75 A; T _j = 125 °C		-	18.3	-	mΩ
		V _{GS} = 18 V; I _D = 75 A; T _j = 150 °C		-	20.3	-	mΩ
R_{G}	gate resistance, each side	f = 1 MHz; T _j = 25 °C, each die with 4.7 Ω R _{G(ext)} in series		-	2.7	-	Ω
g _{fs}	transconductance	V _{DS} = 20 V; I _D = 75 A; T _j = 25 °C		-	48	-	S
Dynamic	characteristics	·					
Q _{G(tot)}	total gate charge	$I_{D} = 50 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	300	-	nC
Q _{GS}	gate-source charge	T _j = 25 °C		-	107	-	nC
Q_{GD}	gate-drain charge			-	51	-	nC
C _{iss}	input capacitance	V _{DS} = 1000 V; V _{GS} = 0 V; f = 100 KHz;		-	6.5	-	nF
C _{oss}	output capacitance	T _j = 25 °C		-	312	-	pF
C_{rss}	reverse transfer capacitance			-	12.3	-	pF
E _{oss}	Coss stored energy			-	156	-	μJ
t _{d(on)}	turn-on delay time	V _{DS} = 800 V; V _{GS} = -4 V/18 V;		-	24	-	ns
t _r	rise time	R _{G(ext)} = 2.4 Ω; I _D = 75 A; L = 100 μH; T _i = 25 °C		-	13	-	ns
t _{d(off)}	turn-off delay time			-	81	-	ns
t _f	fall time			-	25	-	ns
E _{on}	turn-on energy			-	1.41	-	mJ
E _{off}	turn-off energy			-	0.89	-	mJ

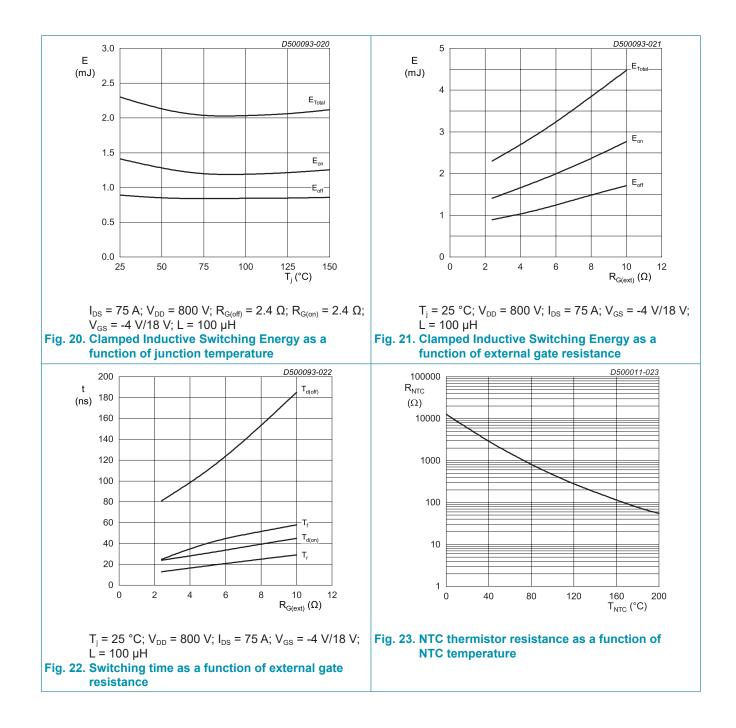
Body did	de						
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics	·					
V_{SD}	source-drain voltage	V_{GS} = -4 V; I_{SD} = 75 A; T_j = 25 °C		-	5.6	-	V
		V _{GS} = -4 V; I _{SD} = 75 A; T _j = 150 °C		-	5.0	-	V
Dynamic	characteristics	· · · · · · · · · · · · · · · · · · ·					
t _{rr}	reverse recovery time	I_{SD} = 80 A; V_{GS} = -4 V; di/dt = 6000 A/µs;		-	20	-	ns
Q _r	recovered charge	V _R = 600 V; T _j = 25 °C		-	828	-	nC
I _{rrm}	reverse recovery current			-	68	-	A
E _{rec}	reverse recovery energy			-	350	-	μJ
t _{rr}	reverse recovery time	I_{SD} = 80 A; V_{GS} = -4 V; di/dt = 9000 A/µs;		-	26	-	ns
Q _r	recovered charge	V _R = 600 V; T _j = 150 °C		-	2046	-	nC
l _{rrm}	reverse recovery current			-	122	-	А
E _{rec}	reverse recovery energy			-	1178	-	μJ
NTC the	mistor						
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R ₂₅	Rated resistance	T _{NTC} = 25 °C		-	5000	-	Ω
R ₁₀₀		T _{NTC} = 100 °C		465±5%		Ω	
B _{25/50}	B-value	$R_2 = R_{25} \exp[B_{25/50}(1/T_2 - 1/(298.15K))]$		3380			К
	Maximum operating temperature			-	200	-	°C
	Dissipation costant			-	2	-	mW/k
	Thermal time constant			-	≤10	-	s





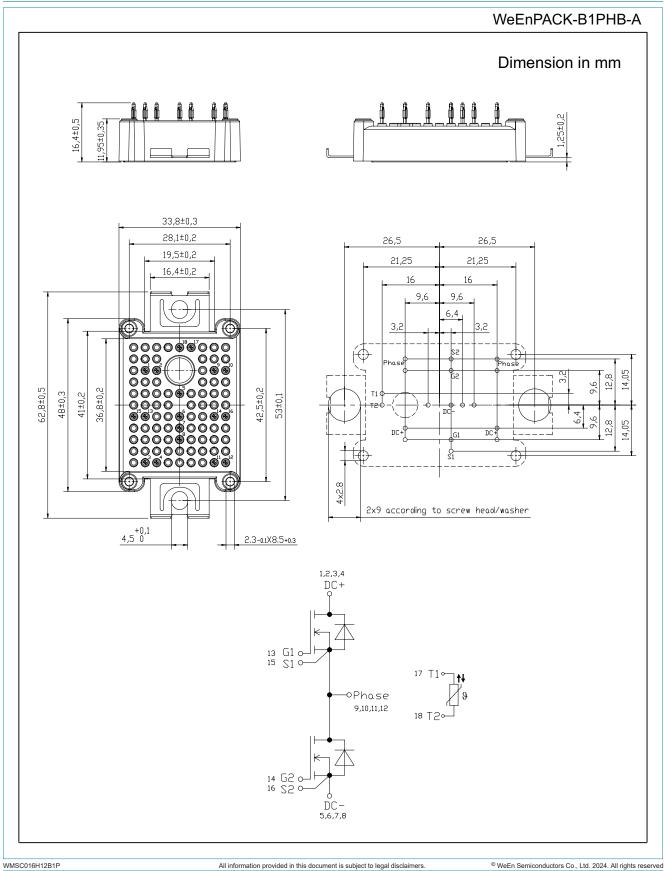






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11. Package outline



Product data sheet

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12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.ween-semi.com</u>.

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