**Product data sheet** 

# 1. General description

WSJM65R044DW is a high voltage N-channel MOSFET in TO247 package, which utilizes the advanced super-junction technology to provide superior FOM  $R_{\rm DS(on)}{}^{*}$   $Q_{\rm g}$  among silicon based MOSFETs. It is particularly suitable for applications require extreme high efficiency and power density.



### 2. Features and benefits

- Superior FOM  $R_{DS(on)} * Q_g$
- Extremely low switching loss
- Integrated ultrafast body diode
- 100% avalanche tested

## 3. Applications

- EV charger
- · High efficiency power supplies
- · On board charger
- Inverters

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit	
Absolute maximum rating								
V <sub>DS</sub>	drain-source voltage				650		V	
$V_{GS}$	gate-source voltage				±30		V	
I <sub>D</sub>	continuous drain current	T <sub>mb</sub> = 25 °C			67		А	
P <sub>tot</sub>	power dissipation	T <sub>mb</sub> = 25 °C			480		W	
T <sub>j</sub>	junction temperature			-55 to 150		°C		
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit	
Static cha	aracteristics							
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = 10 \text{ V}, I_{D} = 34 \text{ A}$		-	36	44	mΩ	
Dynamic characteristics								
Q <sub>G(tot)</sub>	total gate charge	$I_D = 34 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = 10 \text{ V}$		-	123	-	nC	
E <sub>oss</sub>	coss stored erergy	$V_{GS} = 0 \text{ V}; V_{DS} = 0 \text{ to } 400 \text{ V}$		-	14.5	-	μJ	

# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		ID.
2	D	drain	ЩОЩ	D
3	S	source		G ( F )
mb	D	mounting base; connected to drain	1 2 3	sym302 S

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WSJM65R044DW	TO247	WSJM65R044DWQ	Tube	30	TO247N	20-July-2016

# 7. Marking

#### **Table 4. Marking codes**

Type number	Marking codes
WSJM65R044DW	WSJM 65R044DW

# 8. Limiting values

### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V <sub>DS</sub>	drain-source voltage			650	V
V <sub>GS</sub>	gate-source voltage			±30	V
I <sub>D</sub>	continuous drain current	T <sub>mb</sub> = 25 °C		67	Α
		T <sub>mb</sub> = 100 °C		42	Α
I <sub>DM</sub>	pulsed drain current	T <sub>mb</sub> = 25 °C		268	Α
P <sub>tot</sub>	power dissipation	T <sub>mb</sub> = 25 °C		480	W
E <sub>AS</sub>	single pulse drain-to- source avalanche	$I_{AS} = 7.6 \text{ A}; R_{GS} = 25 \Omega; V_{DD} = 50 \text{ V};$ $T_j = 25 \text{ °C}$		288	mJ
E <sub>AR</sub>	repetitive avalanche energy	$I_{AS} = 7.6 \text{ A}; R_{GS} = 25 \Omega; V_{DD} = 50 \text{ V};$ $T_j = 25 \text{ °C}$		1.01	mJ
I <sub>AS</sub>	avalanche current, single pulse			7.6	А
dv/dt	MOSFET dv/dt ruggedness			72	V/ns
dv/dt	reverse diode dv/dt			50	V/ns
dl <sub>F</sub> /dt	maximum diode commutation speed			850	A/µs
T <sub>stg</sub>	storage temperature			-55 to 150	°C
T <sub>j</sub>	junction temperature			-55 to 150	°C

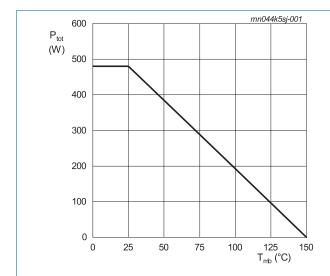


Fig. 1. Normalized total power dissipation as a function of mounting base temperature

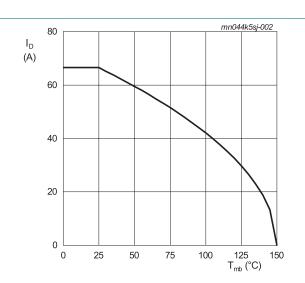


Fig. 2. Continuous Drain Current as a function of mounting base temperature

## 9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base			-	0.2	0.26	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air		-	45	-	K/W

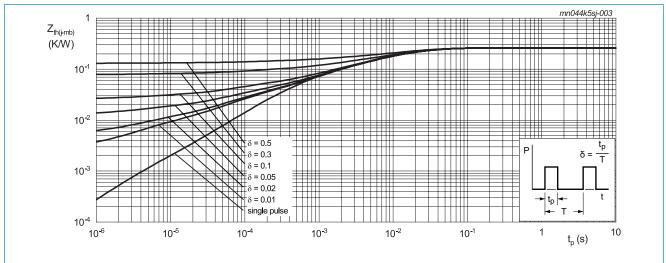


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

## 10. Characteristics

### **Table 7. Characteristics**

T<sub>i</sub> = 25 °C unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 1 \text{ mA}; V_{GS} = 0 \text{ V}$		650	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 250 \ \mu A; \ V_{DS} = V_{GS}$		3	-	5	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}$		-	-	10	μA
		$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 ^{\circ}\text{C}$		-	200	-	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS} = \pm 30 \text{ V}; V_{DS} = 0 \text{ V}$		-	-	±500	nA
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 34 \text{ A}$		-	36	44	mΩ
$R_G$	gate resistance	f = 1 MHz		-	32	-	Ω
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge	$I_D = 34 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = 10 \text{ V}$		-	123	-	nC
$Q_{GS}$	gate-source charge			-	40	-	nC
$Q_{GD}$	gate-drain charge			-	50	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 400 V; V <sub>GS</sub> = 0 V; f = 250 kHz		-	6148	-	pF
C <sub>oss</sub>	output capacitance			-	91	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	0.6	-	pF
C <sub>o(er)</sub>	effective output capacitance, energy related	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 0 to 400 V		-	181	-	pF
C <sub>o(tr)</sub>	effective output capacitance, time related			-	1598	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = 400 \text{ V}; V_{GS} = 10 \text{ V}; R_G = 2 \Omega;$		-	295	-	ns
t <sub>r</sub>	rise time	I <sub>D</sub> = 34 A		-	25	-	ns
$t_{d(off)}$	turn-off delay time			-	433	-	ns
t <sub>f</sub>	fall time			-	10	-	ns
Source-d	rain diode						
$V_{SD}$	source-drain voltage	V <sub>GS</sub> = 0 V; I <sub>S</sub> = 34 A		-	0.94	1.2	V
I <sub>s</sub>	body-diode continuous current	T <sub>mb</sub> = 25 °C		-	-	67	А
t <sub>rr</sub>	reverse recovery time	$V_R = 400 \text{ V}; I_F = 34 \text{ A}; dI_F/dt = 100 \text{ A/}\mu\text{s}$		-	184	-	ns
Q <sub>rr</sub>	reverse recovered charge			-	1.6	-	μC
I <sub>rrm</sub>	reverse recovery current			-	18	-	Α

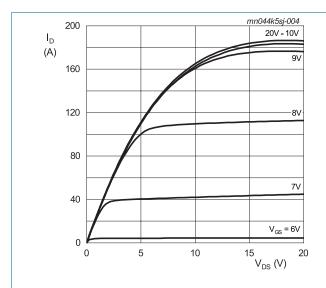
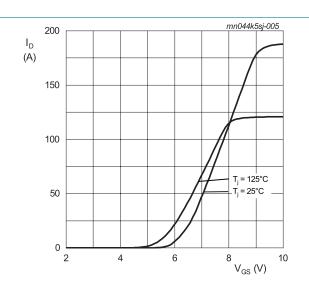
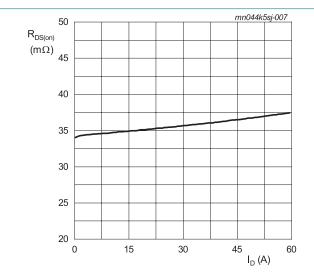


Fig. 4. Drain current as a function of drain-source voltage; typical values



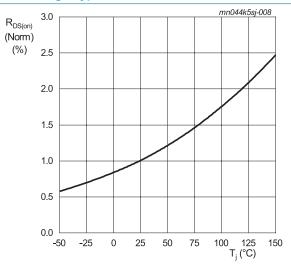
V<sub>DS</sub> = 20 V

Fig. 5. Drain current as a function of gate-source voltage; typical values



V<sub>GS</sub> = 10 V

Fig. 6. Drain-source on-state resistance as a function of drain current; typical values



V<sub>GS</sub> = 10 V; I<sub>D</sub> = 34 A

Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature

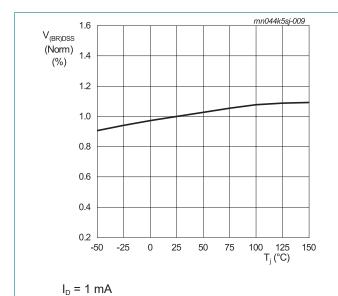
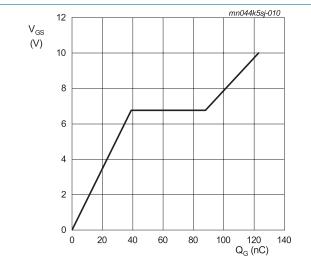
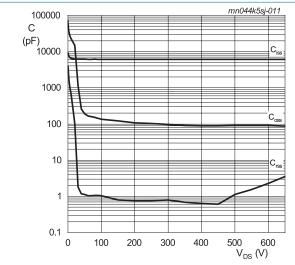


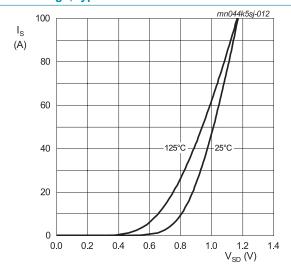
Fig. 8. Normalized drain-source breakdown voltage as a function of junction temperature



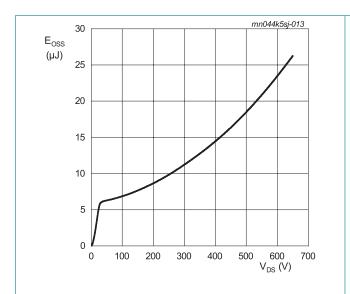
I<sub>D</sub> = 34 A; V<sub>DS</sub> = 400 V Fig. 9. Gate-source voltage as a function of gate charge; typical values



V<sub>GS</sub> = 0 V; f = 250 kHz Fig 10. Capacitances as a function of drain-source voltage; typical values



V<sub>GS</sub> = 0 V Fig 11. Source current as a function of source-drain voltage; typical values





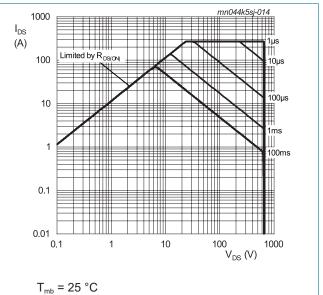
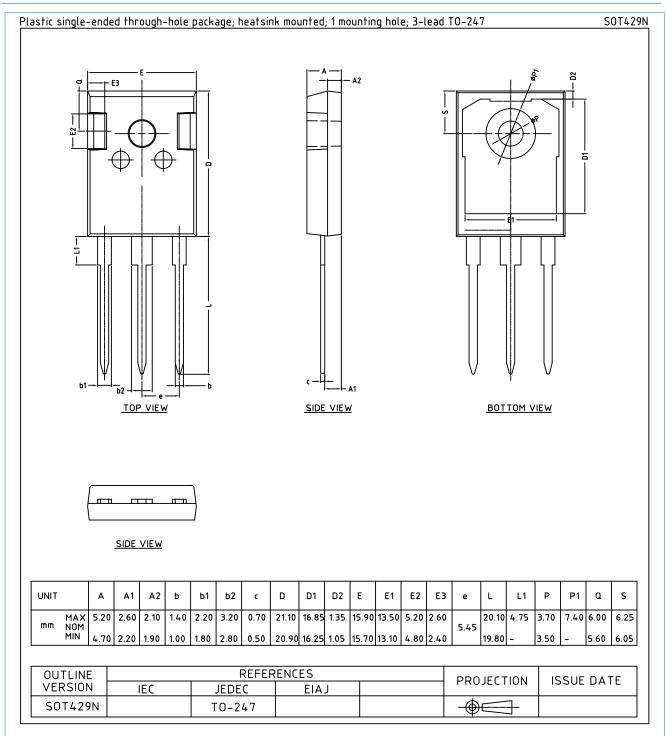


Fig. 13. Safe operating area

# 11. Package outline



## 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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