**Product data sheet** 

## 1. General description

Silicon Carbide MOSFET in a TO247-4L plastic package, designed for high frequency, high efficiency systems.



## 2. Features and benefits

- · Separate driver source pin
- · Low on-resistance
- Fast switching speed
- 0V turn-off gate voltage for simple gate drive
- 100% UIS Tested
- Easy to parallel
- Controllable dV/dt for optimized EMI
- Reduced cooling requirements
- RoHS compliant

## 3. Applications

- · Switch Mode Power Supplies
- UPS
- · Solar string inverter and solar optimizer
- EV Charger
- Motor Drives

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute	maximum rating						
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C			1200		V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C			61.1		Α
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C			417		W
T <sub>j</sub>	junction temperature				-55 to 17	75	°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$R_{\text{DS(on)}}$	drain-source on-state resistance	$V_{GS} = 15 \text{ V}; I_D = 25 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	60	-	mΩ
Dynamic	characteristics				,		
Q <sub>G(tot)</sub>	total gate charge	$I_D = 25 \text{ A}$ ; $V_{DS} = 800 \text{ V}$ ; $V_{GS} = -4 \text{ V}/18 \text{ V}$ ;		-	83	-	nC
$Q_{GD}$	gate-drain charge	T <sub>j</sub> = 25 °C		-	15	-	nC
Source-d	rain diode						
$Q_r$	recovered charge	$I_{SD}$ = 25 A; di/dt = 500 A/ $\mu$ s; $V_{DS}$ = 400 V; $T_{j}$ = 25 °C		-	64	-	nC

# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drian		D
2	S	source		
3	SS	source sense		$G \longrightarrow A$
4	G	gate		SS
mb	D	mounting base; connected to drain		, and the second

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSC2M60120R	TO247-4L	WNSC2M60120R6Q	Tube	30	TO247N-4L	17-Dec-2021

# 7. Marking

### **Table 4. Marking codes**

Type number	Marking codes
WNSC2M60120R	WNSC2M 60120R

# 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C		1200	V
$V_{\rm GS,max}$	gate-source voltage			-12 to 24	V
$V_{GS,op}$	gate-source voltage			-4 to 18	V
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C		417	W
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C		61.1	А
		V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 100 °C		43.2	А
I <sub>DM</sub>	peak drain current	pulse width t <sub>p</sub> limited by T <sub>jmax</sub>	Fig.17	120	А
Is	continuous diode current	V <sub>GS</sub> = -4 V; T <sub>mb</sub> = 25 °C		52.8	А
I <sub>SM</sub>	pulse diode current	$V_{GS}$ = -4 V; pulse width $t_p$ limited by $T_{jmax}$		120	А
E <sub>as</sub>	single pulse drain-to- source avalanche	$I_{AS}$ = 15 A; L = 1 mH; $V_{DD}$ = 100 V; $T_j$ = 25 °C		112.5	mJ
T <sub>stg</sub>	storage temperature			-55 to 175	°C
T <sub>j</sub>	junction temperature			-55 to 175	°C
$T_{sld(M)}$	peak soldering temperature			260	°C

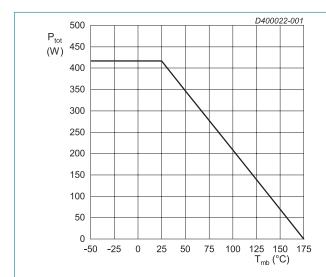


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

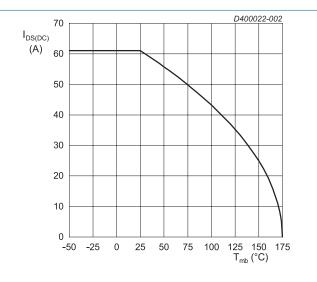


Fig. 2. Continuous Drain Current as a function of mounting base temperature

## 9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base			-	0.36	-	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air		-	40	-	K/W
M <sub>d</sub>	Mounting torque	M3 or 6 - 32 screw		-	-	0.6	Nm

Note: It is recommended that a metal washer is inserted between screw head and mounting tab.

Do not use self-tapping screws.

Device is ESD sensitive. Handling precautions are recommanded.

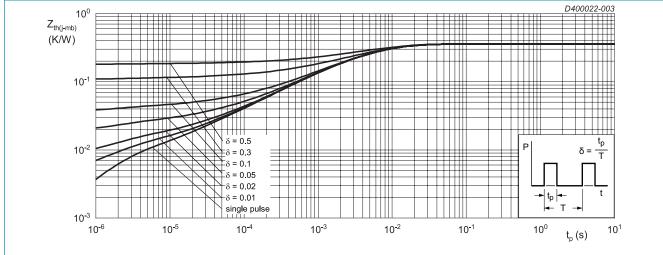
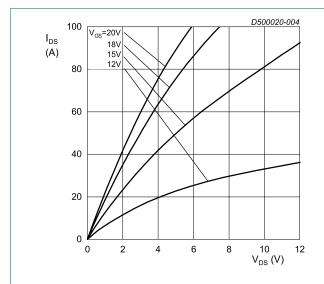


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

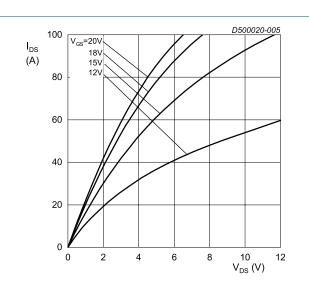
## 10. Characteristics

**Table 7. Characteristics** 

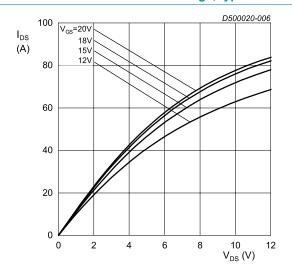
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_j = 25 °C$		1200	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold	$I_D = 6 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 25 \text{ °C}$		1.9	2.6	3.5	V
	voltage	$I_D = 6 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 175 °C$		-	1.9	-	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 1200 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	0.2	100	μΑ
		$V_{DS} = 1200 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$		-	2	-	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS} = 24 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	10	100	nA
		$V_{GS} = -12 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	10	100	nA
R <sub>DS(on)</sub>	drain-source on-state	$V_{GS} = 15 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$		-	60	-	mΩ
	resistance	$V_{GS} = 18 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$		-	49	68	mΩ
		$V_{GS} = 18 \text{ V}; I_D = 25 \text{ A}; T_j = 175 \text{ °C}$		-	93	-	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C		-	2.2	-	Ω
g <sub>fs</sub>	transconductance	$V_{DS} = 20 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$		-	14	-	S
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	83	-	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C		-	31	-	nC
$Q_{GD}$	gate-drain charge			-	15	-	nC
C <sub>iss</sub>	input capacitance	$V_{DS} = 1000 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$		-	1731	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C		-	71	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	7	-	pF
E <sub>oss</sub>	Coss stored energy			-	35.5	-	μJ
t <sub>d(on)</sub>	turn-on delay time	$V_{DS} = 800 \text{ V; } V_{GS} = -4 \text{ V/18 V; } R_{G(ext)} = 5.1$		-	6	-	ns
t <sub>r</sub>	rise time	$Ω$ ; $I_D = 25 A$ ; $L = 330 \mu H$ ; $T_j = 25 °C$		-	11	-	ns
$t_{\text{d(off)}}$	turn-off delay time			-	23	-	ns
t <sub>f</sub>	fall time			-	18	-	ns
E <sub>on</sub>	turn-on energy (SiC Diode FWD)		Fig.20	-	224	-	μJ
E <sub>off</sub>	turn-off energy (SiC Diode FWD)		Fig.20	-	95	-	μJ
E <sub>on</sub>	turn-on energy (Body Diode FWD)		Fig.20	-	302	-	μJ
E <sub>off</sub>	turn-off energy (Body Diode FWD)		Fig.20	-	113	-	μJ
Source-di	ain diode					<u>'</u>	-
V <sub>SD</sub>	source-drain voltage	$V_{GS} = 0 \text{ V}; I_{SD} = 12.5 \text{ A}; T_j = 25 \text{ °C}$		-	3.4	-	V
		V <sub>GS</sub> = -4 V; I <sub>SD</sub> = 12.5 A; T <sub>j</sub> = 25 °C		-	5.1	-	V
		$V_{GS} = -4 \text{ V}; I_{SD} = 12.5 \text{ A}; T_j = 175 \text{ °C}$		-	4.4	-	V
t <sub>rr</sub>	reverse recovery time	$I_{SD} = 25 \text{ A}$ ; di/dt = 500 A/ $\mu$ s; $V_{DS} = 400 \text{ V}$ ;		-	23.8	-	ns
$Q_r$	recovered charge	T <sub>j</sub> = 25 °C		-	64	-	nC
I <sub>rrm</sub>	reverse recovery current			-	4.7	-	Α



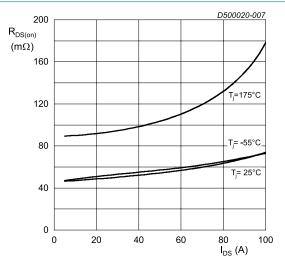
T<sub>j</sub> = -55 °C; t<sub>p</sub> < 200 μs Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values



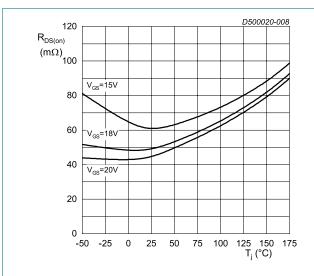
T<sub>j</sub> = 25 °C; t<sub>p</sub> < 200 μs Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



 $T_{j}=175~^{\circ}C;~t_{p}<200~\mu s$  Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

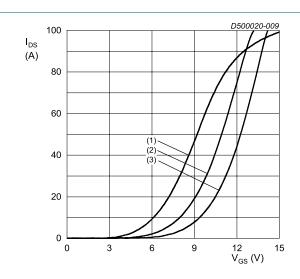


 $V_{GS}$  = 18 V;  $t_p$  < 200 µs Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



 $I_{DS}$  = 25 A;  $t_p$  < 200  $\mu s$ 

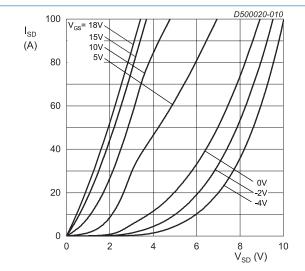
Fig. 8. Drain-source on-state resistance as a function of junction temperature



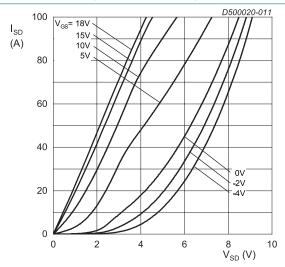
 $V_{DS} = 20 \text{ V; } t_p < 200 \text{ } \mu \text{s}$ (1)  $T_j = 175 \text{ }^{\circ}\text{C}$ (2)  $T_j = 25 \text{ }^{\circ}\text{C}$ 

(3)  $T_i = -55 \,^{\circ}C$ 

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values

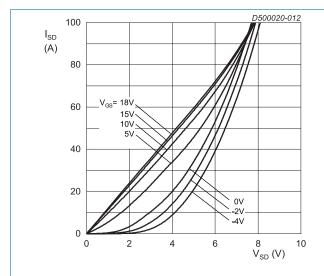


 $T_{j} = -55 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$ Fig. 10. Body diode forward characteristics; typical values

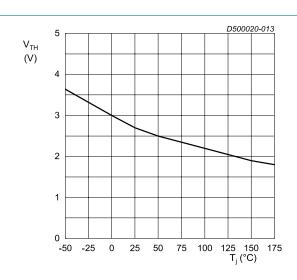


 $T_{j} = 25 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$ 

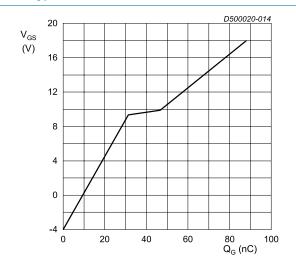
Fig. 11. Body diode forward characteristics; typical values



 $T_j$  = 175 °C;  $t_p$  < 200 µs Fig. 12. Body diode forward characteristics; typical values



V<sub>DS</sub> = 10 V; I<sub>DS</sub> = 6 mA Fig. 13. Threshold voltage as a function of junction temperature



I<sub>DS</sub> = 25 A; I<sub>GS</sub> = 0.1 mA; V<sub>DS</sub> = 800 V; T<sub>j</sub> = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values

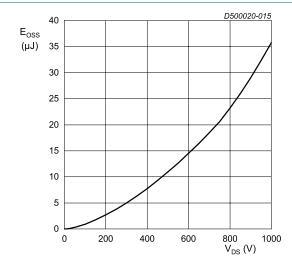
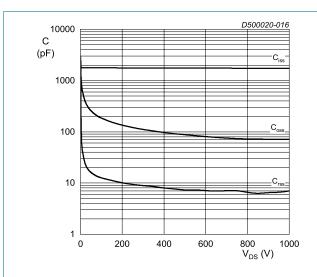
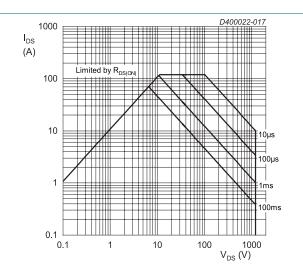


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



 $V_{DS} = 0 - 1000 \text{ V}$ 

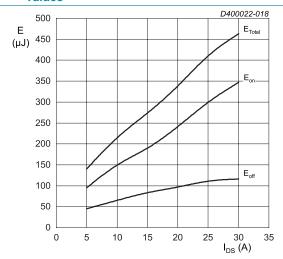
T<sub>j</sub> = 25 °C; V<sub>AC</sub> = 25 mV; f = 1 MHz



 $T_j = 25$  °C; D = 0 Parameter:  $t_p$ 

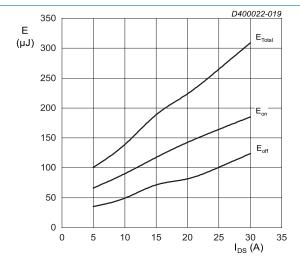
Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values





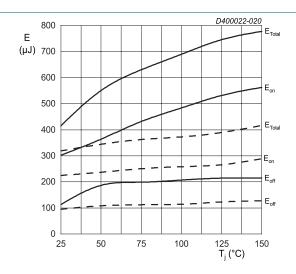
 $T_{\rm j}$  = 25 °C;  $V_{\rm DD}$  = 800 V;  $R_{\rm G(ext)}$  = 5.1 Ω;  $V_{\rm GS}$  = -4 V/18 V; L = 330 μH FWD = WNSC2M60120R

Fig. 18. Clamped Inductive Switching Energy as a function of drain current



 $T_{j}$  = 25 °C;  $V_{DD}$  = 600 V;  $R_{G(ext)}$  = 5.1 Ω;  $V_{GS}$  = -4 V/18 V; L = 330 μH FWD = WNSC2M60120R

Fig. 19. Clamped Inductive Switching Energy as a function of drain current

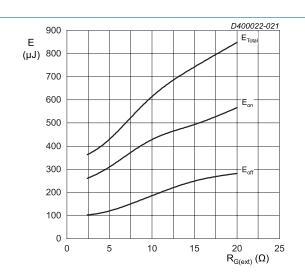


 $I_{DS}=25$  A;  $V_{DD}=800$  V;  $R_{G(ext)}=5.1~\Omega;$   $V_{GS}=-4$  V/18 V;  $L=330~\mu H$ 

FWD = WNSC2M60120R

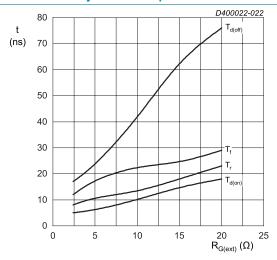
FWD = WNSC2D201200(- - -)

Fig. 20. Clamped Inductive Switching Energy as a function of junction temperature



 $T_{\rm j}$  = 25 °C;  $V_{\rm DD}$  = 800 V;  $I_{\rm DS}$  = 25 A;  $V_{\rm GS}$  = -4 V/18 V FWD = WNSC2M60120R; L = 330  $\mu H$ 

Fig. 21. Clamped Inductive Switching Energy as a function of external gate resistance



 $T_{j}$  = 25 °C;  $V_{DD}$  = 800 V;  $I_{DS}$  = 25 A;  $V_{GS}$  = -4 V/18 V FWD = WNSC2M60120R; L = 330  $\mu H$ 

Fig. 22. Switching time as a function of external gate resistance

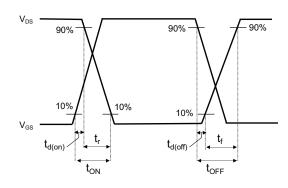
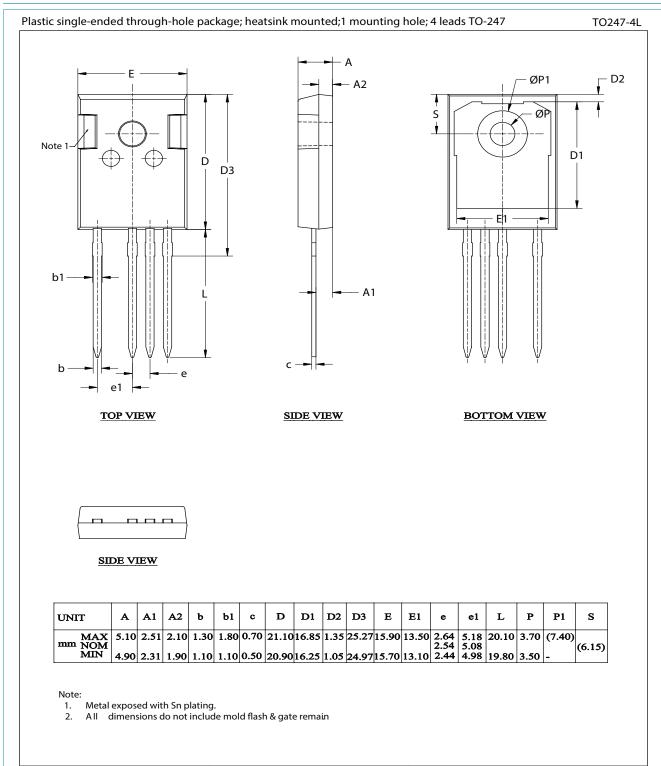


Fig. 23. Switching time definition

# 11. Package outline



## 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 22 November 2023

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