

WTMH116T16R

SCR Module Rev.01 - 31 August 2023

Product data sheet

1. General description

Planar passivated Silicon Controlled Rectifier (SCR) module in TO-240AA for use in applications requiring high blocking voltage capability, high inrush current capability and high thermal cycling performance.

2. Features and benefits

- High blocking voltage capability
- High thermal cycling performance
- · Planar passivated for voltage ruggedness and reliability
- Package meets UL certification
- Package is RoHS compliant
- Industry standard outline
- Soldering pins for PCB mounting
- Copper base plate
- Cathode Kelvin contacts provided
- UL1557 certified (Document number E346397)

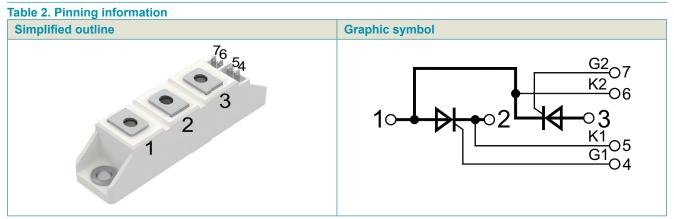
3. Applications

- Softstart AC motor control
- DC Motor control
- AC power control
- Power converter
- Temperature control
- Lighting control

4. Quick reference data

Table 1. Q	uick reference data						
Symbol	Parameter	Conditions	Notes		Values		Unit
Absolute	maximum rating						
V_{DRM}	repetitive peak forward voltage				1600		V
V_{RRM}	repetitive peak reverse voltage				1600		V
I _{T(RMS)}	RMS on-state current	half sine wave			182		А
I _{TSM}	non-repetitive peak on- state current	half sine wave; T _{j(init)} = 25 °C; t _p = 10 ms		2300			А
		half sine wave; T _{j(init)} = 130 °C; t _p = 10 ms			2000		А
		half sine wave; T _{j(init)} = 25 °C; t _p = 8.3 ms			2530		А
		half sine wave; T _{j(init)} = 130 °C; t _p = 8.3 ms		2200			А
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics						
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C		30	-	100	mA
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C		-	0.75	1.2	V
V _T	on-state voltage	I _T = 116 A; T _j = 25 °C		-	-	1.29	V

5. Pinning information



6. Ordering information

able 3. Ordering information								
Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date		
WTMH116T16R	TO-240AA	WTMH116T16RT	Tray	12	WeEnPACK- 20mmPHB-C	30-Jun-2023		

7. Marking

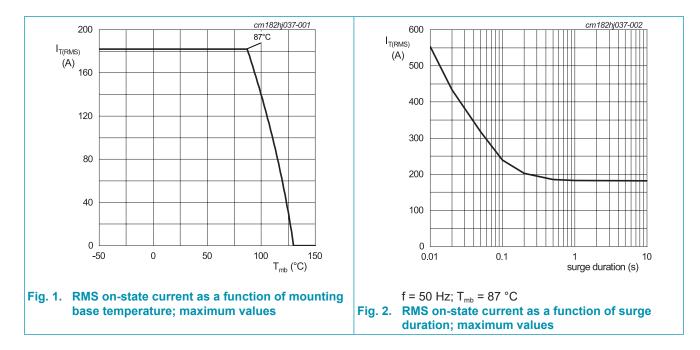
Table 4. Marking codes						
	Type number	Marking codes				
	WTMH116T16R	WTMH116T16R				

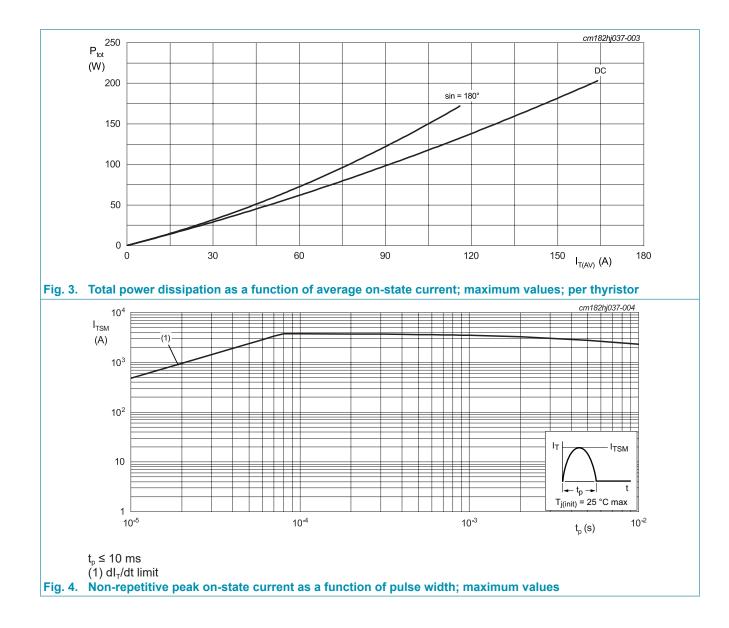
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V _{drm}	repetitive peak forward voltage			1600	V
V _{RRM}	repetitive peak reverse voltage			1600	V
I _{T(AV)}	average on-state current	half sine wave		116	А
I _{T(RMS)}	RMS on-state current	half sine wave		182	А
I _{TSM}	non-repetitive peak on-state	half sine wave; $T_{j(init)}$ = 25 °C; t_p = 10 ms		2300	А
	current	half sine wave; $T_{j(init)}$ = 130 °C; t_p = 10 ms		2000	А
		half sine wave; $T_{j(init)}$ = 25 °C; t_p = 8.3 ms		2530	А
		half sine wave; $T_{j(init)}$ = 130 °C; t_p = 8.3 ms		2200	А
l²t	l ² t for fusing	t_p = 10 ms; sine-wave pulse		26.4	kA²s
dl _⊤ /dt	rate of rise of on-state current	I _G = 200 mA; T _j = 130 °C		200	A/µs
I _{GM}	peak gate current			10	А
V _{RGM}	peak reverse gate voltage			5	V
P _{GM}	peak gate power			20	W
P _{G(AV)}	average gate power	over any 20 ms period		0.5	W
T _{stg}	storage temperature			-40 to 130	°C
Tj	junction temperature			-40 to 130	°C





9. Thermal characteristics

Table 6. Thermal characteristics	Table	6.	Thermal	characteristics
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-c)}	R _{th(j-c)} thermal resistance from junction to case	per thyristor	-	-	0.25	K/W
		per module	-	-	0.125	K/W
R _{th(j-h)}	thermal resistance	per thyristor	-	-	0.48	K/W
	from junction to heatsink	per module	-	-	0.24	K/W

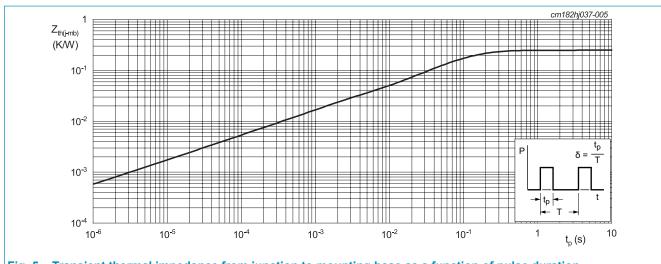


Fig. 5. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Package characteristics

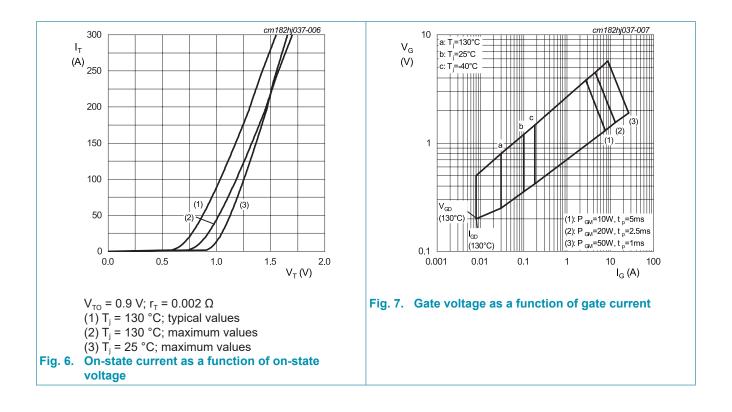
Table 7. Isolation characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{isol}	isolation voltage	50/60 Hz; RMS; I _{ISOL} ≤ 1 mA; t = 1 second; AC	-	-	3600	V
		50/60 Hz; RMS; $I_{ISOL} \le 1$ mA; t = 1 minute; AC	-	-	3000	V

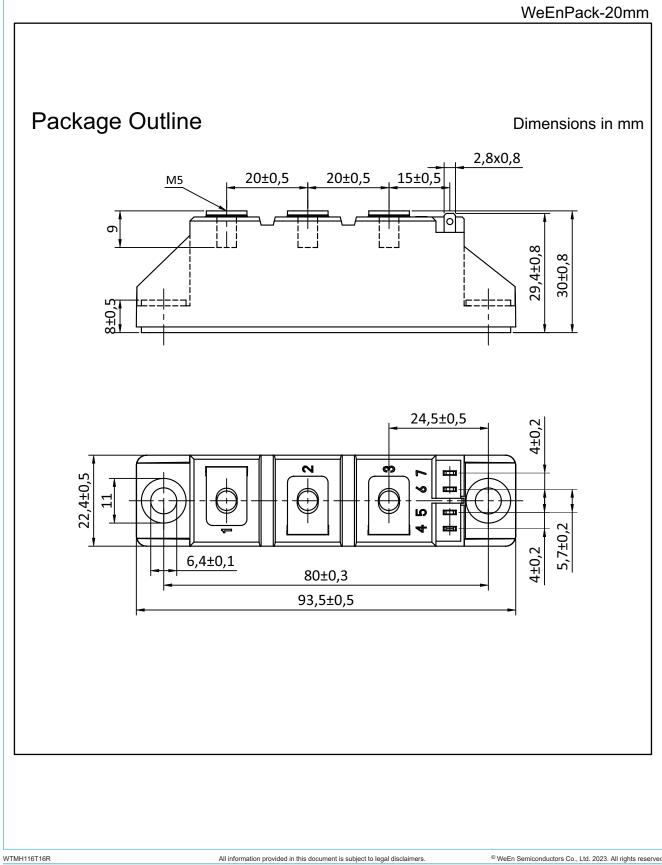
11. Characteristics

able 8. C	haracteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
I _{GT}	gate trigger current	$V_{\rm D}$ = 12 V; I _T = 0.1 A; T _j = 25 °C	30	-	100	mA
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C	-	0.75	1.2	V
		$V_{\rm D} = 2/3 V_{\rm DRM}; I_{\rm T} = 0.1 \text{ A}; T_{\rm j} = 130 \text{ °C}$	0.25	0.4	-	V
I _{GD}	gate non-trigger current	T _j = 130 °C	-	-	8	mA
V _{GD}	gate non-trigger voltage	T _j = 130 °C	-	-	0.2	V
I _L	latching current	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C	-	-	300	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C	-	-	200	mA
V _T	on-state voltage	I _T = 116 A; T _j = 25 °C	-	-	1.29	V
		I _T = 300 A; T _j = 25 °C	-	-	1.65	V
V _{TO}	threshold voltage	T _j = 130 °C	-	-	0.9	V
r _T	slope resistance	T _j = 130 °C	-	-	2.0	mΩ
I _D off-state current		V _D = 1600 V; T _j = 25 °C	-	-	100	μA
		V _D = 1600 V; T _j = 130 °C	-	-	10	mA
I _R	reverse current	V _R = 1600 V; T _j = 25 °C	-	-	100	μA
		V _R = 1600 V; T _j = 130 °C	-	-	10	mA
Dynamic	characteristics	· · · · ·		1		
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 1072 V; T _j = 130 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit	1500	-	-	V/µs
t _{gt}	gate-controlled turn-on time	$I_{TM} = 40 \text{ A}; V_D = 800 \text{ V}; I_G = 100 \text{ mA};$ $(dI_G/dt)_M = 1 \text{ A}/\mu\text{s}; T_i = 25 \text{ °C}$	-	2	-	μs

WTMH116T16R SCR Module



12. Package outline



WTMH116T16R SCR Module

13. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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