Product data sheet

1. General description

Silicon Carbide MOSFET in a 3-lead TO247 plastic package, designed for high frequency, high efficiency systems.





2. Features and benefits

- · Low on-resistance
- Fast switching speed
- · 0V turn-off gate voltage for simple gate drive
- 100% UIS Tested
- Easy to parallel
- Controllable dV/dt for optimized EMI
- Reduced cooling requirements
- RoHS compliant

3. Applications

- · Switch Mode Power Supplies
- UPS
- Solar string inverter and solar optimizer
- EV Charger
- Motor Drives

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit	
Absolute maximum rating								
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		-	-	1200	V	
I _D	drain current	V _{GS} = 20 V; T _{mb} = 25 °C		-	-	24	Α	
P _{tot}	total power dissipation	T _{mb} = 25 °C		-	-	155	W	
T _j	junction temperature			-55	-	175	°C	
Static characteristics								
$R_{\text{DS(on)}}$	drain-source on-state resistance	$V_{GS} = 20 \text{ V}; I_D = 10 \text{ A}; T_j = 25 \text{ °C}$		-	160	196	mΩ	
Dynamic characteristics								
Q _{G(tot)}	total gate charge	$I_D = 10 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = 0 \text{V}/20 \text{ V};$		-	35	-	nC	
Q_{GD}	gate-drain charge	T _j = 25 °C		-	8	-	nC	
Source-d	Source-drain diode							
Q _r	recovered charge	I_{SD} = 10 A; di/dt = 500 A/µs; V_{DS} = 400 V; T_{j} = 25 °C		-	78	-	nC	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	D	drain		
3	S	source]	$G \longrightarrow G$
mb	D	mounting base; connected to drain	1 2 3	sym300 S

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSCM160120W	TO247	WNSCM160120W6Q	Tube	30	SOT429	25-Mar-2013

7. Marking

Table 4. Marking codes

Type number	Marking codes
WNSCM160120W	WNSCM 160120W

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Min	Max	Unit
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		-	1200	V
$V_{\rm GS,max}$	gate-source voltage			-10	25	V
$V_{GS,op}$	gate-source voltage			-5	20	V
P _{tot}	total power dissipation	T _{mb} = 25 °C		-	155	W
I _D	drain current	V _{GS} = 20 V; T _{mb} = 25 °C		-	24	Α
		V _{GS} = 20 V; T _{mb} = 100 °C		-	17	А
I_{DM}	peak drain current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 ^{\circ}C$		-	34	Α
E _{as}	single pulse drain-to- source avalanche	$I_{AS} = 13 \text{ A; } L = 1 \text{ mH; } V_{DD} = 100 \text{ V,}$ $T_{j(init)} = 25 \text{ °C}$		85	-	mJ
T _{stg}	storage temperature			-55	175	°C
T _j	junction temperature			-55	175	°C
$T_{sld(M)}$	peak soldering temperature			-	260	°C

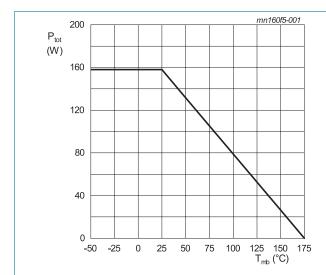


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

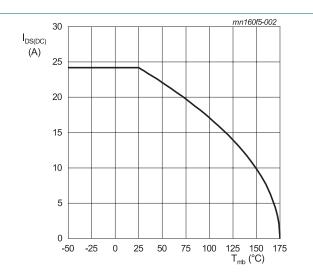


Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base			-	-	0.95	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air		-	40	-	K/W
M _d	Mounting torque	M3 or 6 - 32 screw		-	-	0.6	Nm

Note: It is recommended that a metal washer is inserted between screw head and mounting tab.

Do not use self-tapping screws.

Device is ESD sensitive. Handling precautions are recommanded.

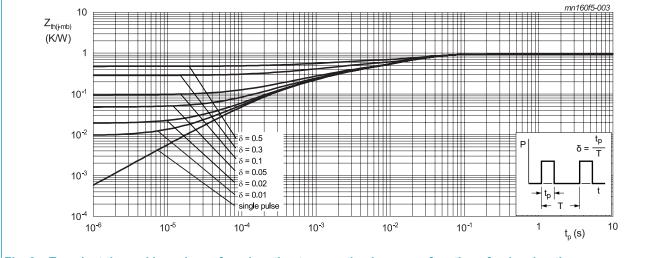


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_j = 25 °C$		1200	-	-	V
$V_{GS(th)}$	gate-source threshold	$I_D = 3 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 25 \text{ °C}$		2.5	3.5	4.5	V
	voltage	$I_D = 3 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 150 \text{ °C}$		-	2.5	-	V
I _{DSS}	drain leakage current	V _{DS} = 1200 V; V _{GS} = 0 V; T _j = 25 °C		-	0.1	100	μA
		V _{DS} = 1200 V; V _{GS} = 0 V; T _j = 150 °C		-	1	-	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C		-	10	100	nA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C		-	10	100	nA
R _{DS(on)}	drain-source on-state	V _{GS} = 20 V; I _D = 10 A; T _j = 25 °C		-	160	196	mΩ
	resistance	V _{GS} = 20 V; I _D = 10 A; T _j = 150 °C		-	220	-	mΩ
R_G	gate resistance	f = 1 MHz; T _j = 25 °C		-	3.9	-	Ω
g _{fs}	transconductance	V _{DS} = 20 V; I _D = 10 A; T _j = 25 °C		-	4.5	-	S
Dynamic	characteristics				1	<u>'</u>	
Q _{G(tot)}	total gate charge	$I_D = 10 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = 0 \text{ V}/20 \text{ V};$ $T_j = 25 \text{ °C}$		-	35	-	nC
Q _{GS}	gate-source charge			-	12	-	nC
Q_{GD}	gate-drain charge			-	8	-	nC
C _{iss}	input capacitance	V _{DS} = 1000 V; V _{GS} = 0 V; f = 1 MHz; T _j = 25 °C		-	736	-	pF
C _{oss}	output capacitance			-	40	-	pF
C _{rss}	reverse transfer capacitance			-	3.8	-	pF
E _{oss}	Coss stored energy			-	20	-	μJ
t _{d(on)}	turn-on delay time	$V_{DS} = 800 \text{ V}; V_{GS} = -5/20 \text{ V}; R_{G(ext)} = 0 \Omega;$		-	6	-	ns
t _r	rise time	$I_D = 10 \text{ A}$; L = 360 μ H; $T_j = 25 ^{\circ}\text{C}$		-	21	-	ns
$t_{d(off)}$	turn-off delay time			-	12	-	ns
t _f	fall time			-	41	-	ns
E _{on}	turn-on energy (SiC Diode FWD)			-	192	-	μJ
E _{off}	turn-off energy (SiC Diode FWD)			-	5	-	μJ
E _{on}	turn-on energy (Body Diode FWD)			-	307	-	μJ
E _{off}	turn-off energy (Body Diode FWD)			-	10	-	μJ
Source-d	rain diode		1				
V_{SD}	source-drain voltage	$V_{GS} = 0 \text{ V; } I_F = 5 \text{ A; } T_j = 25 \text{ °C}$		-	4.1	-	V
		$V_{GS} = 0 \text{ V; } I_F = 5 \text{ A; } T_j = 150 \text{ °C}$		-	3.5	-	V
t _{rr}	reverse recovery time	$I_{SD} = 10 \text{ A}$; di/dt = 500 A/ μ s; $V_{DS} = 400 \text{ V}$;		-	38	-	ns
Q _r	recovered charge	T _j = 25 °C		-	78	-	nC
I _{rrm}	reverse recovery current			-	3.5	-	Α

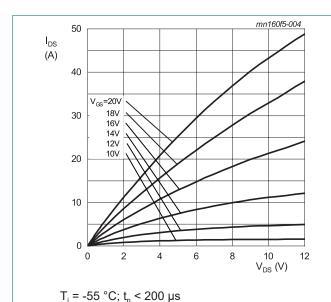
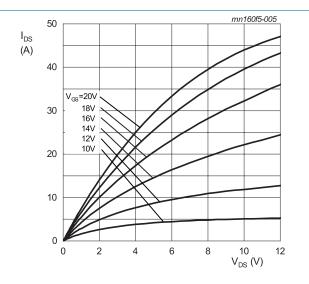
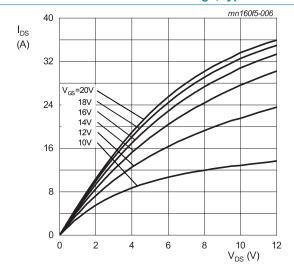


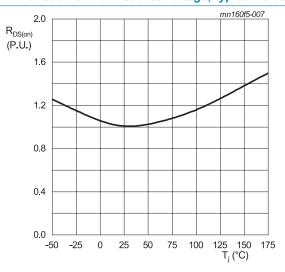
Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values



T_j = 25 °C; t_p < 200 μs Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



T_j = 150 °C; t_p < 200 μs Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values



 I_{DS} = 20 A; V_{GS} = 20 V; t_p < 200 µs Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature

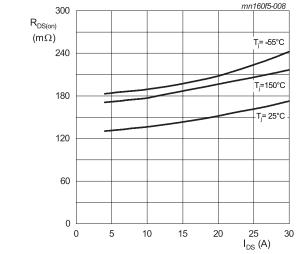
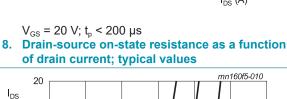
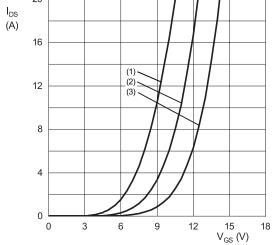


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values





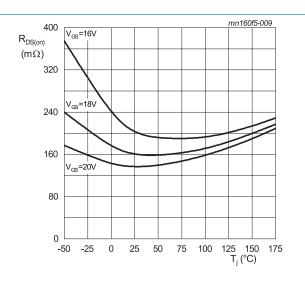
 $V_{DS} = 20 \text{ V}; t_p < 200 \text{ }\mu\text{s}$

 $(1) T_i = 150 °C$

(2) $T_i = 25 \,^{\circ}\text{C}$

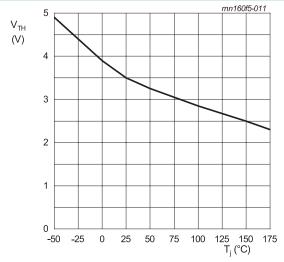
(3) $T_i = -55 \,^{\circ}\text{C}$

Fig. 10. Transfer characteristics; drain current as a function of gate-source voltage; typical values



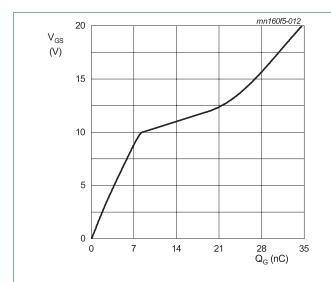
 I_{DS} = 10 A; t_p < 200 μs

Fig. 9. Drain-source on-state resistance as a function of junction temperature



 V_{DS} = 20 V; I_{DS} = 3 mA

Fig. 11. Threshold voltage as a function of junction temperature



 I_{DS} = 10 A; I_{GS} = 1 mA; V_{DS} = 800 V; T_j = 25 °C Fig. 12. Gate-source voltage as a function of gate charge; typical values

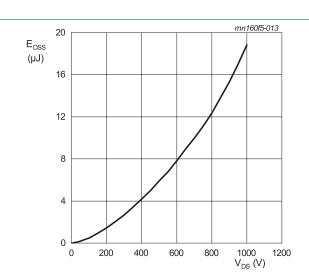
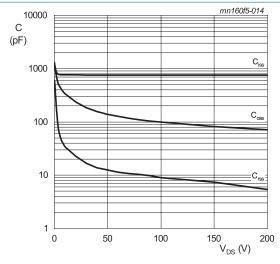
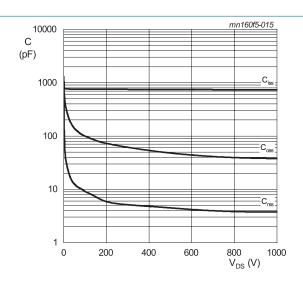


Fig. 13. Output capacitor stored energy as a function of drain-source voltage



V_{DS} = 0 - 200 V T_j = 25 °C; V_{AC} = 25 mV; f = 1 MHz Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical

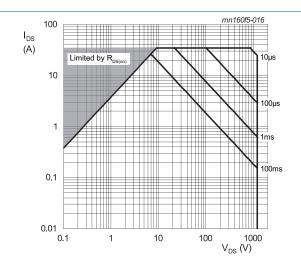
values



 $V_{DS} = 0 - 1000 \text{ V}$ $T_j = 25 \,^{\circ}\text{C}; V_{AC} = 25 \,\text{mV}; f = 1 \,\text{MHz}$

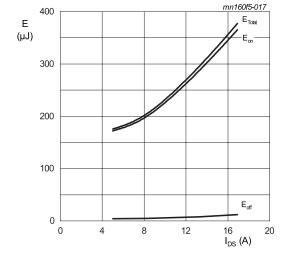
Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

Product data sheet



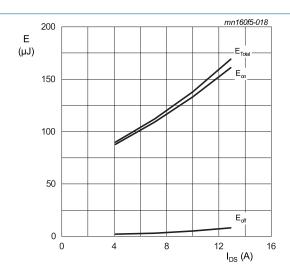
 $T_c = 25$ °C; D = 0 Parameter: t_n

Fig. 16. Forward bias safe operating area



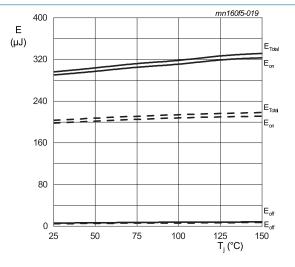
 $T_{\rm j}$ = 25 °C; $V_{\rm DD}$ = 800 V; $R_{\rm G(ext)}$ = 0 Ω ; $V_{\rm GS}$ = -5V/20 V FWD = WNSC051200; L = 360 μH

Fig. 17. Clamped Inductive Switching Energy as a function of drain current



 $T_{\rm j}$ = 25 °C; $V_{\rm DD}$ = 600 V; $R_{\rm G(ext)}$ = 0 $\Omega;$ $V_{\rm GS}$ = -5V/20 V FWD = WNSC051200; L = 360 μH

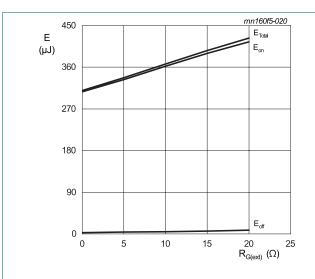
Fig. 18. Clamped Inductive Switching Energy as a function of drain current



 I_{DS} = 10 A; V_{DD} = 800 V; $R_{G(ext)}$ = 0 $\Omega;$ V_{GS} = -5V/20 V L = 360 μH

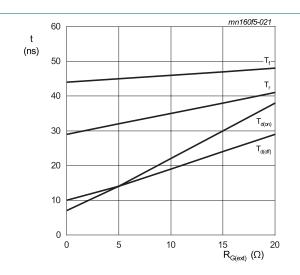
FWD = WNSCM160120W FWD = WNSC051200(- - -)

Fig. 19. Clamped Inductive Switching Energy as a function of junction temperature



 $T_{\rm j}$ = 25 °C; $V_{\rm DD}$ = 800 V; $I_{\rm DS}$ = 10 A; $V_{\rm GS}$ = -5V/20 V FWD = WNSCM160120W; L = 360 μH

Fig. 20. Clamped Inductive Switching Energy as a function of external gate resistance



 $T_{\rm j}$ = 25 °C; $V_{\rm DD}$ = 800 V; $I_{\rm DS}$ = 10 A; $V_{\rm GS}$ = -5V/20 V FWD = WNSCM160120W; L = 360 μH

Fig. 21. Switching time as a function of external gate resistance

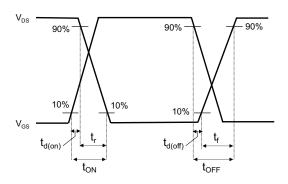
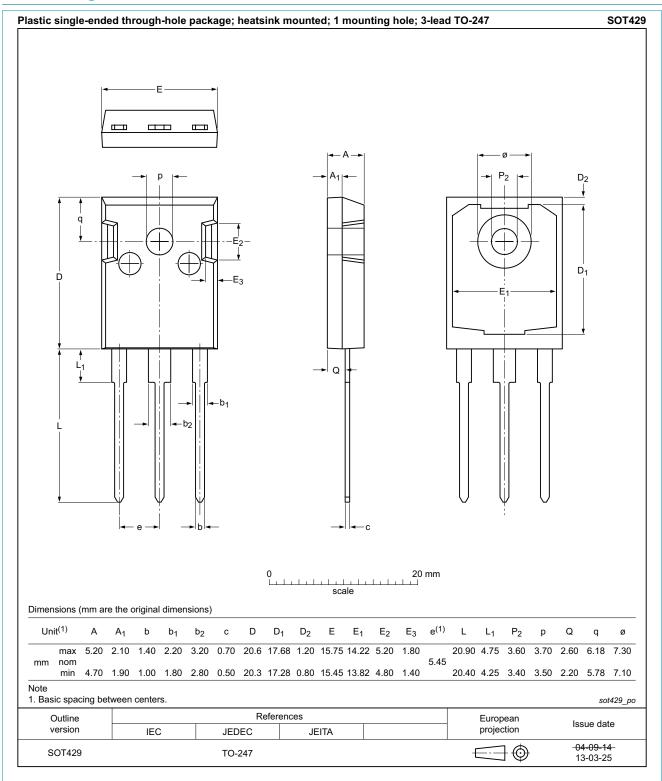


Fig. 22. Switching time definition

11. Package outline



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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