**Product data sheet** 

### 1. General description

WMS30N020S is a high performance logic level N-channel MOSFET in TO252 package, which utilizes advanced Trench MOSFET technology to provide low  $R_{\tiny DS(on)}$  and gate charge. It is designed and qualified in a wide range of industrial and consumer applications.





### 2. Features and benefits

- · Advance High Cell Density Trench Technology
- Low R<sub>DS(on)</sub> to Minimize Conduction Losses
- Low Capacitance to Minimize Switching Losses
- · Optimized Gate Charge to Minimize Driver Losses
- 100% UIS Tested
- · RoHS Compliant and Halogen Free

## 3. Applications

- DC-DC Converters
- BLDC Motor Control
- Load Switch
- Lithium-ion Battery Protection

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit
Absolute	maximum rating						
$V_{DS}$	drain-source voltage				30		V
$V_{GS}$	gate-source voltage				±20		V
I <sub>D</sub>	continuous drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C	[1]		159		Α
P <sub>tot</sub>	power dissipation	T <sub>mb</sub> = 25 °C			83		W
T <sub>j</sub>	junction temperature			-55 to 150		°C	
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
R <sub>DS(on)</sub>	drain-source on-state	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		-	1.6	2.0	mΩ
	resistance	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		-	2.1	3.3	mΩ
Dynamic	characteristics						
$Q_{G(tot)}$	total gate charge	I <sub>D</sub> = 20 A; V <sub>DS</sub> = 15 V; V <sub>GS</sub> = 10 V		-	146	-	nC

# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	[	D
2	D	drain		
3	S	source		G
mb	D	mounting base; connected to drain		sym300 S

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WMS30N020S	TO252	WMS30N020SJ	Reel	2500	TO252N	14-Nov-2016

# 7. Marking

#### Table 4. Marking codes

Type number	Marking codes
WMS30N020S	WMS
	30N020

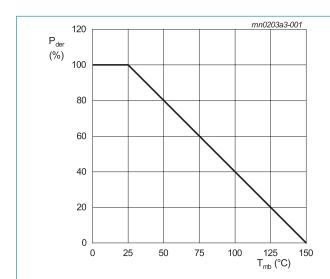
# 8. Limiting values

### **Table 5. Limiting values**

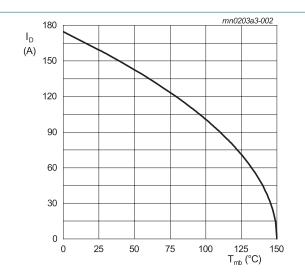
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
$V_{DS}$	drain-source voltage			30	V
V <sub>GS</sub>	gate-source voltage			±20	V
I <sub>D</sub>	continuous drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C	[1]	159	Α
		V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 120 °C		78	Α
I <sub>DM</sub>	pulsed drain current	t <sub>p</sub> = 10 μs; T <sub>mb</sub> = 25 °C		636	Α
P <sub>tot</sub>	power dissipation	T <sub>mb</sub> = 25 °C		83	W
E <sub>as</sub>	single pulse drain-to- source avalanche	$I_{AS}$ = 40 A; L = 0.1 mH; R <sub>GS</sub> = 25 $\Omega$ ; V <sub>GS</sub> = 10 V; T <sub>j</sub> = 25 °C		80	mJ
T <sub>stg</sub>	storage temperature			-55 to 150	°C
T <sub>j</sub>	junction temperature			-55 to 150	°C

[1] Calculated continuous current based on maximum allowable junction temperature. Package current limitation is 70A.



P<sub>der</sub> = (P<sub>tot</sub> / P<sub>tot(25 °C)</sub>) x 100% Fig. 1. Normalized total power dissipation as a function of mounting base temperature



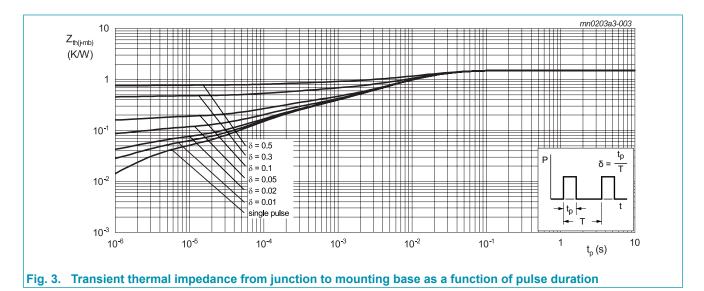
V<sub>GS</sub> = 10 V
Fig. 2. Continuous Drain Current as a function of mounting base temperature

## 9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base			-	1.2	1.5	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[2]	-	-	50	K/W

[2] Surface mount on FR4 board of 1 inch2, 1 oz copper.



### 10. Characteristics

### **Table 7. Characteristics**

T<sub>i</sub> = 25 °C unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V		30	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	$I_D = 250 \ \mu A; \ V_{DS} = V_{GS}$		1.0	1.5	2.4	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 30 V; V <sub>GS</sub> = 0 V		-	-	1	μA
		V <sub>DS</sub> = 30 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 125 °C		-	-	100	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS} = \pm 20 \text{ V}; V_{DS} = 0 \text{ V}$		-	-	±100	nA
R <sub>DS(on)</sub>	drain-source on-state	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 20 A		-	1.6	2.0	mΩ
	resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 20 A		-	2.1	3.3	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz		-	1.1	-	Ω
Dynamic	characteristics						
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 20 A; V <sub>DS</sub> = 15 V; V <sub>GS</sub> = 10 V		-	146	-	nC
Q <sub>GS</sub>	gate-source charge			-	21	-	nC
$Q_{GD}$	gate-drain charge			-	25	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 15 V; V <sub>GS</sub> = 0 V; f = 1 MHz		-	7940	-	pF
C <sub>oss</sub>	output capacitance			-	847	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	710	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = 15 \text{ V}; V_{GS} = 10 \text{ V}; R_G = 6 \Omega;$		-	17	-	ns
t <sub>r</sub>	rise time	$I_D = 20 \text{ A}$		-	31	-	ns
$t_{d(off)}$	turn-off delay time			-	60	-	ns
t <sub>f</sub>	fall time			-	34	-	ns
Source-di	rain diode						
V <sub>SD</sub>	source-drain voltage	V <sub>GS</sub> = 0 V; I <sub>S</sub> = 1 A		-	0.67	1	V
		$V_{GS} = 0 \text{ V; } I_S = 1 \text{ A; } T_j = 125 \text{ °C}$		-	0.50	-	V
Is	body-diode continuous current	T <sub>mb</sub> = 25 °C		-	-	90	А
t <sub>rr</sub>	reverse recovery time	$V_{GS} = 0 \text{ V; } I_S = 20 \text{ A; } di/dt = 100 \text{ A/}\mu\text{s}$		-	19	-	ns
Q <sub>rr</sub>	reverse recovered charge			-	12	-	nC
I <sub>rrm</sub>	reverse recovery current			-	1	-	Α

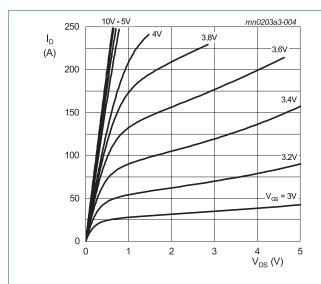
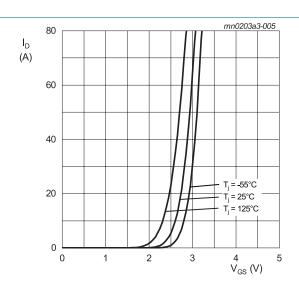
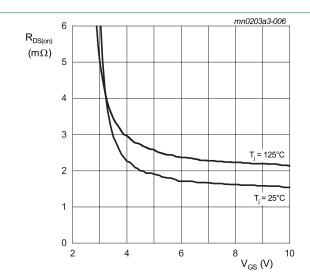


Fig. 4. Drain current as a function of drain-source voltage; typical values



V<sub>DS</sub> = 5 V

Fig. 5. Drain current as a function of gate-source voltage; typical values



I<sub>D</sub> = 20 A
Fig. 6. Drain-source on-state resistance as a function of gate-source voltage; typical values

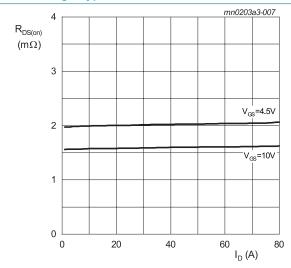
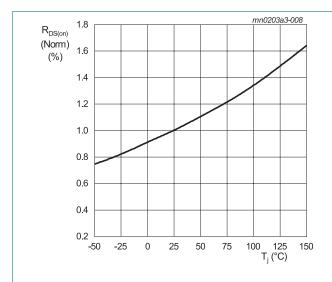
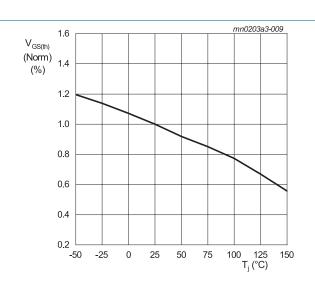


Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



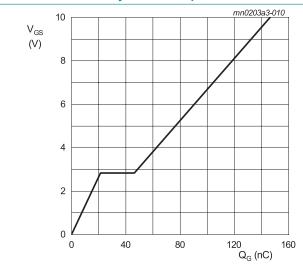
 $V_{GS}$  = 10 V;  $I_{D}$  = 20 A

Fig. 8. Normalized drain-source on-state resistance as a function of junction temperature



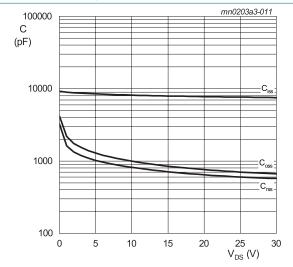
 $V_{DS}$  =  $V_{GS}$ ;  $I_D$  = 250  $\mu A$ 

Fig. 9. Normalized gate-source threshold voltage as a function of junction temperature



 $I_D = 20 \text{ A}; V_{DS} = 15 \text{ V}$ 

Fig. 10. Gate-source voltage as a function of gate charge; typical values



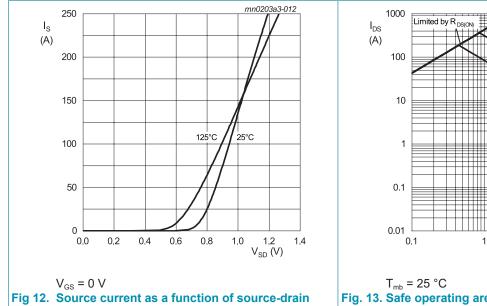
 $V_{GS} = 0 V; f = 1 MHz$ 

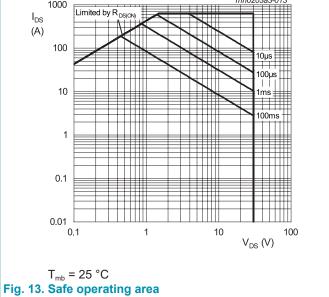
Fig 11. Capacitances as a function of drain-source voltage; typical values

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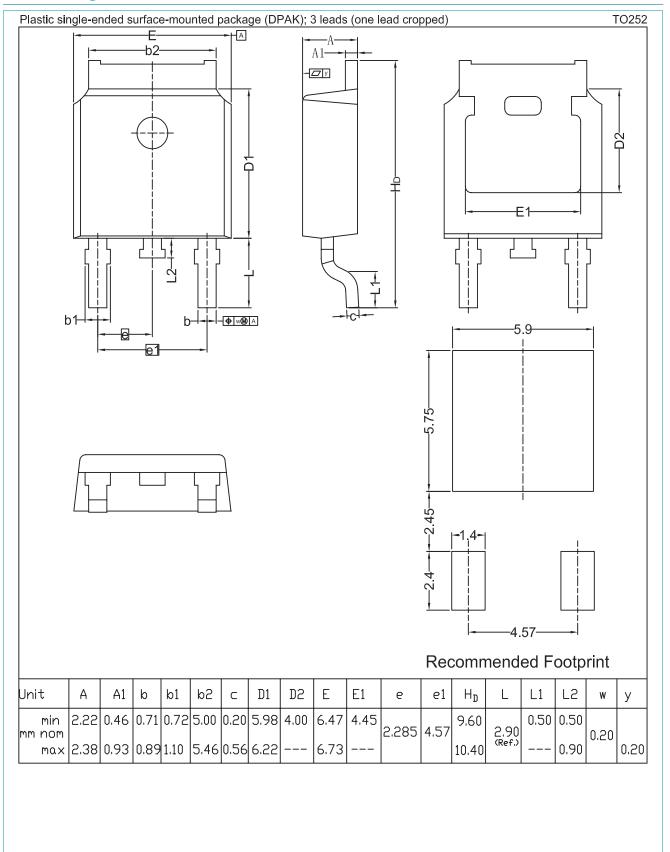
voltage; typical values

### N-Channel Silicon MOSFET





# 11. Package outline



### 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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