

WMS20N270SE N-Channel Silicon MOSFET Rev.01 - 18 January 2023

1. General description

WMS20N270SE is a high performance super logic level N-channel MOSFET in PDFN3.3X3.3 package, which utilizes advanced Trench MOSFET technology to provide low $R_{DS(on)}$ and gate charge. It is designed and qualified in a wide range of industrial and consumer applications.



2. Features and benefits

- Advance High Cell Density Trench Technology
- Low R_{DS(on)} to Minimize Conduction Losses
- Low Capacitance to Minimize Switching Losses
- Optimized Gate Charge to Minimize Driver Losses
- 100% UIS Tested
- RoHS Compliant, Halogen Free and Lead Free

3. Applications

- DC-DC Converters
- BLDC Motor Control
- Load Switch
- Lithium-ion Battery Protection

4. Quick reference data

Table 1. Qi	uick reference data						
Symbol	Parameter	Conditions	Notes	s Values			Unit
Absolute	maximum rating						
V _{DS}	drain-source voltage				20		V
V _{GS}	gate-source voltage				±10		V
I _D	continuous drain current	V _{GS} = 4.5 V; T _{mb} = 25 °C	[1]		18		А
P _{tot}	power dissipation	T _{mb} = 25 °C			14		W
Tj	junction temperature			-55 to 150			°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics					·	
$R_{\text{DS(on)}}$	drain-source on-state	V _{GS} = 4.5 V, I _D = 8 A		-	17	27	mΩ
resistance		V _{GS} = 2.5 V, I _D = 3 A		-	25	44	mΩ
Dynamic characteristics							
Q _{G(tot)}	total gate charge	$I_{\rm D}$ = 8 A; $V_{\rm DS}$ = 10 V; $V_{\rm GS}$ = 4.5 V		-	5.1	-	nC

5. Pinning information

Table 2. P	Table 2. Pinning information						
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1-3	S	source	8765	D			
4	G	gate					
5-8	D	drain		G sym300 S			

6. Ordering information

Table 3. Ordering information							
Type number	Package	Orderable part number	Packing	Small packing	Package	Package	
	Name		method	quantity	version	issue date	
WMS20N270SE	PDFN3.3X3.3	WMS20N270SEJ	Reel	5000	PDFN3.3X3.3N	22-Sep-2022	

7. Marking

Table 4. Marking codes	
Type number	Marking codes
WMS20N270SE	2N270S

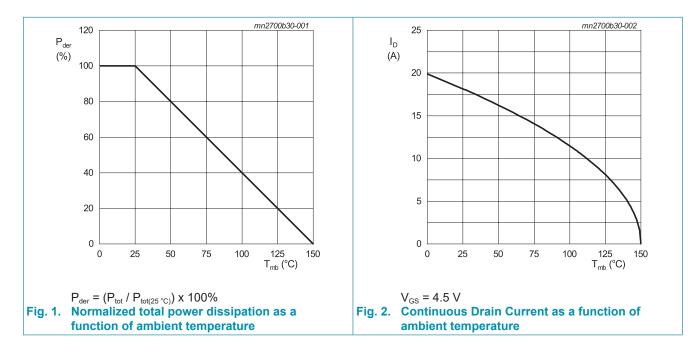
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V _{DS}	drain-source voltage			20	V
V _{GS}	gate-source voltage			±10	V
I _D	continuous drain current	V _{GS} = 4.5 V; T _{mb} = 25 °C	[1]	18	А
		V _{GS} = 4.5 V; T _{mb} = 120 °C		8.9	А
I _{DM}	pulsed drain current	t _p = 10 μs; T _{mb} = 25 °C		72	А
P _{tot}	power dissipation	T _{mb} = 25 °C		14	W
E _{as}	single pulse drain-to- source avalanche	I_{AS} = 7 A; L = 0.1 mH; R _{GS} = 25 Ω; V _{GS} = 4.5 V; T _j = 25 °C		2.5	mJ
T _{stg}	storage temperature			-55 to 150	°C
Tj	junction temperature			-55 to 150	°C

[1] Calculated continuous current based on maximum allowable junction temperature. Package current limitation is 8A.

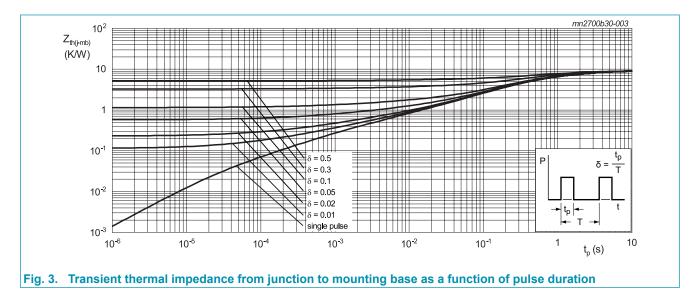


9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base			-	7	9.1	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[2]	-	-	60	K/W

[2] Surface mount on FR4 board of 1 inch², 1 oz copper.



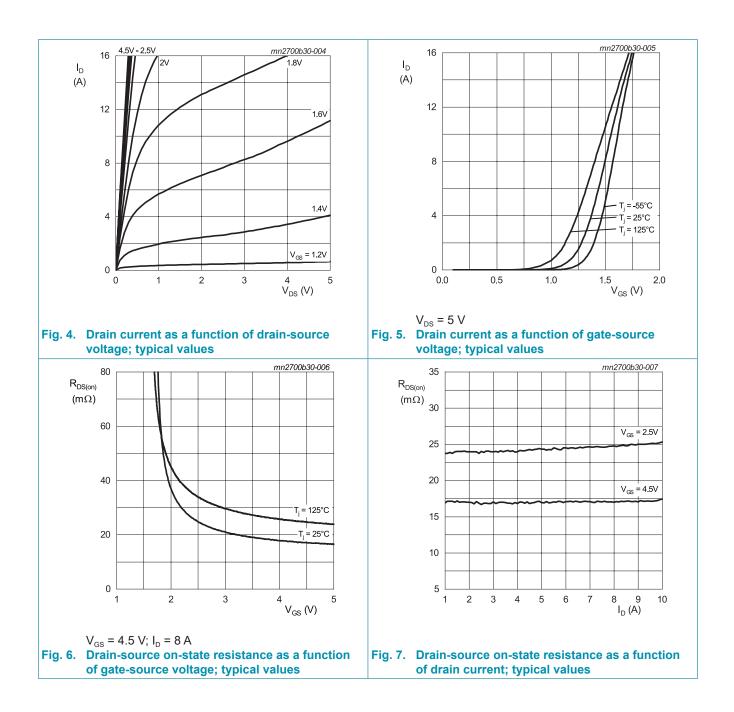
10. Characteristics

Table 7. Characteristics

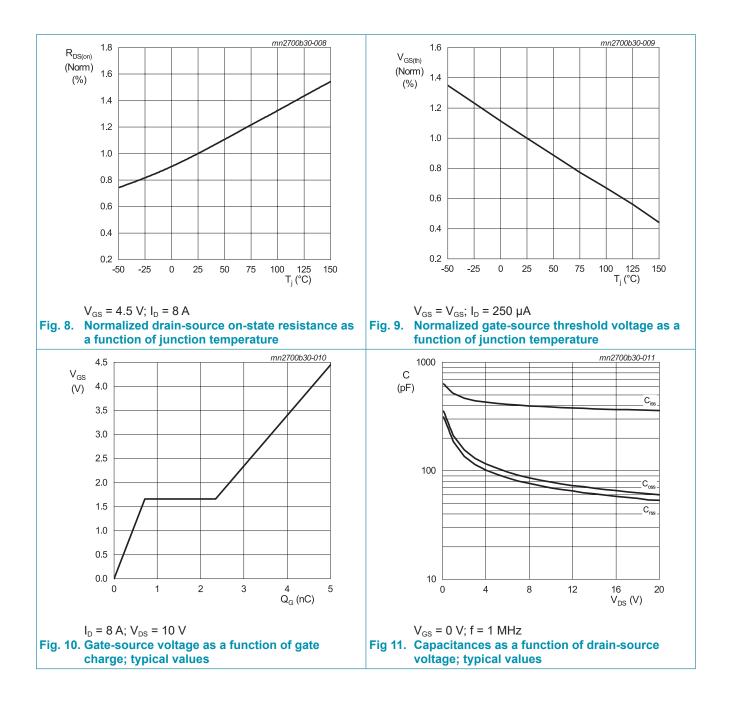
$T_j = 25 \ ^{\circ}C \ unless \ or \ $	otherwise noted
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Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_{\rm D}$ = 250 µA; $V_{\rm GS}$ = 0 V		20	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	I_D = 250 µA; V_{DS} = V_{GS}		0.5	0.7	1.1	V
I _{DSS}	drain leakage current	V _{DS} = 20 V; V _{GS} = 0 V		-	-	1	μA
		V _{DS} = 20 V; V _{GS} = 0 V; T _j = 125 °C		-	-	10	μA
I _{GSS}	gate leakage current	$V_{GS} = \pm 10 \text{ V}; V_{DS} = 0 \text{ V}$		-	-	±100	nA
$R_{\text{DS(on)}}$	drain-source on-state	V _{GS} = 4.5 V; I _D = 8 A		-	17	27	mΩ
	resistance	V _{GS} = 2.5 V; I _D = 3 A		-	25	44	mΩ
R _G	gate resistance	f = 1 MHz		-	3.3	-	Ω
Dynamic	characteristics	·					
Q _{G(tot)}	total gate charge	I_{D} = 8 A; V_{DS} = 10 V; V_{GS} = 4.5 V		-	5.1	-	nC
Q _{GS}	gate-source charge			-	0.7	-	nC
Q _{GD}	gate-drain charge			-	1.6	-	nC
C _{iss}	input capacitance	V _{DS} = 10 V; V _{GS} = 0 V; f = 1 MHz		-	388	-	pF
C _{oss}	output capacitance			-	79	-	pF
C _{rss}	reverse transfer capacitance			-	70	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 10 \text{ V}; V_{GS} = 10 \text{ V}; R_{G} = 6 \Omega;$		-	7.0	-	ns
t _r	rise time	$I_{D} = 8 A$		-	15	-	ns
t _{d(off)}	turn-off delay time			-	26	-	ns
t _f	fall time			-	12	-	ns
Source-d	rain diode	·					
V _{SD}	source-drain voltage	V _{GS} = 0 V; I _S = 1 A		-	0.71	1	V
		V _{GS} = 0 V; I _S = 1 A; T _j = 125 °C		-	0.55	-	V
ls	body-diode continuous current	T _{mb} = 25 °C		-	-	15	А
t _{rr}	reverse recovery time	V_{GS} = 0 V; I _S = 8 A; di/dt = 100 A/µs		-	13	-	ns
Q _{rr}	reverse recovered charge			-	4.7	-	nC
l _{rrm}	reverse recovery current			-	0.6	-	А

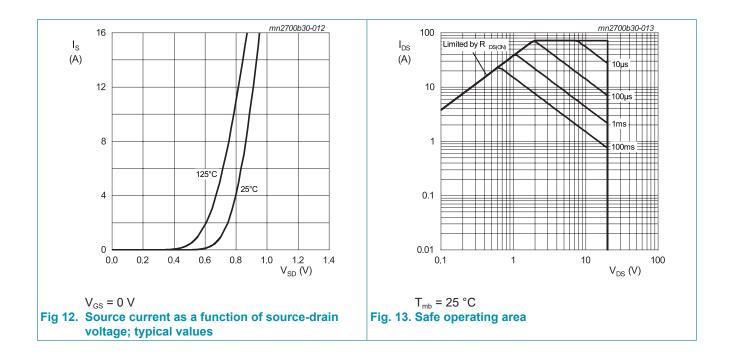
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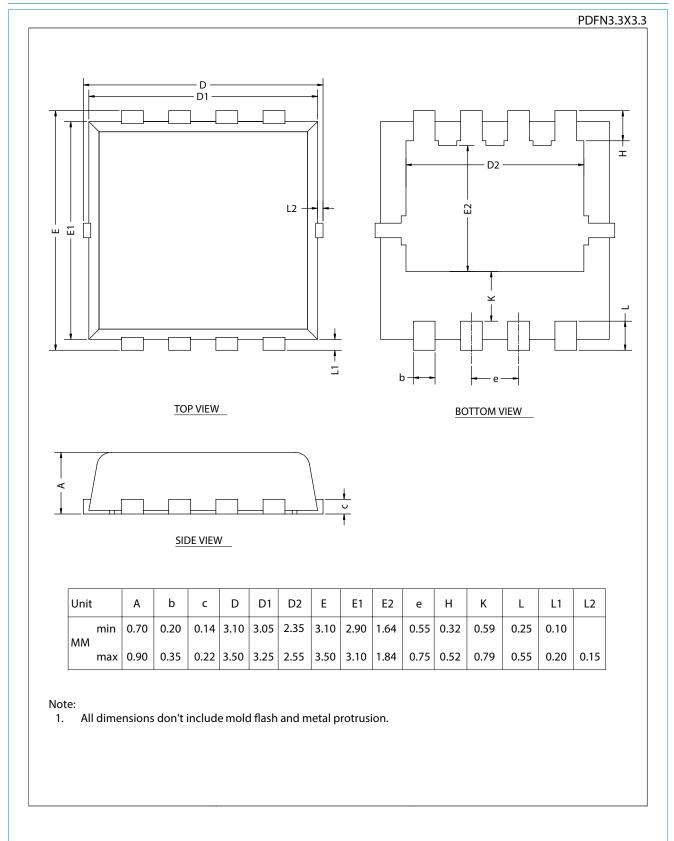
N-Channel Silicon MOSFET



WMS20N270SE N-Channel Silicon MOSFET



11. Package outline



WMS20N270SE

N-Channel Silicon MOSFET

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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