Product data sheet

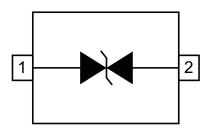
1. General description

The ESDHDxxBC series are designed to protect voltage sensitive components which are connected to data and transmission lines from overvoltage caused by ESD (electrostatic discharge), CDE (Cable Discharge Events), and EFT (electrical fast transients). Excellent clamping capability, this series offers a low leakage current in a miniature SOD323 package.



2. Features and benefits

- Peak pulse power 350W @ 8/20µs waveform
- IEC 61000-4-2 (ESD) ±30kV(air), ±30kV(contact)
- · Protects Bi-directional I/O line
- Low clamping voltage
- Low leakage current
- Meet MSL level1
- · Halogen free and RoHS compliant



3. Applications

- Computer Interfaces Protection
- Microprocessors Protection
- Serial and Parallel Ports Protection
- Control Signal Lines Protection
- Power lines on PCB Protection
- Portable instrumentation
- Peripherals





4. Ordering information

Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
ESDHDxxBC	SOD323	ESDHDxxBCX	Tape and reel	3000	SOD323X	13-Oct-2020
ESDHD03BC	SOD323	ESDHD03BCX	Tape and reel	3000	SOD323X	13-Oct-2020

5. Absolute maximum ratings

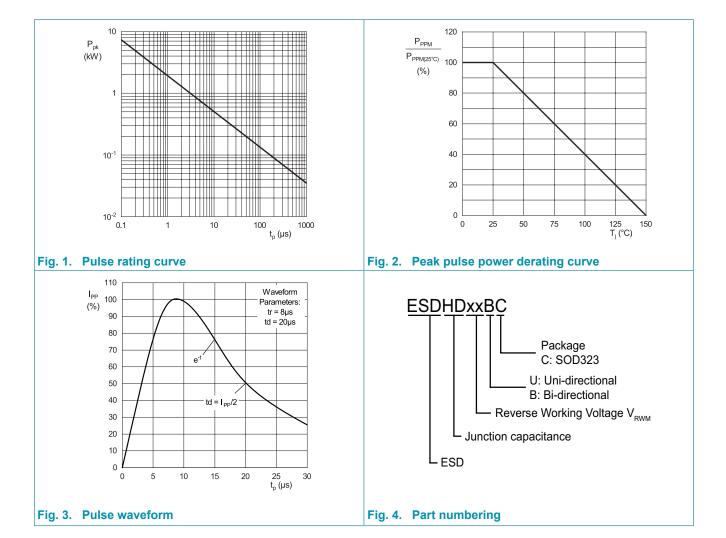
In accordance with the Absolute Maximum Rating System (IEC 60134). $T_{c} = 25 \, ^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Conditions	Values	Unit		
Absolute maximum rating						
P _{PPM}	peak pulse power	t _p = 8/20 μs	350	W		
V_{ESD}	ESD per IEC 61000-4-2 (air) ESD per IEC 61000-4-2 (contact)		±30 ±30	kV kV		
T _{stg}	storage temperature range		-55 to 150	°C		
T _j	operating temperature range		-55 to 150	°C		

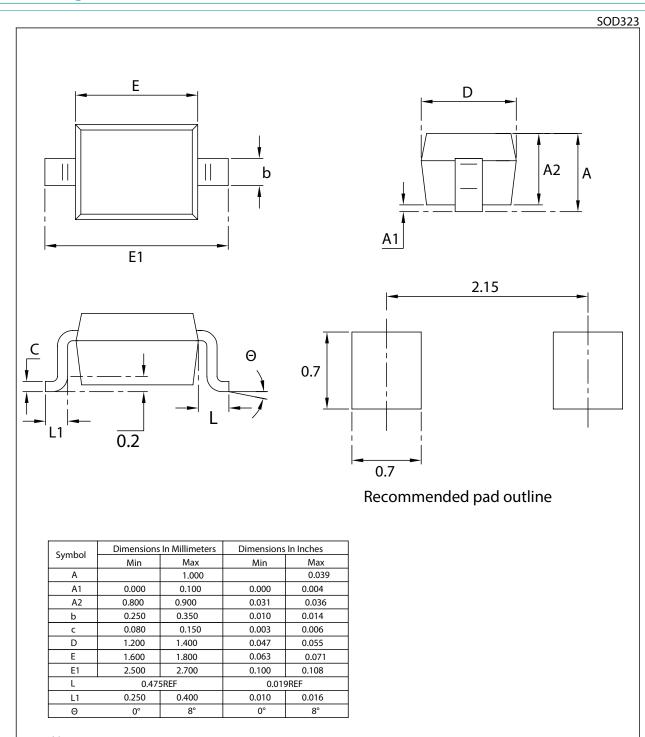
6. Characteristics

 T_i = 25 °C unless otherwise specified.

Product type	Max. Reverse Working Voltage V _{RWM} (V)	Min. Breakdown Voltage V _{BR} @ I _T = 1 mA (V)	Max. Clamping Voltage V _c @ I _{pp} = 1 A (V)	Max. Clamping Voltage V _c @ Max I _{pp} (V)	Max. Peak Pulse current I _{pp} @ 8/20 µs (A)	Maximum Reverse Leakage I _R @ V _R (μA)	Typ. C _j (pF) @ 0 V, 1 MHz	Marking
ESDHD03BC	3.3	3.6	7.5	16	25	1	150	2A
ESDHD05BC	5	6	9.8	18	24	1	120	2B
ESDHD08BC	8	8.5	13.4	24	18	1	110	2C
ESDHD12BC	12	13.3	19	32	13	1	70	2D
ESDHD15BC	15	16.5	24	38	10	1	40	2J
ESDHD18BC	18	20	29	45	8	1	35	2K
ESDHD24BC	24	26.7	43	52	7	1	30	2H



7. Package outline



Note:

- 1. Controlling dimension: in millimeters.
- 2. General tolerance:+/-0.05mm.
- 3. The pad layout is for reference purposes only.

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8. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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