**Product data sheet** 

## 1. General description

WeEn Gen-2 Silicon Carbide MOSFET in a TO247-4L plastic package, designed for high frequency, high efficiency systems.



## 2. Features and benefits

- · Kelvin source configuration
- Low specific on-resistance
- Optimized dynamic performance
- 0V turn-off V<sub>GS</sub> for simple gate driving
- 100% UIS Tested
- Easy to parallel
- RoHS compliant
- Automotive Qualified (AEC-Q101)

## 3. Applications

- · Automotive on board chargers
- Automotive DC-DC converters
- Automotive electric compressor motor drives
- HV battery management systems

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit		
Absolute	Absolute maximum rating								
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C			750		V		
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C			80		Α		
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C			283		W		
T <sub>j</sub>	junction temperature			-55 to 175 °C		°C			
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit		
Static cha	racteristics								
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = 15 \text{ V}; I_D = 25 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	40	-	mΩ		
		$V_{GS}$ = 18 V; $I_{D}$ = 25 A; $T_{j}$ = 25 °C		-	31	40	mΩ		
Dynamic	characteristics								
Q <sub>G(tot)</sub>	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	90	-	nC		
$Q_{GD}$	gate-drain charge	T <sub>j</sub> = 25 °C		-	10.8	-	nC		
Source-di	rain diode				•				
Q <sub>r</sub>	recovered charge	$I_{SD}$ = 25 A; di/dt = 500 A/µs; $V_{DS}$ = 400 V; $T_{j}$ = 25 °C		-	97.9	-	nC		

# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain		D
2	S	source		
3	SS	source sense		G (E)
4	G	gate		SS Sym301 S
mb	D	mounting base; connected to drain		,

# 6. Ordering information

## **Table 3. Ordering information**

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSC2M40075R-A	TO247-4L	WNSC2M40075R-A6Q	Tube	30	TO247N-4L	17-Dec-2021

# 7. Marking

## **Table 4. Marking codes**

Type number	Marking codes
WNSC2M40075R-A	WNSC2M 40075R-A

# 8. Limiting values

### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C		750	V
$V_{\rm GS,max}$	gate-source voltage	Absolute maximum values		-10 to 22	V
$V_{GS,op}$	gate-source voltage	Recommended operational values		-4 to 18	V
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C, T <sub>j</sub> = 175 °C		283	W
I <sub>D</sub>	drain current	V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 25 °C		80	Α
		V <sub>GS</sub> = 18 V; T <sub>mb</sub> = 100 °C		56	Α
I <sub>DM</sub>	peak drain current	pulse width t <sub>p</sub> limited by T <sub>jmax</sub>	Fig.17	160	Α
Is	continuous diode current	V <sub>GS</sub> = -4 V; T <sub>mb</sub> = 25 °C		53	Α
I <sub>SM</sub>	pulse diode current	$V_{GS}$ = -4 V; pulse width $t_p$ limited by $T_{jmax}$		160	А
E <sub>as</sub>	single pulse drain-to- source avalanche	$I_{AS} = 23.8 \text{ A}; L = 1 \text{ mH}; V_{DD} = 100 \text{ V};$ $T_j = 25 \text{ °C}$		283	mJ
T <sub>stg</sub>	storage temperature			-55 to 175	°C
T <sub>j</sub>	junction temperature			-55 to 175	°C
$T_{sld(M)}$	peak soldering temperature			260	°C

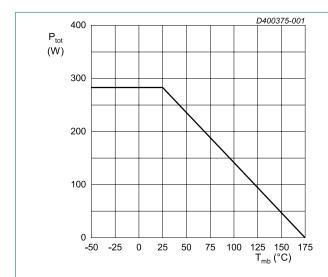


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

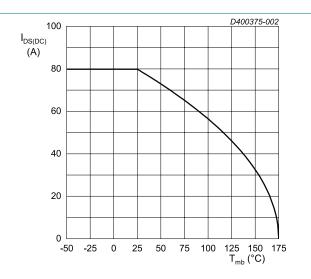


Fig. 2. Continuous Drain Current as a function of mounting base temperature

## 9. Thermal & Mechanical characteristics

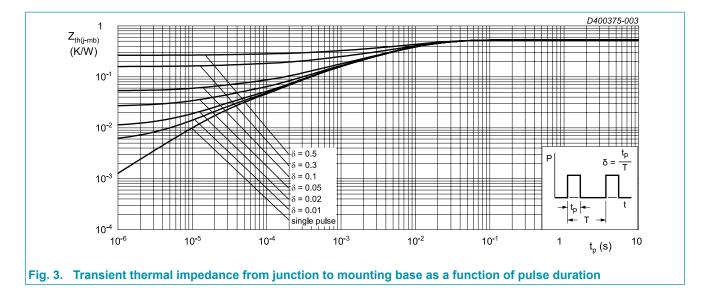
Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base			-	0.53	-	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air		-	40	-	K/W
M <sub>d</sub>	Mounting torque	M3 or 6 - 32 screw		-	-	0.6	Nm

Note: It is recommended that a metal washer is inserted between screw head and mounting tab.

Do not use self-tapping screws.

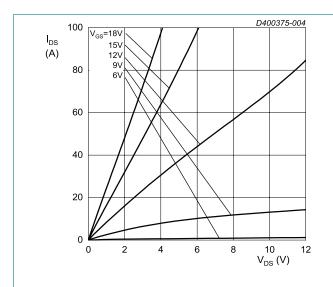
Device is ESD sensitive. Handling precautions are recommended.



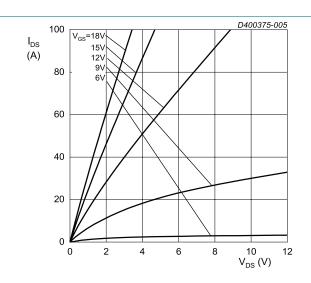
## 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_j = 25 °C$		750	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold	$I_D = 7 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$		2	2.8	3.6	V
	voltage	$I_D = 7 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C}$		-	2.1	-	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 750 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	0.1	50	μΑ
		V <sub>DS</sub> = 750 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C		-	5	-	μΑ
I <sub>GSS</sub>	gate leakage current	$V_{GS} = 22 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	5	100	nA
		$V_{GS} = -10 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	5	100	nA
R <sub>DS(on)</sub>	drain-source on-state	V <sub>GS</sub> = 15 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C		-	40	-	mΩ
	resistance	V <sub>GS</sub> = 18 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C		-	31	40	mΩ
		V <sub>GS</sub> = 18 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 175 °C		-	37	-	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C		-	1.14	-	Ω
g <sub>fs</sub>	transconductance	$V_{DS} = 20 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}$		-	14	-	S
Dynamic	characteristics		,				
Q <sub>G(tot)</sub>	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 400 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	90	-	nC
$Q_{GS}$	gate-source charge	T <sub>j</sub> = 25 °C		-	38	-	nC
$Q_{GD}$	gate-drain charge			-	10.8	-	nC
C <sub>iss</sub>	input capacitance	$V_{DS} = 400 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$		-	2139	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C		-	156	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	11.5	-	pF
E <sub>oss</sub>	Coss stored energy			-	12.5	-	μJ
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 400 V; V <sub>GS</sub> = -4 V/18 V;		-	22	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 5.1 \Omega$ ; $I_D = 12.5 A$ ; $L = 100 \mu H$ ; $T_i = 25 °C$		-	11	-	ns
$t_{\text{d(off)}}$	turn-off delay time	1		-	44	-	ns
t <sub>f</sub>	fall time			-	20	-	ns
E <sub>on</sub>	turn-on energy (SIC Diode FWD)		Fig.19	-	74	-	μJ
E <sub>off</sub>	turn-off energy (SIC Diode FWD)		Fig.19	-	16	-	μJ
E <sub>on</sub>	turn-on energy (Body Diode FWD)		Fig.19	-	82	-	μJ
E <sub>off</sub>	turn-off energy (Body Diode FWD)		Fig.19	-	15	-	μJ
Source-d	rain diode					<u>'</u>	
V <sub>SD</sub>	source-drain voltage	V <sub>GS</sub> = 0 V; I <sub>SD</sub> = 25 A; T <sub>j</sub> = 25 °C		-	3.7	-	V
		V <sub>GS</sub> = -4 V; I <sub>SD</sub> = 25 A; T <sub>j</sub> = 25 °C		-	4.2	-	V
		$V_{GS} = -4 \text{ V}; I_{SD} = 25 \text{ A}; T_j = 175 ^{\circ}\text{C}$		-	3.7	-	V
t <sub>rr</sub>	reverse recovery time	$I_{SD} = 25 \text{ A}$ ; di/dt = 500 A/ $\mu$ s; $V_{DS} = 400 \text{ V}$ ;		-	30.8	-	ns
Q <sub>r</sub>	recovered charge	T <sub>j</sub> = 25 °C		-	97.9	-	nC
I <sub>rrm</sub>	reverse recovery current			-	6.4	-	Α

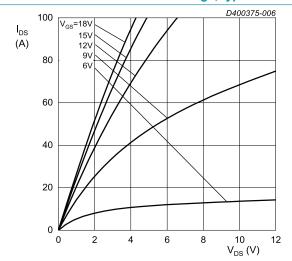


T<sub>j</sub> = -55 °C; t<sub>p</sub> < 200 μs Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values

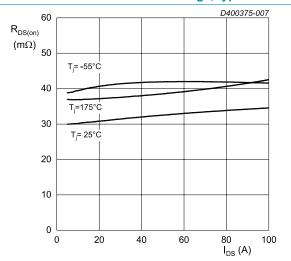


 $T_j = 25 \,^{\circ}\text{C}; t_p < 200 \,\mu\text{s}$ 

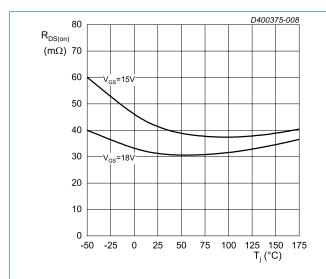
Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



T<sub>j</sub> = 175 °C; t<sub>p</sub> < 200 μs Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

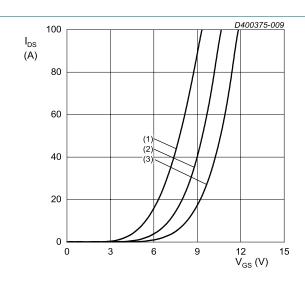


 $V_{GS}$  = 18 V;  $t_p$  < 200 µs Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



 $I_{DS}$  = 25 A;  $t_p$  < 200  $\mu s$ 

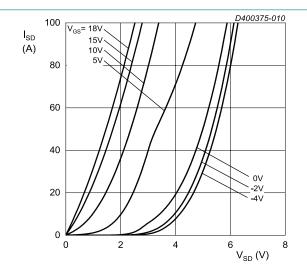
Fig. 8. Drain-source on-state resistance as a function of junction temperature



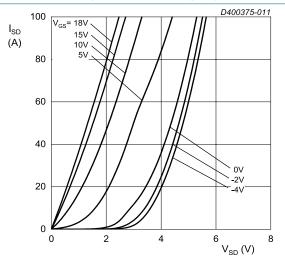
 $V_{DS} = 20 \text{ V; } t_p < 200 \text{ } \mu \text{s}$ (1)  $T_j = 175 \text{ }^{\circ}\text{C}$ (2)  $T_j = 25 \text{ }^{\circ}\text{C}$ 

(3)  $T_i = -55 \,^{\circ}C$ 

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values



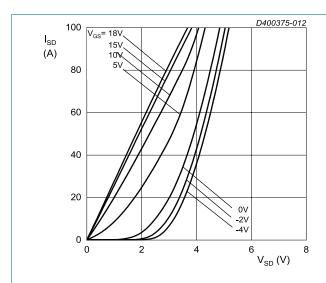
 $T_{j} = -55 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$ Fig. 10. Body diode forward characteristics; typical values



 $T_{j} = 25 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$ 

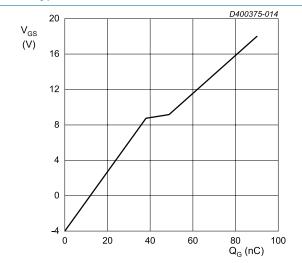
Fig. 11. Body diode forward characteristics; typical values

**Product data sheet** 

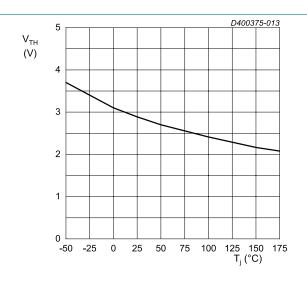


 $T_i = 175 \,^{\circ}\text{C}; t_p < 200 \,\mu\text{s}$ 

Fig. 12. Body diode forward characteristics; typical values



 $I_{DS}$  = 25 A;  $I_{GS}$  = 0.1 mA;  $V_{DS}$  = 400 V;  $T_i$  = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values



 $V_{DS} = V_{GS}$ ;  $I_{DS} = 7$  mA Fig. 13. Threshold voltage as a function of junction temperature

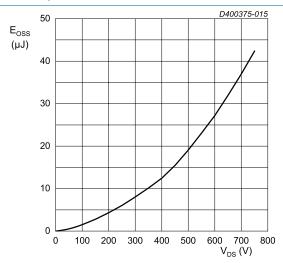
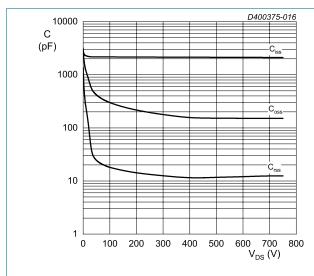


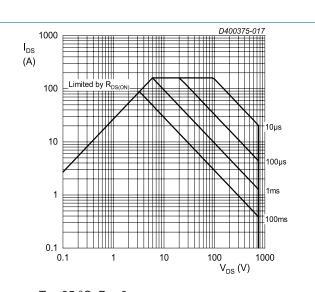
Fig. 15. Output capacitor stored energy as a function of drain-source voltage



 $V_{DS} = 0 - 750 \text{ V}$ 

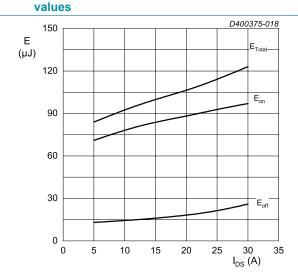
 $T_i = 25 \, ^{\circ}C; \, V_{AC} = 25 \, \text{mV}; \, f = 1 \, \text{MHz}$ 

Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical



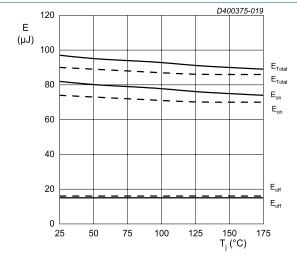
 $T_i = 25 \, ^{\circ}C; D = 0$ Parameter: t₀

Fig. 17. Forward bias safe operating area



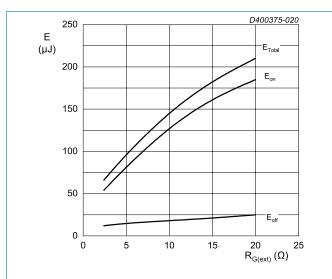
 $T_{j}$  = 25 °C;  $V_{DD}$  = 400 V;  $R_{G(ext)}$  = 5.1  $\Omega;$   $V_{GS}$  = -4 V/18 V; L = 100  $\mu H$ FWD = WNSC2M40075R-A

Fig. 18. Clamped Inductive Switching Energy as a function of drain current



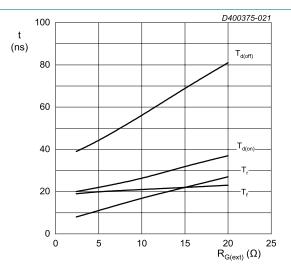
 $I_{DS}$  =12.5 A;  $V_{DD}$  = 400 V;  $R_{G(ext)}$  = 5.1  $\Omega;$   $V_{GS}$  = -4 V/18 V; L = 100  $\mu H$ FWD = WNSC2M40075R-A FWD = WNSC6D20650W(- - -)

Fig. 19. Clamped Inductive Switching Energy as a function of junction temperature



 $T_{j}$  = 25 °C;  $V_{DD}$  = 400 V;  $I_{DS}$  = 12.5 A;  $V_{GS}$  = -4 V/18 V FWD = WNSC2M40075R-A; L = 100  $\mu H$ 

Fig. 20. Clamped Inductive Switching Energy as a function of external gate resistance



 $T_{\rm j}$  = 25 °C;  $V_{\rm DD}$  = 400 V;  $I_{\rm DS}$  = 12.5 A;  $V_{\rm GS}$  = -4 V/18 V FWD = WNSC2M40075R-A; L = 100  $\mu H$ 

Fig. 21. Switching time as a function of external gate resistance

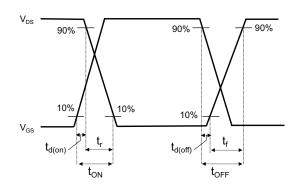
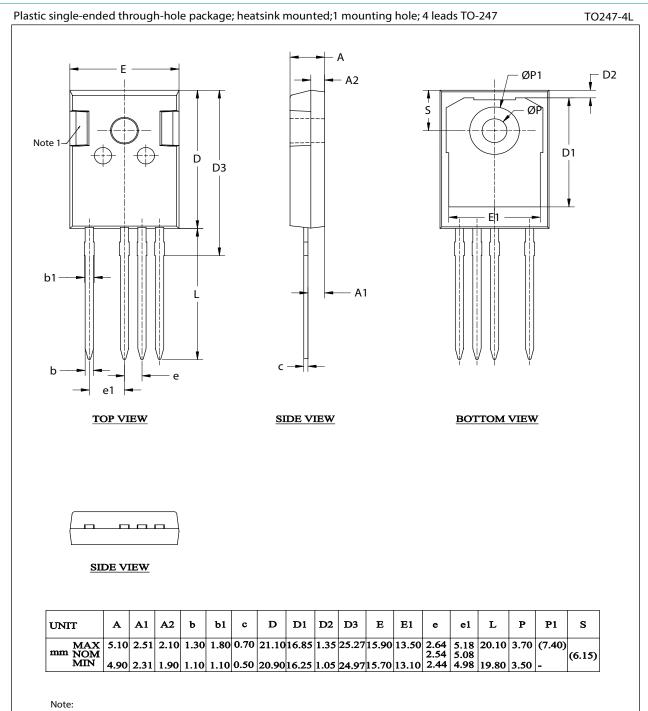


Fig. 22. Switching time definition

## 11. Package outline



- Metal exposed with Sn plating.
- 2. All dimensions do not include mold flash & gate remain

## 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.ween-semi.com">http://www.ween-semi.com</a>.

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