

1. General description

WeEn Gen-2 Silicon Carbide MOSFET in a TO247-4L plastic package, designed for high frequency, high efficiency systems.



AEC - Q101 Qualified



2. Features and benefits

- Kelvin source configuration
- Low specific on-resistance
- Optimized dynamic performance
- 0V turn-off V_{GS} for simple gate driving
- 100% UIS Tested
- Easy to parallel
- RoHS compliant
- Automotive Qualified (AEC-Q101)

3. Applications

- Automotive on board chargers
- Automotive DC-DC converters
- Automotive electric compressor motor drives
- HV battery management systems

4. Quick reference data

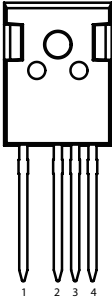
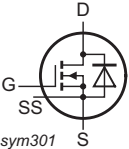
Table 1. Quick reference data

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Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute maximum rating							
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		750			V
I _D	drain current	V _{GS} = 18 V; T _{mb} = 25 °C		106			A
P _{tot}	total power dissipation	T _{mb} = 25 °C, T _j = 175 °C		375			W
T _j	junction temperature			-55 to 175			°C
Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
R _{DS(on)}	drain-source on-state resistance	V _{GS} = 15 V; I _D = 30 A; T _j = 25 °C		-	30	-	mΩ
		V _{GS} = 18 V; I _D = 30 A; T _j = 25 °C		-	23	30	mΩ
Dynamic characteristics							
Q _{G(tot)}	total gate charge	I _D = 30 A; V _{DS} = 400 V; V _{GS} = -4 V/18 V; T _j = 25 °C		-	117	-	nC
Q _{GD}	gate-drain charge			-	14.7	-	nC
Source-drain diode							
Q _r	recovered charge	I _{SD} = 30 A; di/dt = 500 A/μs; V _{DS} = 400 V; T _j = 25 °C		-	127	-	nC

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain		
2	S	source		
3	SS	source sense		
4	G	gate		
mb	D	mounting base; connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSC2M30075R-A	TO247-4L	WNSC2M30075R-A6Q	Tube	30	TO247N-4L	17-Dec-2021

7. Marking

Table 4. Marking codes

Type number	Marking codes
WNSC2M30075R-A	WNSC2M 30075R-A

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V_{DS}	drain-source voltage	$25\text{ °C} \leq T_J \leq 175\text{ °C}$		750	V
$V_{GS,max}$	gate-source voltage	Absolute maximum values		-10 to 22	V
$V_{GS,op}$	gate-source voltage	Recommended operational values		-4 to 18	V
P_{tot}	total power dissipation	$T_{mb} = 25\text{ °C}$; $T_J = 175\text{ °C}$		375	W
I_D	drain current	$V_{GS} = 18\text{ V}$; $T_{mb} = 25\text{ °C}$		106	A
		$V_{GS} = 18\text{ V}$; $T_{mb} = 100\text{ °C}$		75	A
I_{DM}	peak drain current	pulse width t_p limited by T_{Jmax}	Fig.17	211	A
I_S	continuous diode current	$V_{GS} = -4\text{ V}$; $T_{mb} = 25\text{ °C}$		70	A
I_{SM}	pulse diode current	$V_{GS} = -4\text{ V}$; pulse width t_p limited by T_{Jmax}		211	A
E_{as}	single pulse drain-to-source avalanche	$I_{AS} = 27.2\text{ A}$; $L = 1\text{ mH}$; $V_{DD} = 100\text{ V}$; $T_J = 25\text{ °C}$		369	mJ
T_{stg}	storage temperature			-55 to 175	°C
T_J	junction temperature			-55 to 175	°C
$T_{sld(M)}$	peak soldering temperature			260	°C

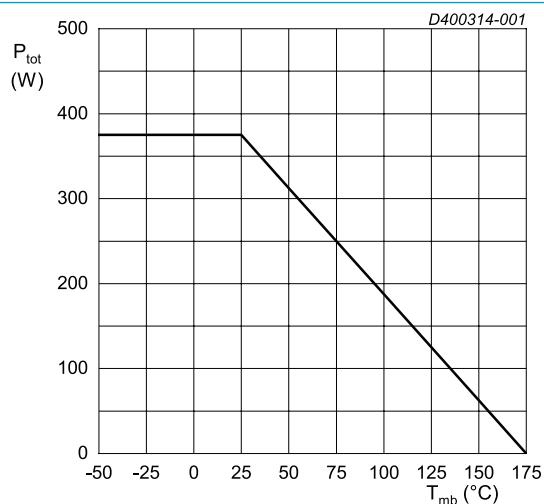


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

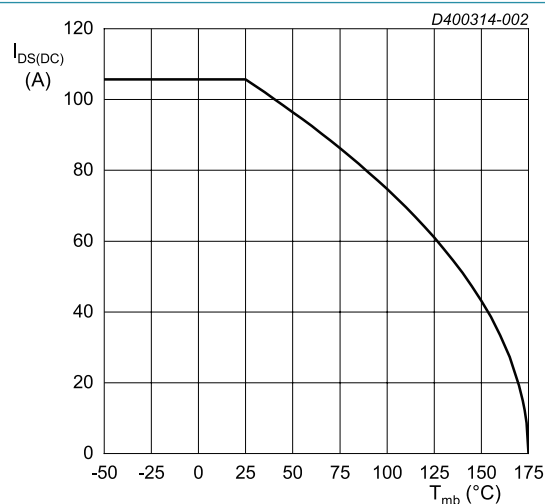


Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base			-	0.40	-	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air		-	40	-	K/W
M_d	Mounting torque	M3 or 6 - 32 screw		-	-	0.6	Nm

Note: It is recommended that a metal washer is inserted between screw head and mounting tab.
Do not use self-tapping screws.
Device is ESD sensitive. Handling precautions are recommended.

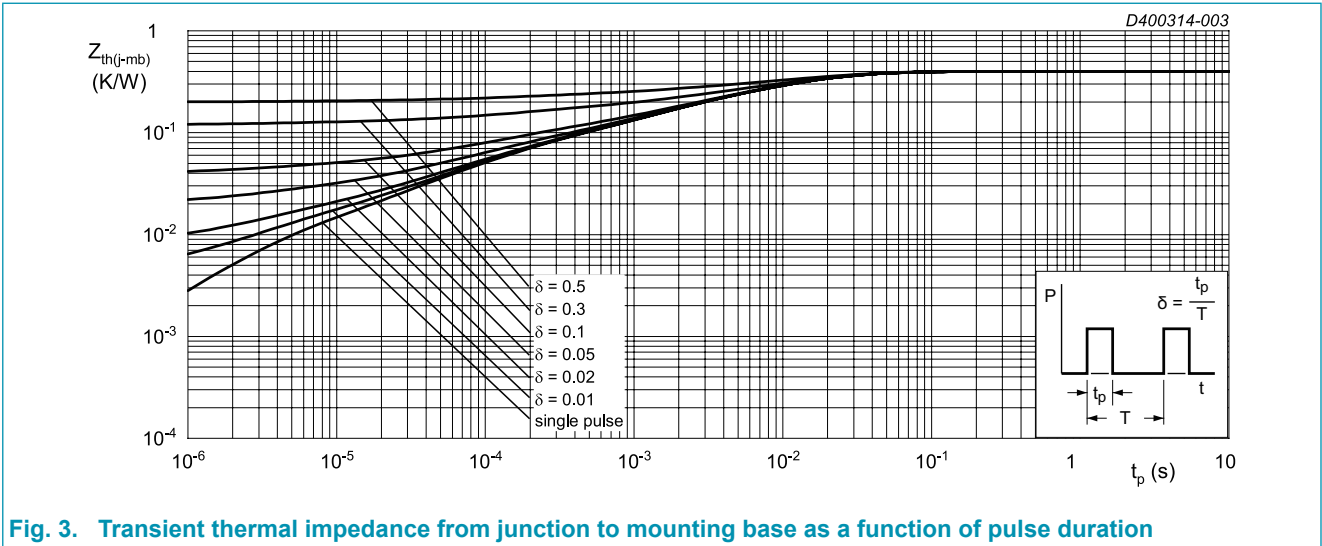


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 100 μA; V _{GS} = 0 V; T _J = 25 °C		750	-	-	V
V _{GS(th)}	gate-source threshold voltage	I _D = 8.5 mA; V _{DS} = V _{GS} ; T _J = 25 °C		2	2.8	3.6	V
		I _D = 8.5 mA; V _{DS} = V _{GS} ; T _J = 175 °C		-	2.1	-	V
I _{DSS}	drain leakage current	V _{DS} = 750 V; V _{GS} = 0 V; T _J = 25 °C		-	0.1	50	μA
		V _{DS} = 750 V; V _{GS} = 0 V; T _J = 175 °C		-	5	-	μA
I _{GSS}	gate leakage current	V _{GS} = 22 V; V _{DS} = 0 V; T _J = 25 °C		-	5	100	nA
		V _{GS} = -10 V; V _{DS} = 0 V; T _J = 25 °C		-	5	100	nA
R _{DS(on)}	drain-source on-state resistance	V _{GS} = 15 V; I _D = 30 A; T _J = 25 °C		-	30	-	mΩ
		V _{GS} = 18 V; I _D = 30 A; T _J = 25 °C		-	23	30	mΩ
		V _{GS} = 18 V; I _D = 30 A; T _J = 175 °C		-	28	-	mΩ
R _G	gate resistance	f = 1 MHz; T _J = 25 °C		-	1.05	-	Ω
g _{fs}	transconductance	V _{DS} = 20 V; I _D = 30 A; T _J = 25 °C		-	21.5	-	S
Dynamic characteristics							
Q _{G(tot)}	total gate charge	I _D = 30 A; V _{DS} = 400 V; V _{GS} = -4 V/18 V; T _J = 25 °C		-	117	-	nC
Q _{GS}	gate-source charge			-	49	-	nC
Q _{GD}	gate-drain charge			-	14.7	-	nC
C _{iss}	input capacitance	V _{DS} = 400 V; V _{GS} = 0 V; f = 1 MHz; T _J = 25 °C		-	2781	-	pF
C _{oss}	output capacitance			-	197	-	pF
C _{rss}	reverse transfer capacitance			-	13.5	-	pF
E _{oss}	Coss stored energy			-	15.8	-	μJ
t _{d(on)}	turn-on delay time	V _{DS} = 400 V; V _{GS} = -4 V/18 V; R _{G(ext)} = 5.1 Ω; I _D = 15 A; L = 100 μH; T _J = 25 °C		-	26	-	ns
t _r	rise time			-	18	-	ns
t _{d(off)}	turn-off delay time			-	52	-	ns
t _f	fall time			-	16	-	ns
E _{on}	turn-on energy (SiC Diode FWD)		Fig.19	-	75	-	μJ
E _{off}	turn-off energy (SiC Diode FWD)		Fig.19	-	17	-	μJ
E _{on}	turn-on energy (Body Diode FWD)		Fig.19	-	122	-	μJ
E _{off}	turn-off energy (Body Diode FWD)		Fig.19	-	16	-	μJ
Source-drain diode							
V _{SD}	source-drain voltage	V _{GS} = 0 V; I _{SD} = 30 A; T _J = 25 °C		-	3.7	-	V
		V _{GS} = -4 V; I _{SD} = 30 A; T _J = 25 °C		-	4.2	-	V
		V _{GS} = -4 V; I _{SD} = 30 A; T _J = 175 °C		-	3.7	-	V
t _{rr}	reverse recovery time	I _{SD} = 30 A; di/dt = 500 A/μs; V _{DS} = 400 V; T _J = 25 °C		-	34.1	-	ns
Q _r	recovered charge			-	127	-	nC
I _{rrm}	reverse recovery current			-	7.4	-	A

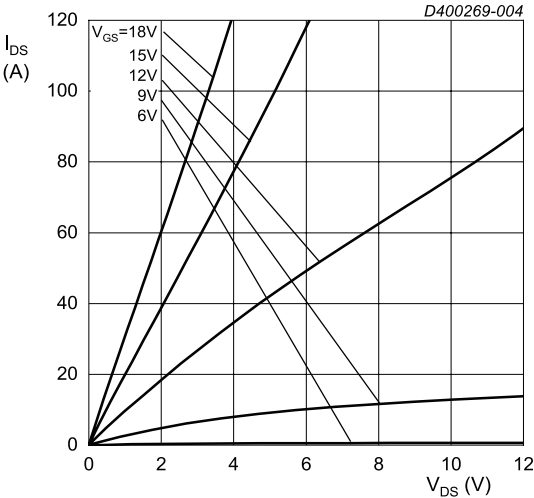


Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values

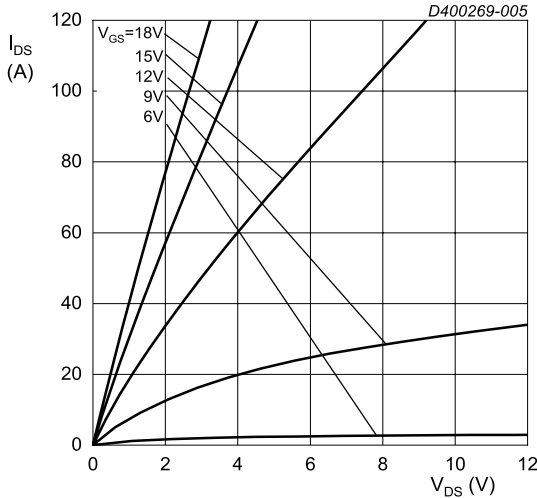


Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values

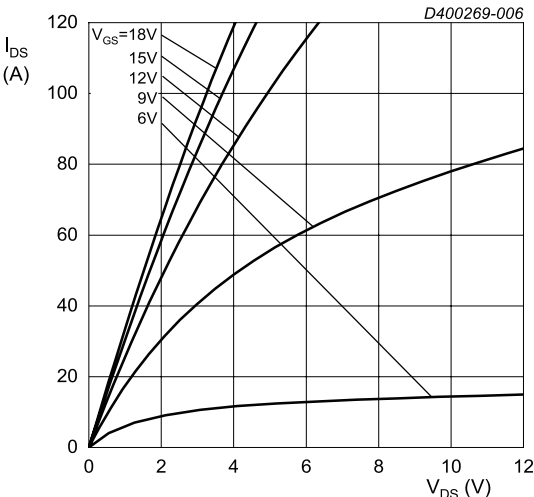


Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

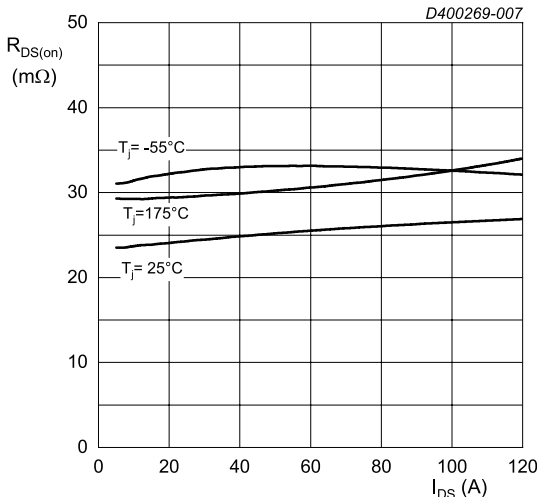
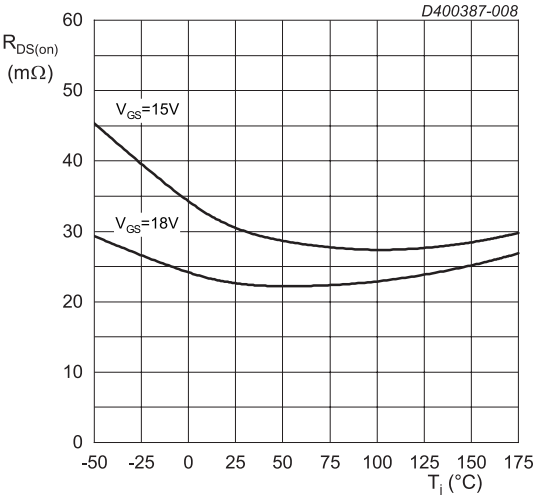
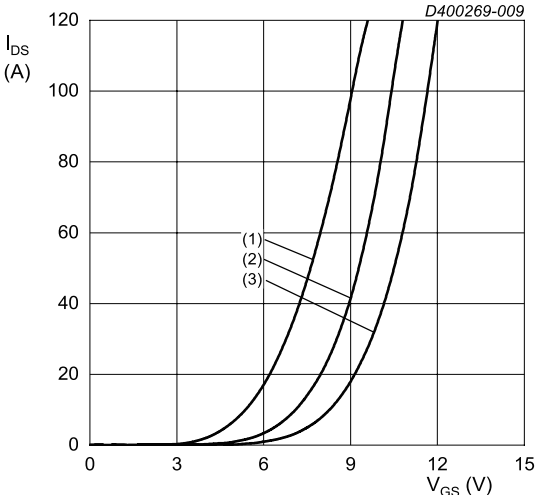


Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



$I_{DS} = 30\text{ A}$; $t_p < 200\text{ }\mu\text{s}$

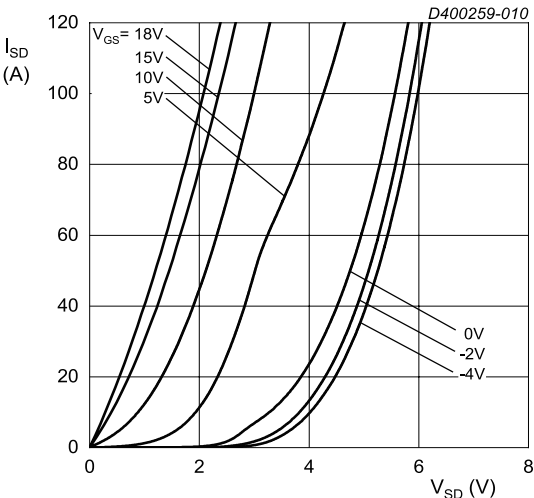
Fig. 8. Drain-source on-state resistance as a function of junction temperature



$V_{DS} = 20\text{ V}$; $t_p < 200\text{ }\mu\text{s}$

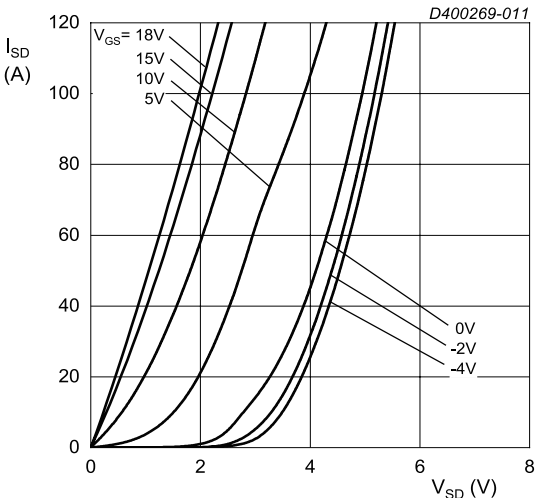
- (1) $T_j = 175\text{ }^\circ\text{C}$
- (2) $T_j = 25\text{ }^\circ\text{C}$
- (3) $T_j = -55\text{ }^\circ\text{C}$

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values



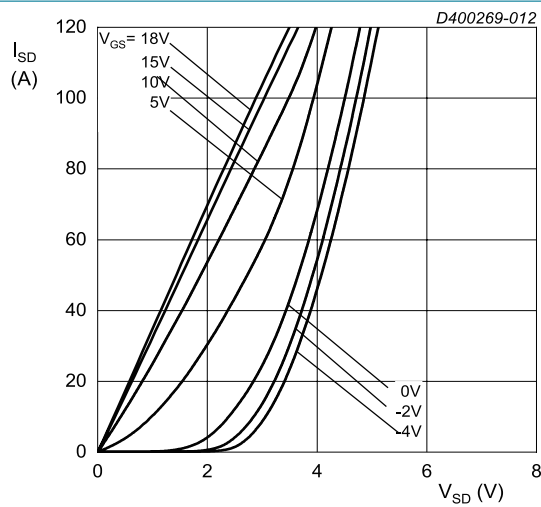
$T_j = -55\text{ }^\circ\text{C}$; $t_p < 200\text{ }\mu\text{s}$

Fig. 10. Body diode forward characteristics; typical values

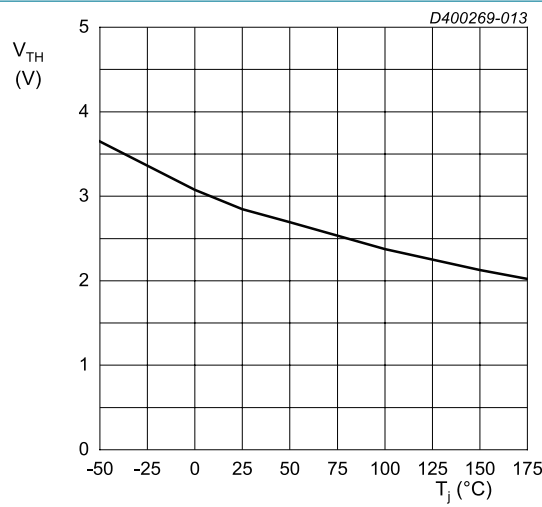


$T_j = 25\text{ }^\circ\text{C}$; $t_p < 200\text{ }\mu\text{s}$

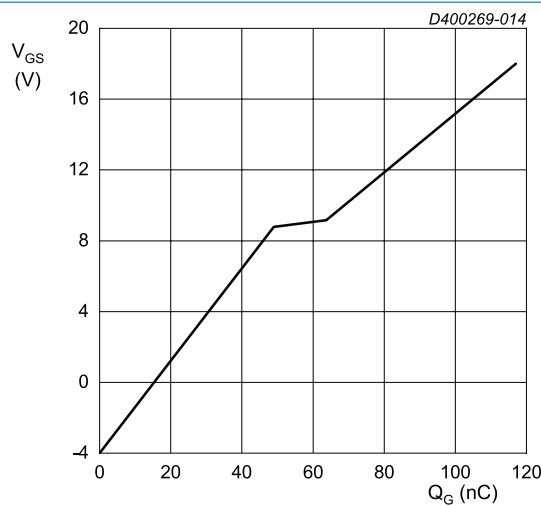
Fig. 11. Body diode forward characteristics; typical values



$T_j = 175\text{ }^{\circ}\text{C}$; $t_p < 200\text{ }\mu\text{s}$
Fig. 12. Body diode forward characteristics; typical values



$V_{DS} = V_{GS}$; $I_{DS} = 8.5\text{ mA}$
Fig. 13. Threshold voltage as a function of junction temperature



$I_{DS} = 30\text{ A}$; $I_{GS} = 0.1\text{ mA}$; $V_{DS} = 400\text{ V}$; $T_j = 25\text{ }^{\circ}\text{C}$
Fig. 14. Gate-source voltage as a function of gate charge; typical values

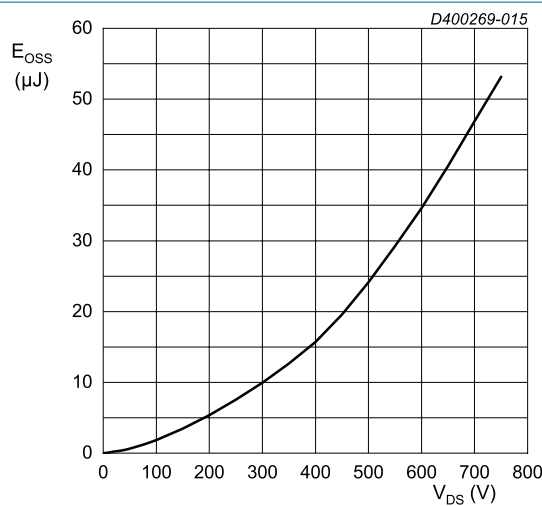
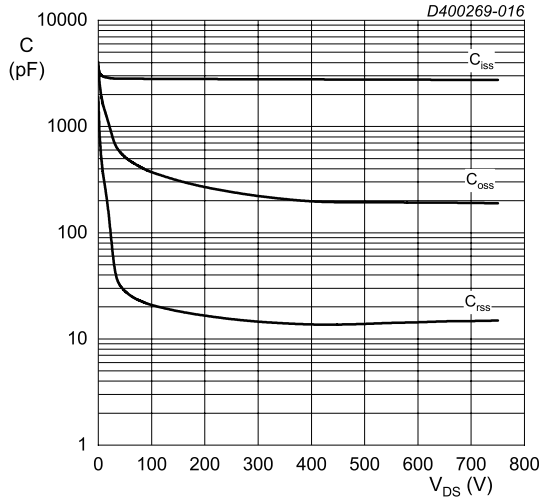
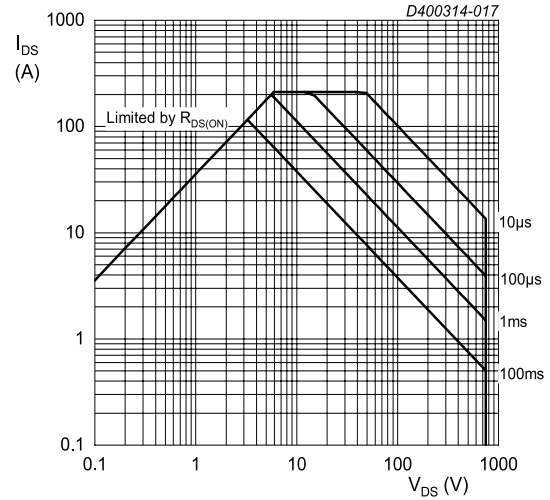


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



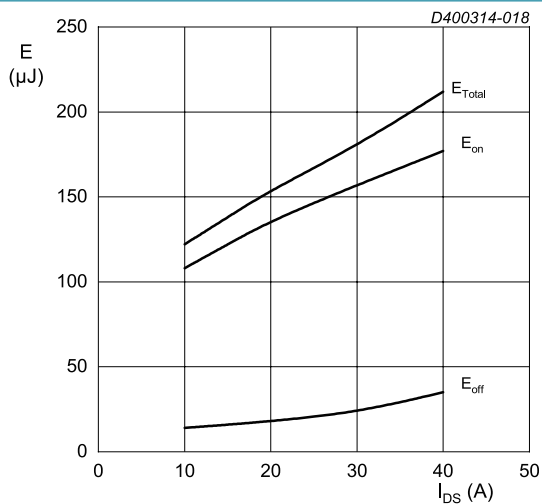
$V_{DS} = 0 - 750$ V
 $T_j = 25$ °C; $V_{AC} = 25$ mV; $f = 1$ MHz

Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



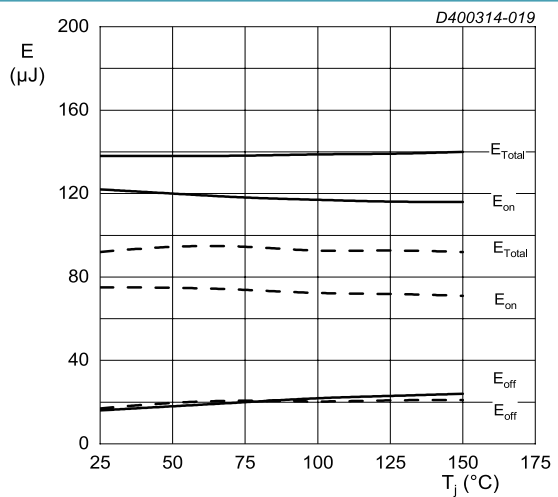
$T_j = 25$ °C; $D = 0$
 Parameter: t_p

Fig. 17. Forward bias safe operating area



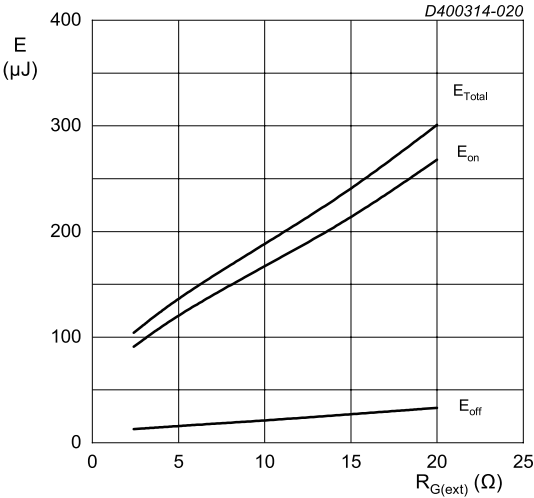
$T_j = 25$ °C; $V_{DD} = 400$ V; $R_{G(ext)} = 5.1$ Ω;
 $V_{GS} = -4$ V/18 V; $L = 100$ μH
 FWD = WNSC2M30075R-A

Fig. 18. Clamped Inductive Switching Energy as a function of drain current



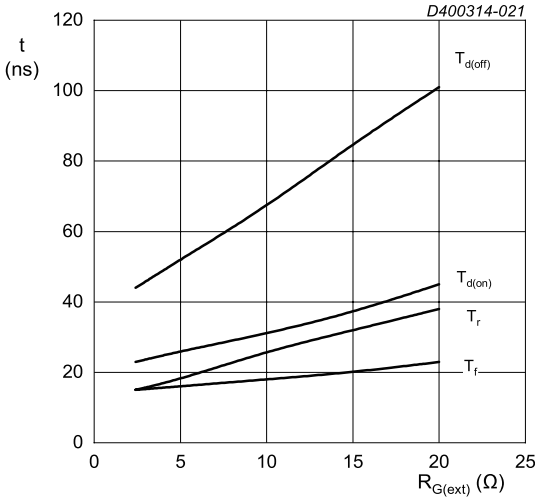
$I_{DS} = 15$ A; $V_{DD} = 400$ V; $R_{G(ext)} = 5.1$ Ω;
 $V_{GS} = -4$ V/18 V; $L = 100$ μH
 FWD = WNSC2M30075R-A
 FWD = WNSC6D30650W(---)

Fig. 19. Clamped Inductive Switching Energy as a function of junction temperature



$T_j = 25\text{ }^\circ\text{C}$; $V_{DD} = 400\text{ V}$; $I_{DS} = 15\text{ A}$; $V_{GS} = -4\text{ V}/18\text{ V}$
FWD = WNSC2M30075R-A; $L = 100\text{ }\mu\text{H}$

Fig. 20. Clamped Inductive Switching Energy as a function of external gate resistance



$T_j = 25\text{ }^\circ\text{C}$; $V_{DD} = 400\text{ V}$; $I_{DS} = 15\text{ A}$; $V_{GS} = -4\text{ V}/18\text{ V}$
FWD = WNSC2M30075R-A; $L = 100\text{ }\mu\text{H}$

Fig. 21. Switching time as a function of external gate resistance

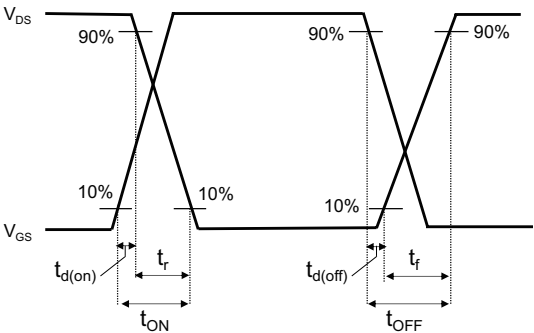
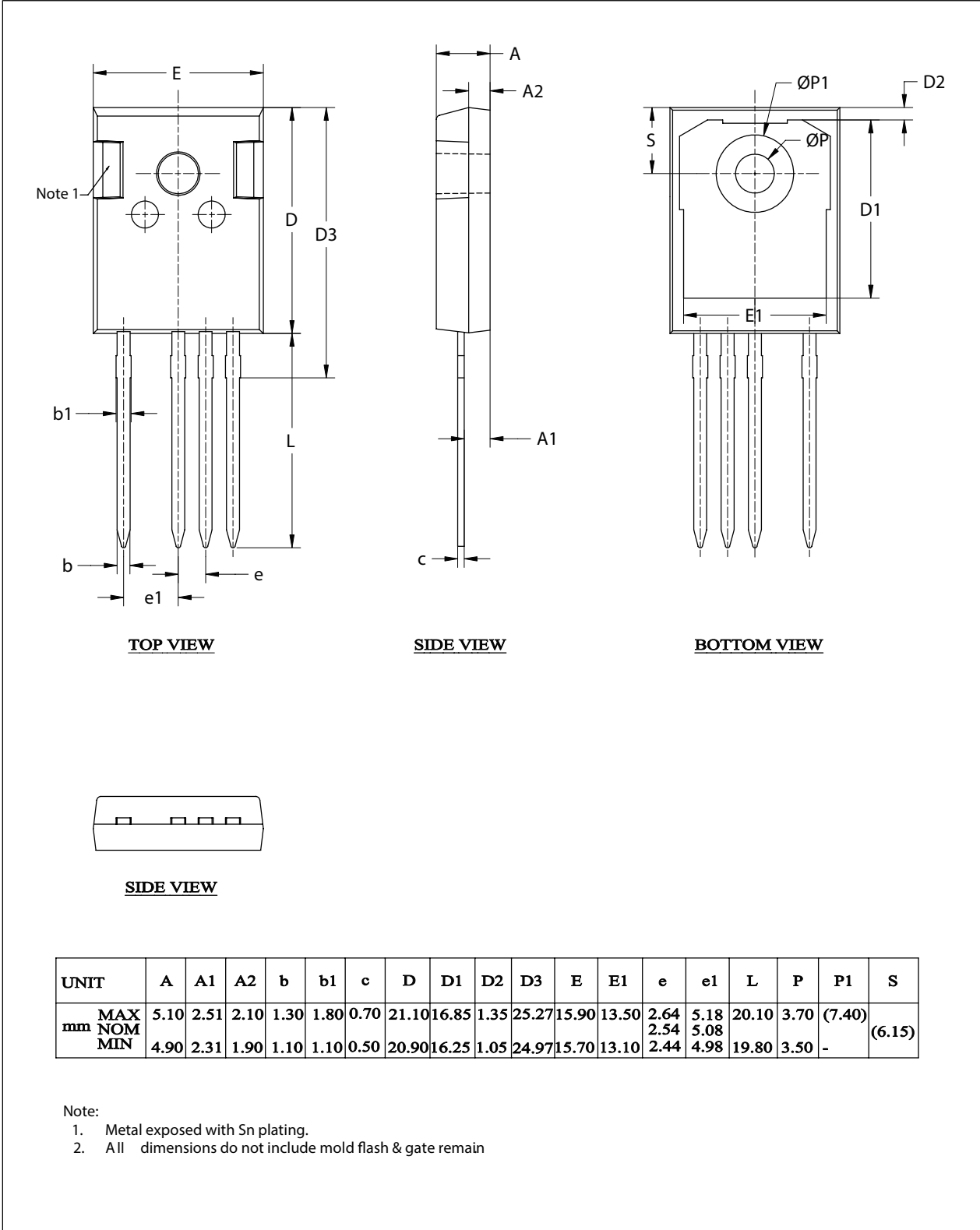


Fig. 22. Switching time definition

11. Package outline

Plastic single-ended through-hole package; heatsink mounted; 1 mounting hole; 4 leads TO-247

TO247-4L



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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