Product data sheet

1. General description

Silicon Carbide MOSFET in a TO247-4L NL plastic package, designed for high frequency, high efficiency systems.



2. Features and benefits

- · Separate driver source pin
- · Low on-resistance
- Fast switching speed
- · 0V turn-off gate voltage for simple gate drive
- 100% UIS Tested
- Easy to parallel
- Controllable dV/dt for optimized EMI
- Reduced cooling requirements
- RoHS compliant

3. Applications

- Switch Mode Power Supplies
- UPS
- · Solar string inverter and solar optimizer
- EV Charger
- Motor Drives

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute	maximum rating						
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		1200			V
I _D	drain current	V _{GS} = 18 V; T _{mb} = 25 °C			216		А
P _{tot}	total power dissipation	T _{mb} = 25 °C, T _j = 175 °C			1071		
T _j	junction temperature			-55 to 175			°C
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
R _{DS(on)}	drain-source on-state	$V_{GS} = 15 \text{ V}; I_D = 75 \text{ A}; T_j = 25 \text{ °C}$		-	17	-	mΩ
resistar	resistance	V_{GS} = 18 V; I_D = 75 A; T_j = 25 °C		-	12	18	mΩ
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	$I_D = 75 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	321	-	nC
Q_{GD}	gate-drain charge	T _j = 25 °C		-	57	-	nC
Source-di	rain diode					•	
Q _r	recovered charge	$I_{SD} = 50 \text{ A}$; di/dt = 500 A/ μ s; $V_{DS} = 400 \text{ V}$; $T_j = 25 ^{\circ}\text{C}$		-	250	-	nC

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	D	drian		D				
2	S	source						
3	SS	source sense		$G \longrightarrow A$				
4	G	gate		SS				
mb	D	mounting base; connected to drain	1 2 3 4					

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity		Package issue date
WNSC2M12120RS	TO247-4L NL	WNSC2M12120RS6Q	Tube	30	TO247N-4L NL	10-Jun-2025

7. Marking

Table 4. Marking codes

Type number	Marking codes
WNSC2M12120RS	WNSC2M 12120RS

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		1200	V
$V_{\rm GS,max}$	gate-source voltage	Absolute maximum values		-12 to 24	V
$V_{GS,op}$	gate-source voltage	Recommended operational values		-4 to 18	V
P _{tot}	total power dissipation	T _{mb} = 25 °C, T _j = 175 °C		1071	W
I _D	drain current	V _{GS} = 18 V; T _{mb} = 25 °C		216	А
		V _{GS} = 18 V; T _{mb} = 100 °C		152.8	Α
I _{DM}	peak drain current	pulse width t _p limited by T _{jmax}	Fig.17	430	Α
Is	continuous diode current	V _{GS} = -4 V; T _{mb} = 25 °C		148	А
I _{SM}	pulse diode current	V_{GS} = -4 V; pulse width t_p limited by T_{jmax}		430	А
E _{as}	single pulse drain-to- source avalanche	$I_{AS} = 35 \text{ A}; L = 1 \text{ mH}; V_{DD} = 100 \text{ V};$ $T_j = 25 \text{ °C}$		612.5	mJ
T _{stg}	storage temperature			-55 to 175	°C
T _j	junction temperature			-55 to 175	°C
$T_{sld(M)}$	peak soldering temperature			260	°C

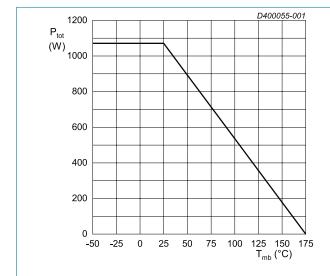


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

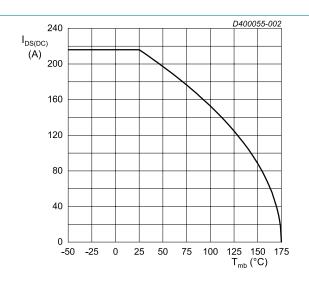


Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base			-	0.14	-	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air		-	40	-	K/W
M _d	Mounting torque	M3 or 6 - 32 screw		-	-	0.6	Nm

Note: It is recommended that a metal washer is inserted between screw head and mounting tab.

Do not use self-tapping screws.

Device is ESD sensitive. Handling precautions are recommended.

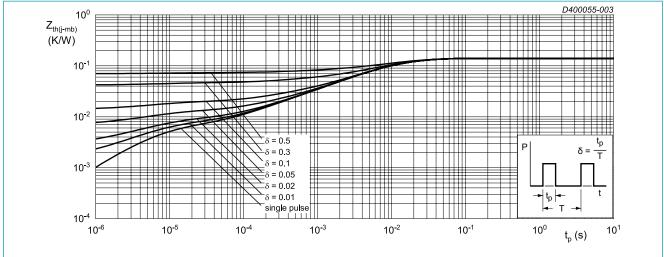


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 100 \mu A; V_{GS} = 0 V; T_j = 25 °C$		1200	-	-	V
V _{GS(th)} gate-source threshold voltage		$I_D = 28 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 25 \text{ °C}$		1.9	2.6	3.5	V
		$I_D = 28 \text{ mA}; V_{DS} = 10 \text{ V}; T_j = 175 °C$		-	1.9	-	V
I _{DSS}	drain leakage current	$V_{DS} = 1200 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	0.2	100	μA
		$V_{DS} = 1200 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$		-	2	-	μA
I _{GSS}	gate leakage current	$V_{GS} = 24 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	10	100	nA
		$V_{GS} = -12 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	10	100	nA
R _{DS(on)}	drain-source on-state	$V_{GS} = 15 \text{ V}; I_D = 75 \text{ A}; T_j = 25 \text{ °C}$		-	17	-	mΩ
	resistance	$V_{GS} = 18 \text{ V}; I_D = 75 \text{ A}; T_j = 25 \text{ °C}$		-	12	18	mΩ
		$V_{GS} = 18 \text{ V}; I_D = 75 \text{ A}; T_j = 175 \text{ °C}$		-	22	-	mΩ
R_{G}	gate resistance	f = 1 MHz; T _j = 25 °C		-	0.53	-	Ω
g _{fs}	transconductance	$V_{DS} = 20 \text{ V}; I_D = 75 \text{ A}; T_j = 25 \text{ °C}$		-	56	-	S
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	$I_D = 75 \text{ A}; V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V};$		-	321	-	nC
Q _{GS}	gate-source charge	T _j = 25 °C		-	115	-	nC
Q_{GD}	gate-drain charge			-	57	-	nC
C _{iss}	input capacitance	$V_{DS} = 1000 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$		-	6782	-	pF
C _{oss}	output capacitance	T _j = 25 °C		-	289	-	pF
C _{rss}	reverse transfer capacitance			-	23	-	pF
E _{oss}	Coss stored energy			-	144.5	-	μJ
t _{d(on)}	turn-on delay time	$V_{DS} = 800 \text{ V}; V_{GS} = -4 \text{ V}/18 \text{ V}; R_{G(ext)} = 2.4$		-	4	-	ns
t _r	rise time	Ω ; $I_D = 75 \text{ A}$; $L = 100 \mu\text{H}$; $T_j = 25 \degree \text{C}$		-	36	-	ns
$t_{\text{d(off)}}$	turn-off delay time			-	45	-	ns
t _f	fall time			-	26	-	ns
E _{on}	turn-on energy (SIC Diode FWD)		Fig.20	-	1103	-	μJ
E _{off}	turn-off energy (SiC Diode FWD)		Fig.20	-	229	-	μJ
E _{on}	turn-on energy (Body Diode FWD)		Fig.20	-	1442	-	μJ
E _{off}	turn-off energy (Body Diode FWD)		Fig.20	-	170	-	μJ
Source-d	ain diode					'	'
V _{SD}	source-drain voltage	$V_{GS} = 0 \text{ V; } I_{SD} = 37.5 \text{ A; } T_j = 25 ^{\circ}\text{C}$		-	2.9	-	V
		V_{GS} = -4 V; I_{SD} = 37.5 A; T_j = 25 °C		-	4.7	-	V
		$V_{GS} = -4 \text{ V}; I_{SD} = 37.5 \text{ A}; T_j = 175 \text{ °C}$		-	4.1	-	V
t _{rr}	reverse recovery time	$I_{SD} = 50 \text{ A}$; di/dt = 500 A/ μ s; $V_{DS} = 400 \text{ V}$;		-	45.5	-	ns
Q_r	recovered charge	T _j = 25 °C		-	250	-	nC
I _{rrm}	reverse recovery current			-	10	-	Α

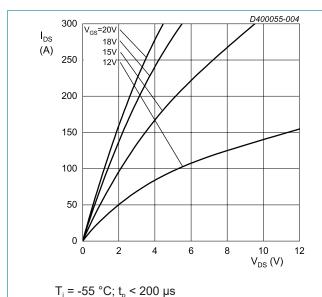
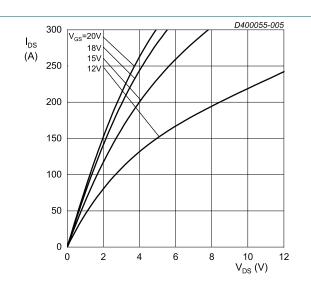
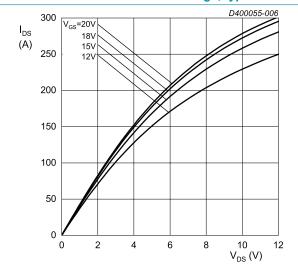


Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values

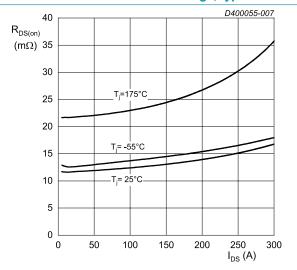


 $T_j = 25 \, ^{\circ}C; \, t_p < 200 \, \mu s$

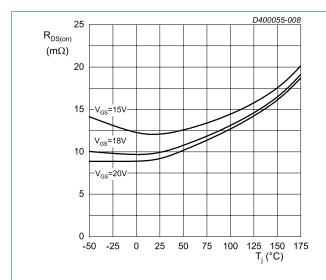
Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



 $T_j = 175~^{\circ}C; \, t_p < 200~\mu s$ Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

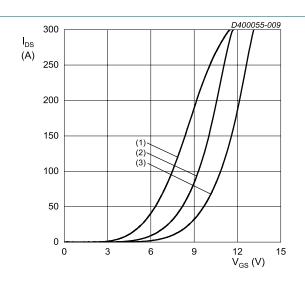


 V_{GS} = 18 V; t_p < 200 µs Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



 I_{DS} = 75 A; t_p < 200 μs

Fig. 8. Drain-source on-state resistance as a function of junction temperature



$$V_{DS}$$
 = 20 V; t_p < 200 μ s

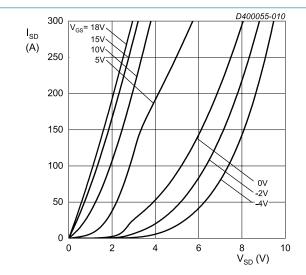
(1)
$$T_j = 175 \,^{\circ}\text{C}$$

(2) $T_j = 25 \,^{\circ}\text{C}$

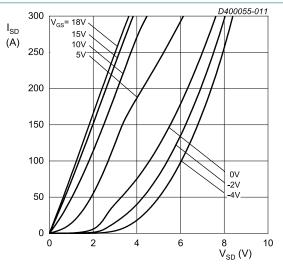
$$(2) T_i = 25 °C$$

$$(3) T_i = -55 ^{\circ}C$$

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values

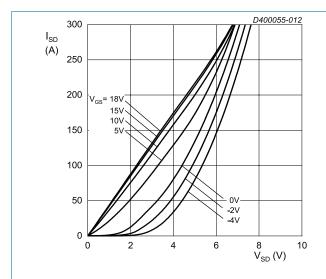


 $T_{j} = -55 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$ Fig. 10. Body diode forward characteristics; typical values

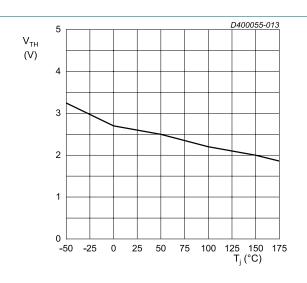


 $T_{j} = 25 \, ^{\circ}\text{C}; t_{p} < 200 \, \mu\text{s}$

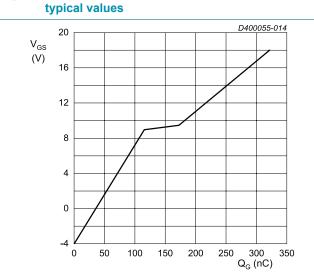
Fig. 11. Body diode forward characteristics; typical values



 T_{j} = 175 °C; t_{p} < 200 μs Fig. 12. Body diode forward characteristics;



V_{DS} = 10 V; I_{DS} = 28 mA Fig. 13. Threshold voltage as a function of junction temperature



I_{DS} = 75 A; I_{GS} = 0.1 mA; V_{DS} = 800 V; T_j = 25 °C Fig. 14. Gate-source voltage as a function of gate charge; typical values

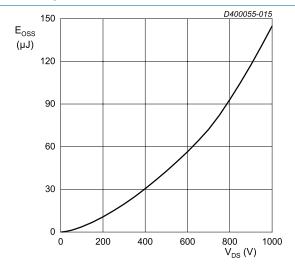
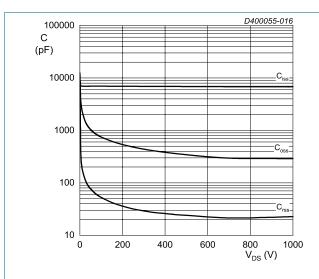
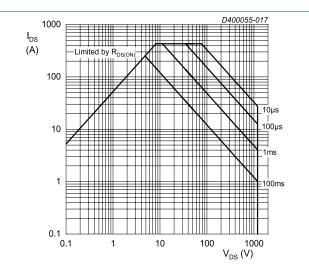


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



 $V_{DS} = 0 - 1000 V$

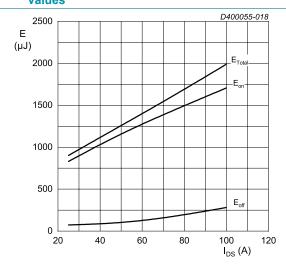
 $T_j = 25 \, ^{\circ}C; \, V_{AC} = 25 \, \text{mV}; \, f = 1 \, \text{MHz}$



T_j = 25 °C; D = 0 Parameter: t_o

Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

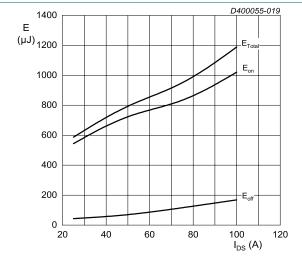




 T_{j} = 25 °C; V_{DD} = 800 V; $R_{G(ext)}$ = 2.4 $\Omega;$ V_{GS} = -4 V/18 V; L = 100 μH

FWD = WNSC2M12120RS

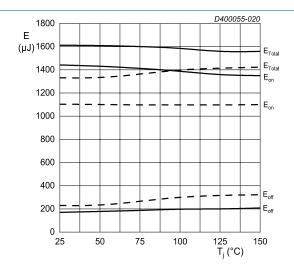
Fig. 18. Clamped Inductive Switching Energy as a function of drain current



 T_{j} = 25 °C; V_{DD} = 600 V; $R_{G(ext)}$ = 2.4 $\Omega;$ V_{GS} = -4 V/18 V; L = 100 μH

 $V_{GS} = -4 \text{ V}/18 \text{ V}; L = 100 \mu\text{H}$ FWD = WNSC2M12120RS

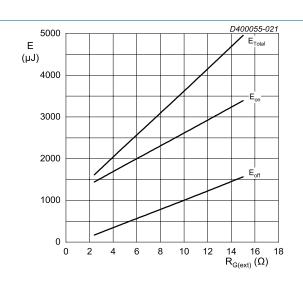
Fig. 19. Clamped Inductive Switching Energy as a function of drain current



 $I_{DS} = 75$ A; $V_{DD} = 800$ V; $R_{G(ext)} = 2.4~\Omega;$ $V_{GS} = -4$ V/18 V; $L = 100~\mu H$

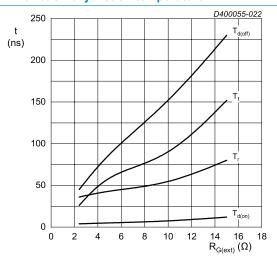
FWD = WNSC2M12120RS FWD = WNSC2D501200W(- - -)

Fig. 20. Clamped Inductive Switching Energy as a function of junction temperature



 $T_{\rm j}$ = 25 °C; $V_{\rm DD}$ = 800 V; $I_{\rm DS}$ = 75 A; $V_{\rm GS}$ = -4 V/18 V FWD = WNSC2M12120RS; L = 100 μH

Fig. 21. Clamped Inductive Switching Energy as a function of external gate resistance



 T_{i} = 25 °C; V_{DD} = 800 V; I_{DS} = 75 A; V_{GS} = -4 V/18 V FWD = WNSC2M12120RS; L = 100 μ H

Fig. 22. Switching time as a function of external gate resistance

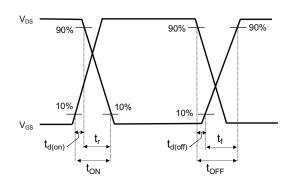
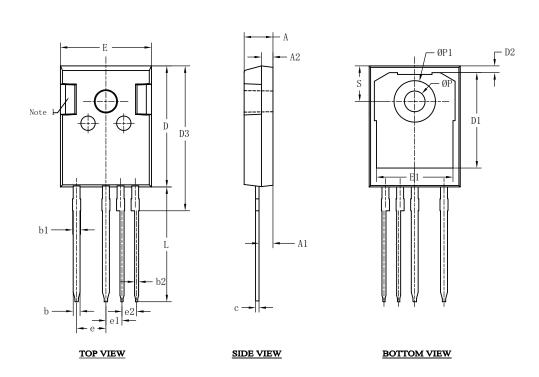


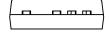
Fig. 23. Switching time definition

11. Package outline

Plastic single-ended through-hole package; headsink mounted; 1 mounting hole; 4 leads TO-247 Narrow Leads

TO247-4L NL





SIDE VIEW

UNIT A	A A	. A2	ъ	b 1	b2	С	D	D1	D2	D3	E	E1	е	e1	e2	L	P	P1	S
mm NOM MIN 4 6		1 2.10 1 1.90											5.08	2.79	2.54			` 1	6.30

Note:

- 1. Metal exposed.
- 2. All dimensions do not include mold flash & gate remain

WNSC2M12120RS

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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WNSC2M12120RS

N-Channel Silicon Carbide MOSFET

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