WNSC2M70075TB-A

N-Channel Silicon Carbide MOSFET

Rev.02 - 19 December 2025 Product data sheet

1. General description

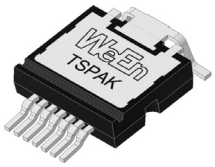
WeEn Gen-2 Silicon Carbide MOSFET in a TSPAK plastic package, featured with top side cooling structure, designed for high frequency, high efficiency systems.

RoHS Halogen-Free

AEC - Q101 Qualified

2. Features and benefits

- Top side cooling structure
- Kelvin source configuration
- Low specific on-resistance
- Optimized dynamic performance
- 0V turn-off V_{GS} for simple gate driving
- 100% UIS Tested
- Easy to parallel
- RoHS compliant
- Automotive Qualified (AEC-Q101)



3. Applications

- Automotive on board chargers
- Automotive DC-DC converters
- Automotive electric compressor motor drives
- HV battery management systems

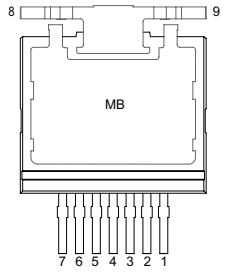
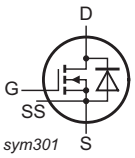
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute maximum rating							
V_{DS}	drain-source voltage	25 °C ≤ T_j ≤ 175 °C		750			V
I_D	drain current	V_{GS} = 18 V; T_{mb} = 25 °C		51			A
P_{tot}	total power dissipation	T_{mb} = 25 °C, T_j = 175 °C		208			W
T_j	junction temperature			-55 to 175			°C
Symbol	**Parameter**	**Conditions**	**Notes**	**Min**	**Typ**	**Max**	**Unit**
Static characteristics							
$R_{DS(on)}$	drain-source on-state resistance	V_{GS} = 15 V; I_D = 15 A; T_j = 25 °C		-	71.5	-	mΩ
		V_{GS} = 18 V; I_D = 15 A; T_j = 25 °C		-	55	72	mΩ
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	I_D = 15 A; V_{DS} = 400 V; V_{GS} = -4 V/18 V; T_j = 25 °C		-	53	-	nC
Q_{GD}	gate-drain charge			-	7.3	-	nC
Source-drain diode							
Q_r	recovered charge	I_{SD} = 15 A; di/dt = 500 A/µs; V_{DS} = 400 V; T_j = 25 °C		-	58.6	-	nC

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	SS	source sense		
3-7	S	source		
8-9 mb	D	mounting base; connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WNSC2M70075TB-A	TSPAK	WNSC2M70075TB-A6J	Reel	600	TSPAKH	06-Dec-2024

7. Marking

Table 4. Marking codes

Type number	Marking codes
WNSC2M70075TB-A	WNSC2M 70075TB-A

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V_{DS}	drain-source voltage	25 °C ≤ T_j ≤ 175 °C		750	V
$V_{GS,max}$	gate-source voltage	Absolute maximum values		-10 to 22	V
$V_{GS,op}$	gate-source voltage	Recommended operational values		-4 to 18	V
P_{tot}	total power dissipation	T_{mb} = 25 °C, T_j = 175 °C		208	W
I_D	drain current	V_{GS} = 18 V; T_{mb} = 25 °C		51	A
		V_{GS} = 18 V; T_{mb} = 100 °C		36	A
I_{DM}	peak drain current	pulse width t_p limited by T_{jmax}	Fig.17	103	A
I_S	continuous diode current	V_{GS} = -4 V; T_{mb} = 25 °C		37	A
I_{SM}	pulse diode current	V_{GS} = -4 V; pulse width t_p limited by T_{jmax}		103	A
E_{as}	single pulse drain−to-source avalanche	I_{AS} = 17.2 A; L = 1 mH; V_{DD} = 100 V; T_j = 25 °C		147	mJ
T_{stg}	storage temperature			-55 to 175	°C
T_j	junction temperature			-55 to 175	°C
$T_{sld(M)}$	peak soldering temperature			260	°C

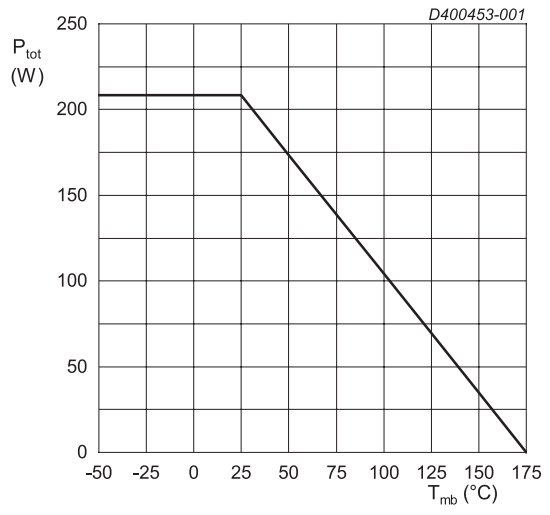


Fig. 1. Total power dissipation as a function of mounting base temperature; maximum values

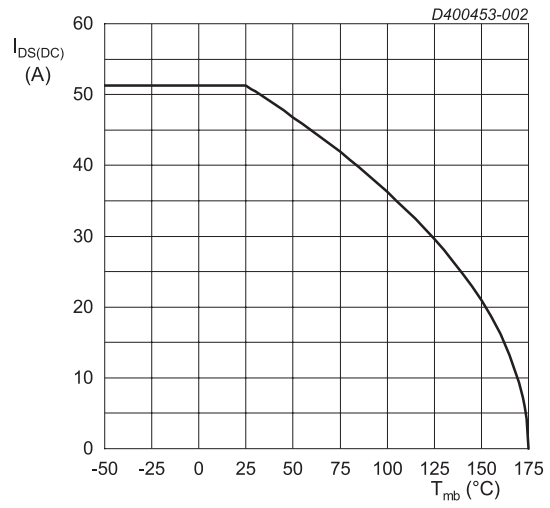


Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j\text{-}mb)}$	thermal resistance from junction to mounting base			-	0.72	-	K/W
$R_{th(j\text{-}a)}$	thermal resistance from junction to ambient	in free air		-	40	-	K/W

Note: Device is ESD sensitive. Handling precautions are recommended.

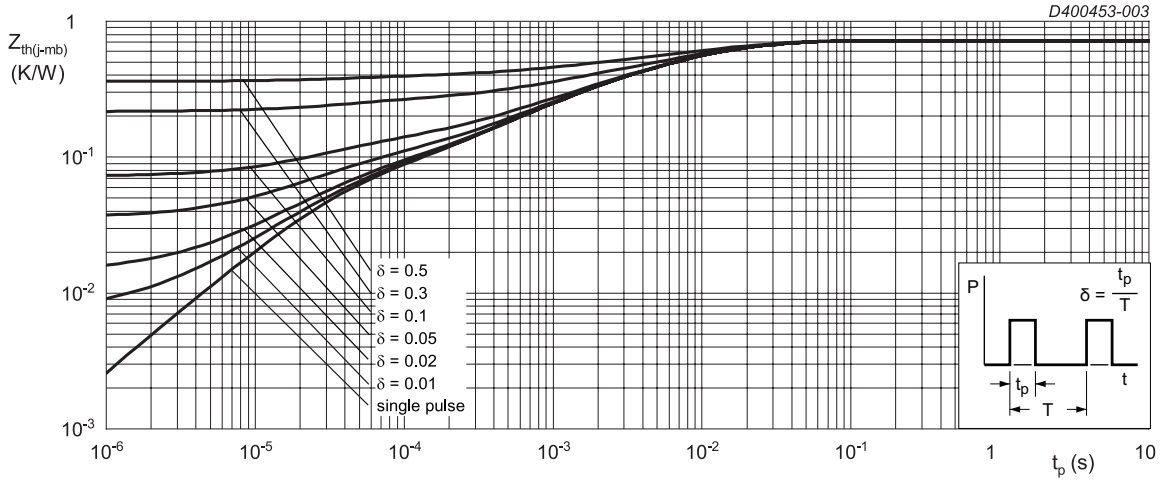
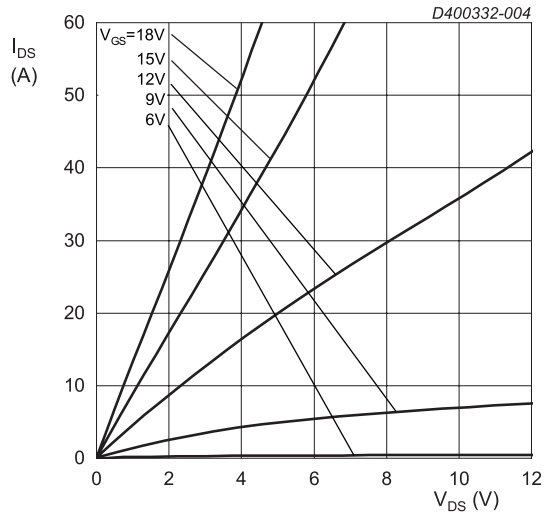


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

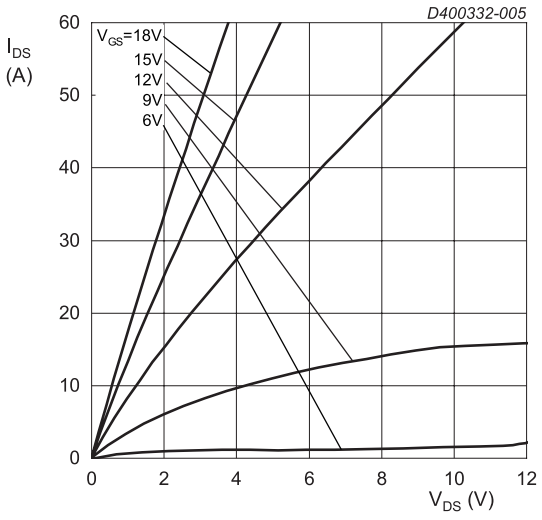
Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	I_D = 100 µA; V_{GS} = 0 V; T_j = 25 °C		750	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	I_D = 4 mA; V_{DS} = V_{GS}; T_j = 25 °C		2.0	2.8	3.6	V
		I_D = 4 mA; V_{DS} = V_{GS}; T_j = 175 °C		-	2.1	-	V
I_{DSS}	drain leakage current	V_{DS} = 750 V; V_{GS} = 0 V; T_j = 25 °C		-	0.1	50	µA
		V_{DS} = 750 V; V_{GS} = 0 V; T_j = 175 °C		-	5	-	µA
I_{GSS}	gate leakage current	V_{GS} = 22 V; V_{DS} = 0 V; T_j = 25 °C		-	5	100	nA
		V_{GS} = -10 V; V_{DS} = 0 V; T_j = 25 °C		-	5	100	nA
$R_{DS(on)}$	drain-source on-state resistance	V_{GS} = 15 V; I_D = 15 A; T_j = 25 °C		-	71.5	-	mΩ
		V_{GS} = 18 V; I_D = 15 A; T_j = 25 °C		-	55	72	mΩ
		V_{GS} = 18 V; I_D = 15 A; T_j = 175 °C		-	66	-	mΩ
R_G	gate resistance	f = 1 MHz; T_j = 25 °C		-	1.36	-	Ω
g_{fs}	transconductance	V_{DS} = 20 V; I_D = 15 A; T_j = 25 °C		-	10	-	S
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	I_D = 15 A; V_{DS} = 400 V; V_{GS} = -4 V/18 V; T_j = 25 °C		-	53	-	nC
Q_{GS}	gate-source charge			-	22	-	nC
Q_{GD}	gate-drain charge			-	7.3	-	nC
C_{iss}	input capacitance	V_{DS} = 400 V; V_{GS} = 0 V; f = 1 MHz; T_j = 25 °C		-	1217	-	pF
C_{oss}	output capacitance			-	98	-	pF
C_{rss}	reverse transfer capacitance			-	8	-	pF
E_{oss}	Coss stored energy			-	7.8	-	µJ
$t_{d(on)}$	turn-on delay time	V_{DS} = 400 V; V_{GS} = -4 V/18 V; $R_{G(ext)}$ = 5.1 Ω; I_D = 7.5 A; L = 100 µH; T_j = 25 °C		-	14	-	ns
t_r	rise time			-	11	-	ns
$t_{d(off)}$	turn-off delay time			-	33	-	ns
t_f	fall time			-	22	-	ns
E_{on}	turn-on energy (Body Diode FWD)		Fig.19	-	56	-	µJ
E_{off}	turn-off energy (Body Diode FWD)		Fig.19	-	5	-	µJ
Source-drain diode							
V_{SD}	source-drain voltage	V_{GS} = 0 V; I_{SD} = 15 A; T_j = 25 °C		-	3.7	-	V
		V_{GS} = -4 V; I_{SD} = 15 A; T_j = 25 °C		-	4.2	-	V
		V_{GS} = -4 V; I_{SD} = 15 A; T_j = 175 °C		-	3.7	-	V
t_{rr}	reverse recovery time	I_{SD} = 15 A; di/dt = 500 A/µs; V_{DS} = 400 V; T_j = 25 °C		-	25.3	-	ns
Q_r	recovered charge			-	58.6	-	nC
I_{rrm}	reverse recovery current			-	4.6	-	A



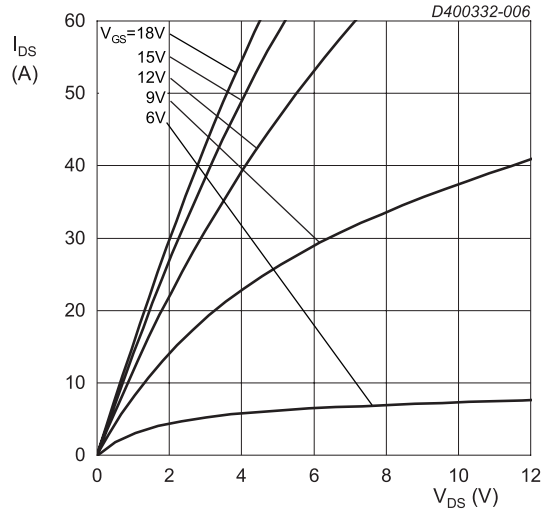
T_j = -55 °C; t_p < 200 µs

Fig. 4. Output characteristics; drain current as a function of drain-source voltage; typical values



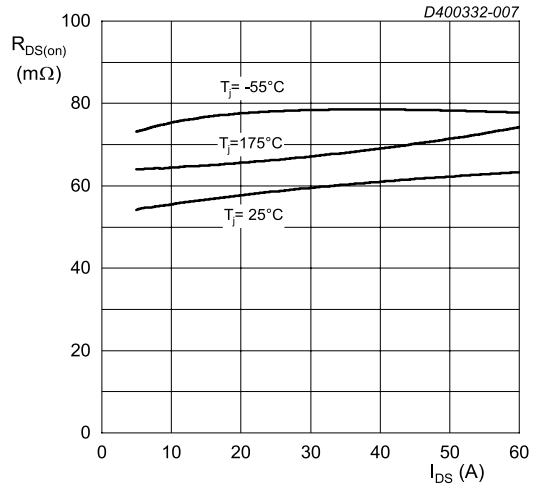
T_j = 25 °C; t_p < 200 µs

Fig. 5. Output characteristics; drain current as a function of drain-source voltage; typical values



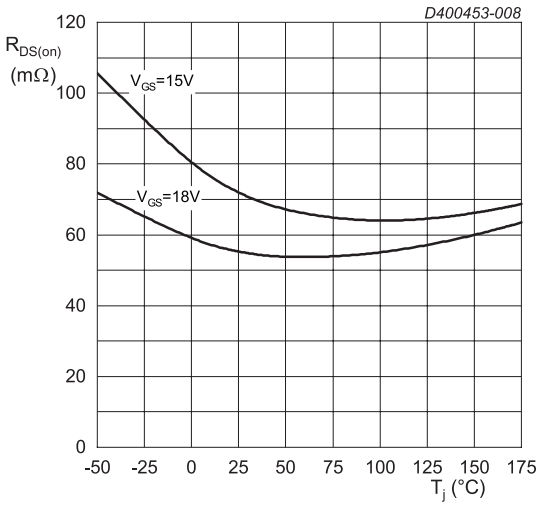
T_j = 175 °C; t_p < 200 µs

Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values



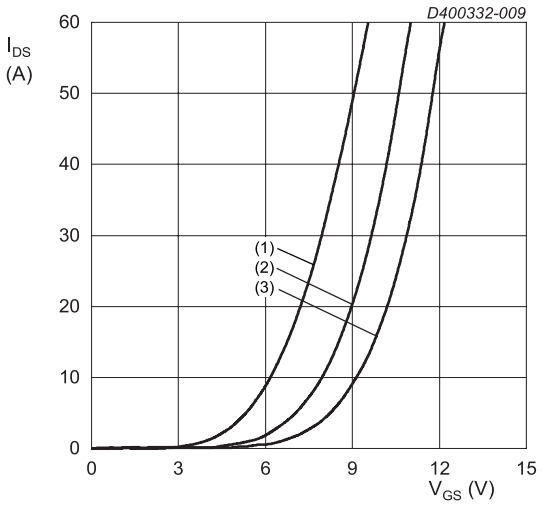
V_{GS} = 18 V; t_p < 200 µs

Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



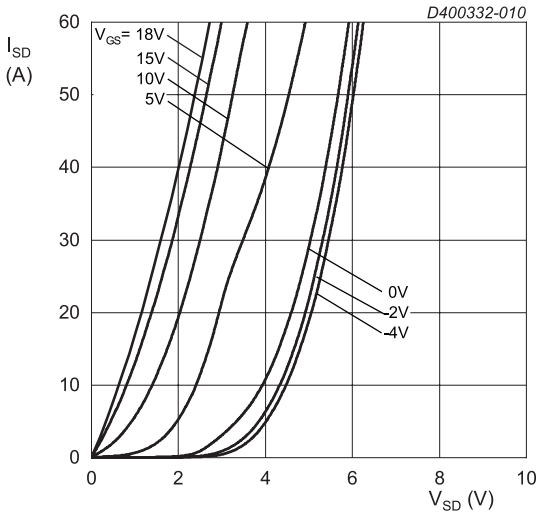
I_{DS} = 15 A; t_p < 200 µs

Fig. 8. Drain-source on-state resistance as a function of junction temperature



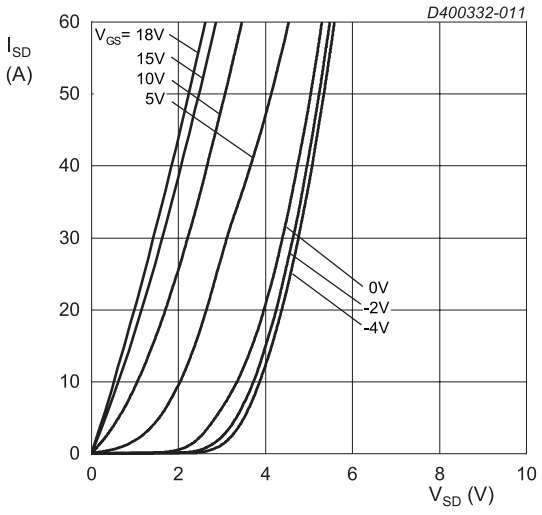
V_{DS} = 20 V; t_p < 200 µs
(1) T_j = 175 °C
(2) T_j = 25 °C
(3) T_j = -55 °C

Fig. 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values



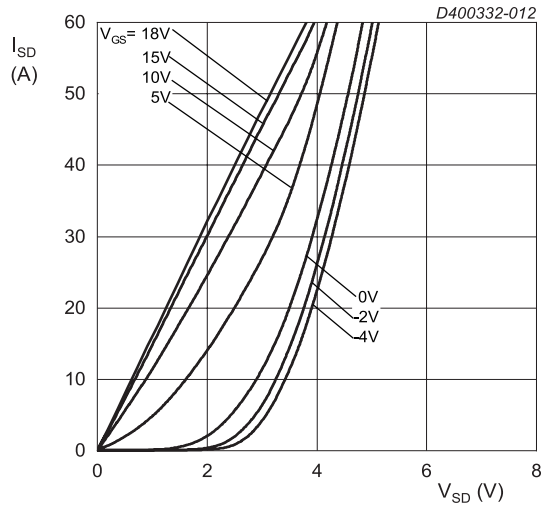
T_j = -55 °C; t_p < 200 µs

Fig. 10. Body diode forward characteristics; typical values



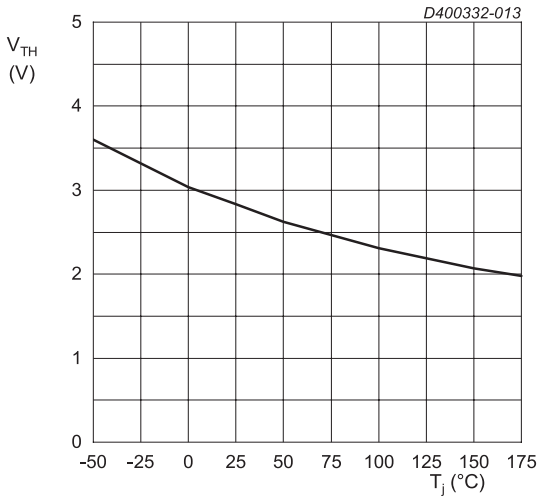
T_j = 25 °C; t_p < 200 µs

Fig. 11. Body diode forward characteristics; typical values



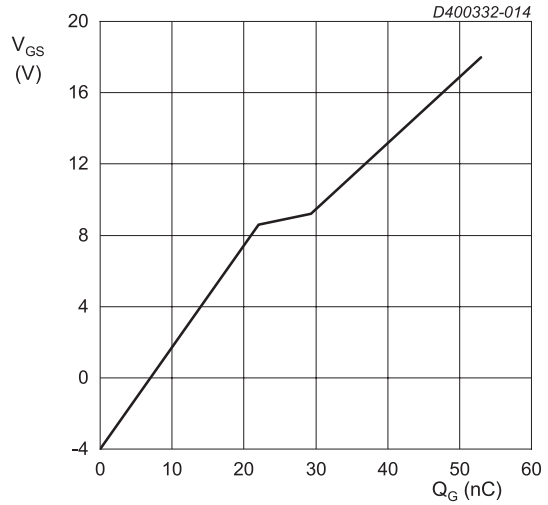
T_j = 175 °C; t_p < 200 µs

Fig. 12. Body diode forward characteristics; typical values



V_{DS} = V_{GS}; I_{DS} = 4 mA

Fig. 13. Threshold voltage as a function of junction temperature



I_{DS} = 15 A; I_{GS} = 0.1 mA; V_{DS} = 400 V; T_j = 25 °C

Fig. 14. Gate-source voltage as a function of gate charge; typical values

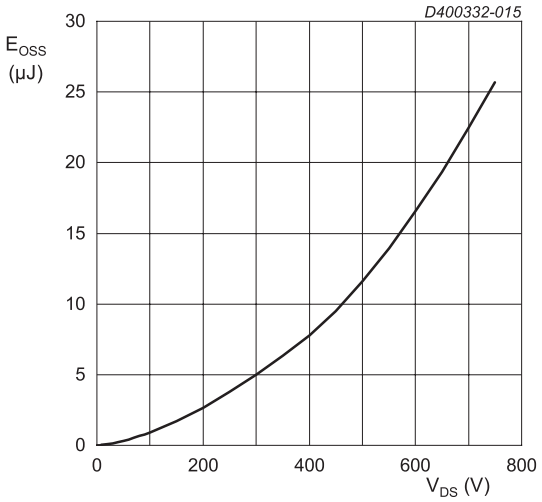
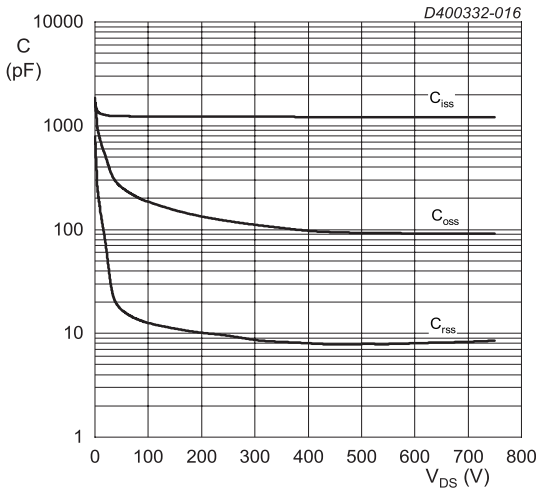
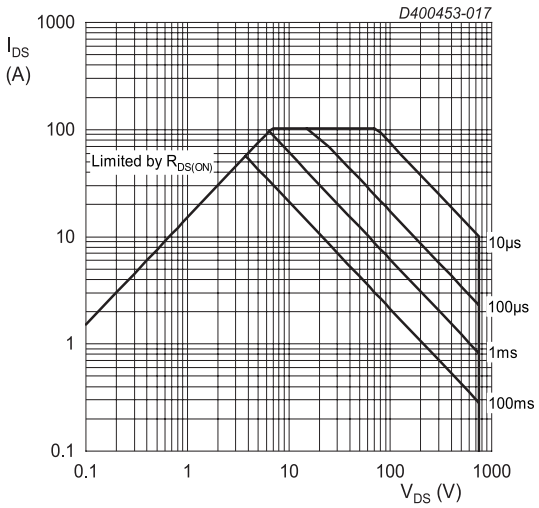


Fig. 15. Output capacitor stored energy as a function of drain-source voltage



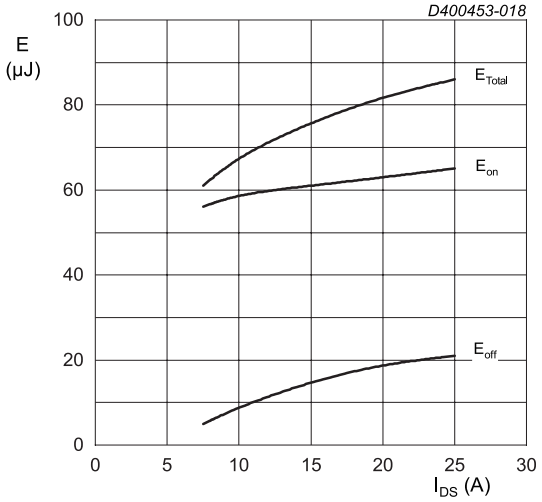
V_{DS} = 0 - 750 V
T_j = 25 °C; V_{AC} = 25 mV; f = 1 MHz

Fig. 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



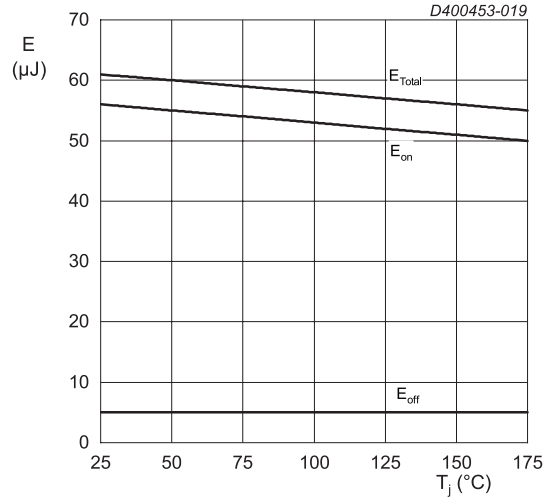
T_j = 25 °C; D = 0
Parameter: t_p

Fig. 17. Forward bias safe operating area



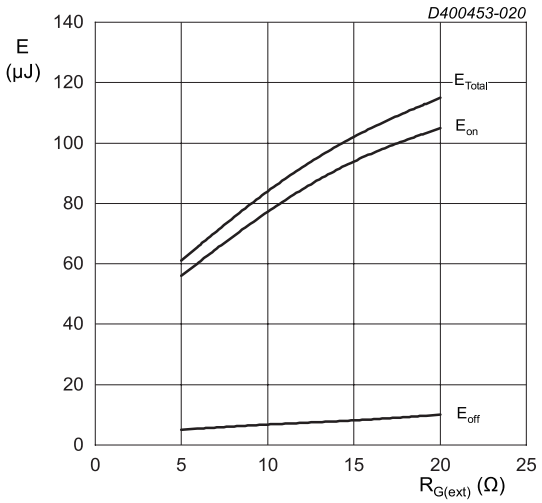
T_j = 25 °C; V_{DD} = 400 V; $R_{G(ext)}$ = 5.1 Ω;
V_{GS} = -4 V/18 V; L = 100 µH
FWD = WNSC2M70075TB-A

Fig. 18. Clamped Inductive Switching Energy as a function of drain current



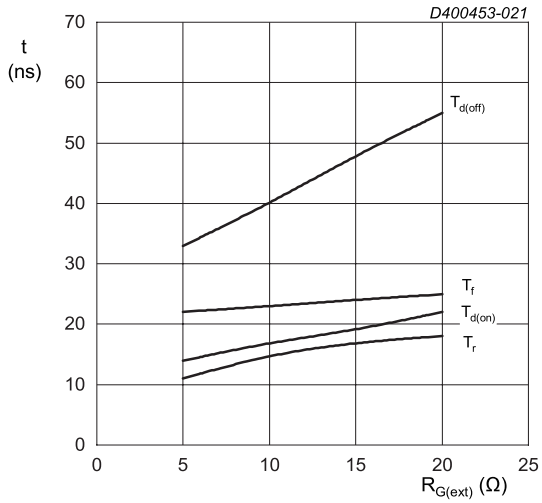
I_{DS} = 7.5 A; V_{DD} = 400 V; $R_{G(ext)}$ = 5.1 Ω;
V_{GS} = -4 V/18 V; L = 100 µH
FWD = WNSC2M70075TB-A

Fig. 19. Clamped Inductive Switching Energy as a function of junction temperature



T_j = 25 °C; V_{DD} = 400 V; I_{DS} = 7.5 A; V_{GS} = -4 V/18 V
FWD = WNSC2M70075TB-A; L = 100 µH

Fig. 20. Clamped Inductive Switching Energy as a function of external gate resistance



T_j = 25 °C; V_{DD} = 400 V; I_{DS} = 7.5 A; V_{GS} = -4 V/18 V
FWD = WNSC2M70075TB-A; L = 100 µH

Fig. 21. Switching time as a function of external gate resistance

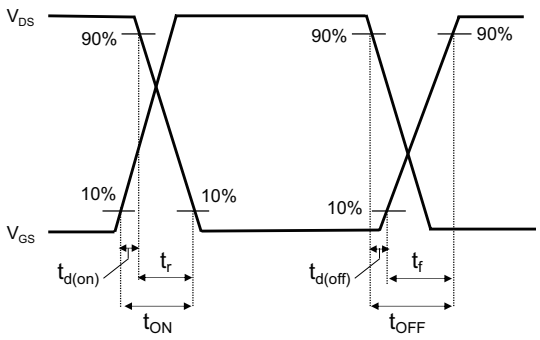
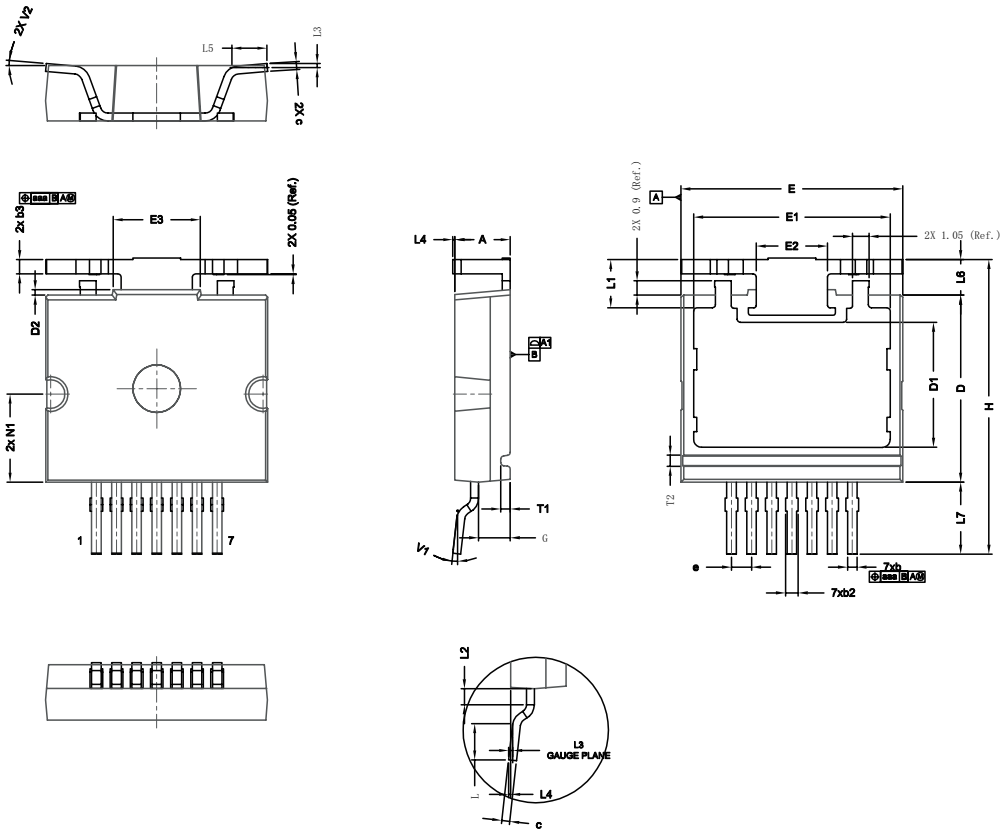
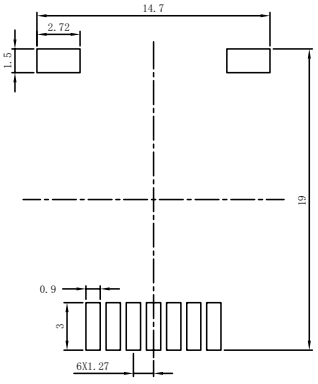


Fig. 22. Switching time definition

11. Package outline



SYMBOL	DIMENSIONS MILLIMETER			SYMBOL	DIMENSIONS MILLIMETER		
	MIN.	NOM.	MAX.		MIN.	NOM.	MAX.
A	3.40	3.50	3.60	H	18.00	18.58	19.00
A1		0.05		L	2.185	2.285	2.385
b	0.50	0.60	0.70	L1		3.04	
b2	0.70	0.80	0.90	L2	0.90	1.00	1.10
b3	0.80	0.90	0.98	L3		0.26	
c	0.40	0.50	0.60	L4	0.075	0.125	0.175
D	11.70	11.80	11.90	L5	2.10	2.20	2.30
D1	7.78	7.88	7.98	L6	2.14	2.24	2.34
D2	0.24	0.34	0.44	L7	4.44	4.54	4.64
E	13.90	14.00	14.10	N1	5.46	5.56	5.66
E1	12.30	12.40	12.50	T1		0.6(Ref)	
E2	4.45	4.50	4.55	T2		0.7(Ref)	
E3	5.45	5.50	5.55	V1	0°		8°
e		1.27		V2	0°		8°
G	1.90	2.00	2.10	aaa		0.1	



Footprint(Only for reference)

NOTES.
1. DIMENSION AND TOLERANCE CONFORM TO ASME Y14.5M
2. CONTROLLING DIMENSIONS : MILLIMETER. CONVERTED INCH. DIMENSION ARE NOT NECESSARILY EXACT.
3. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.ween-semi.com.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. WeEn Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local WeEn Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between WeEn Semiconductors and its customer, unless WeEn Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the WeEn Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, WeEn Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. WeEn Semiconductors takes no responsibility for the content in this document if provided by an information source outside of WeEn Semiconductors.

In no event shall WeEn Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, WeEn Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of WeEn Semiconductors.

Right to make changes — WeEn Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — WeEn Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an WeEn Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. WeEn Semiconductors and its suppliers accept no liability for inclusion and/or use of WeEn Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. WeEn Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using WeEn Semiconductors products, and WeEn Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the WeEn Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

WeEn Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using WeEn Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). WeEn does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific WeEn Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. WeEn Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without WeEn Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond WeEn Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies WeEn Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond WeEn Semiconductors' standard warranty and WeEn Semiconductors' product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

13. Contents

For more information, please visit: http://www.ween-semi.com
For sales office addresses, please send an email to: salesaddresses@ween-semi.com
Date of release: 19 December 2025