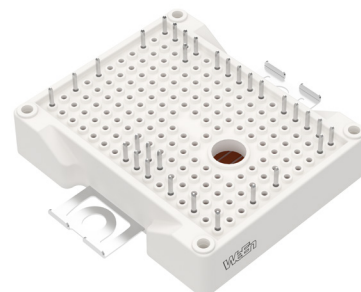


## 1. General description

WMHG150N06B2S-B is a I-type NPC (Neutral Point Clamped) three-level module consisting of two 150A, 650V outer IGBTs with inverse diodes, two 150A, 650V inner IGBTs with inverse diodes, two neutral point 120A, 650V Sic diodes and an NTC thermistor. The integrated field stop trench IGBTs and FRDs provide lower conduction losses and switching losses, enabling designers to achieve high efficiency and superior reliability.



## 2. Features and benefits

- I-NPC topology
- Low switching losses
- Low Vcesat
- Compact design
- Solder pin
- Integrated NTC temperature sensor
- Al<sub>2</sub>O<sub>3</sub> substrate with low thermal resistance

## 3. Applications

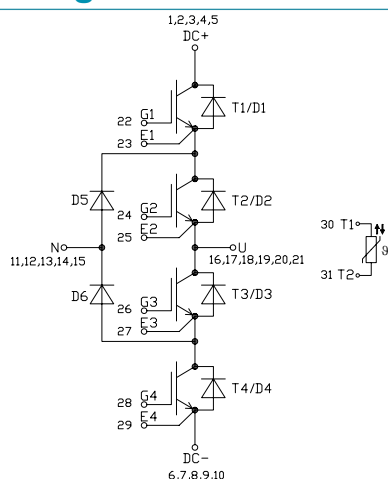
- Three-level applications
- Solar
- Motor Drives
- UPS

## 4. Ordering information

Table 1. Ordering information

| Type number     | Package Name | Orderable part number | Packing method | Small packing quantity | Package version  | Package issue date |
|-----------------|--------------|-----------------------|----------------|------------------------|------------------|--------------------|
| WMHG150N06B2S-B | WeEnPACK-B2  | WMHG150N06B2S-BT      | Tray           | 14                     | WeEnPACK-B2PTL-B | 28-Jun-2024        |

## 5. Circuit diagram



## 6. Limiting values

Table 2. Limiting values

| Symbol                        | Parameter                             | Test Condition                                                                            | Value    | Unit               |
|-------------------------------|---------------------------------------|-------------------------------------------------------------------------------------------|----------|--------------------|
| <b>IGBT, T1/T4</b>            |                                       |                                                                                           |          |                    |
| $V_{CE}$                      | Collector-emitter voltage             |                                                                                           | 650      | V                  |
| $V_{GE}$                      | Gate-emitter voltage                  |                                                                                           | $\pm 20$ | V                  |
| $I_{CN}$                      | Implemented collector current         |                                                                                           | 300      | A                  |
| $I_C$                         | Continuous collector current          | $T_C = 95\text{ }^{\circ}\text{C}$ , limited by $T_{jmax}$                                | 150      | A                  |
| $I_{Cpulse}$                  | Pulsed collector current              | tp limited by $T_{jmax}$                                                                  | 450      | A                  |
| $P_{tot}$                     | Total power dissipation               | $T_C = 95\text{ }^{\circ}\text{C}$                                                        | 333      | W                  |
| $T_{jmax}$                    | Maximum junction temperature          |                                                                                           | 175      | $^{\circ}\text{C}$ |
| <b>IGBT, T2/T3</b>            |                                       |                                                                                           |          |                    |
| $V_{CE}$                      | Collector-emitter voltage             |                                                                                           | 650      | V                  |
| $V_{GE}$                      | Gate-emitter voltage                  |                                                                                           | $\pm 20$ | V                  |
| $I_{CN}$                      | Implemented collector current         |                                                                                           | 300      | A                  |
| $I_C$                         | Continuous collector current          | $T_C = 95\text{ }^{\circ}\text{C}$ , limited by $T_{jmax}$                                | 150      | A                  |
| $I_{Cpulse}$                  | Pulsed collector current              | tp limited by $T_{jmax}$                                                                  | 450      | A                  |
| $P_{tot}$                     | Total power dissipation               | $T_C = 95\text{ }^{\circ}\text{C}$                                                        | 333      | W                  |
| $t_{sc}$                      | Short circuit withstand time          | $V_{GE} = 15\text{ V}$ ; $V_{CC} = 400\text{ V}$ ;<br>$T_j = 150\text{ }^{\circ}\text{C}$ | 5        | $\mu\text{s}$      |
| $T_{jmax}$                    | Maximum junction temperature          |                                                                                           | 175      | $^{\circ}\text{C}$ |
| <b>SIC Diode, D1/D4/D5/D6</b> |                                       |                                                                                           |          |                    |
| $V_{RRM}$                     | Diode repetitive peak reverse voltage |                                                                                           | 650      | V                  |
| $I_{FN}$                      | Diode Implemented collector current   |                                                                                           | 240      | A                  |
| $I_F$                         | Diode Continuous collector current    | $T_C = 95\text{ }^{\circ}\text{C}$ , limited by $T_{jmax}$                                | 120      | A                  |
| $I_{FRM}$                     | Diode repetitive peak forward current | tp limited by $T_{jmax}$                                                                  | 360      | A                  |
| $P_{tot}$                     | Total power dissipation               | $T_C = 95\text{ }^{\circ}\text{C}$                                                        | 200      | W                  |
| $T_{jmax}$                    | Maximum junction temperature          |                                                                                           | 175      | $^{\circ}\text{C}$ |
| <b>Diode, D2/D3</b>           |                                       |                                                                                           |          |                    |
| $V_{RRM}$                     | Diode repetitive peak reverse voltage |                                                                                           | 650      | V                  |
| $I_{FN}$                      | Diode Implemented collector current   |                                                                                           | 300      | A                  |
| $I_F$                         | Diode Continuous collector current    | $T_C = 95\text{ }^{\circ}\text{C}$ , limited by $T_{jmax}$                                | 150      | A                  |
| $I_{FRM}$                     | Diode repetitive peak forward current | tp limited by $T_{jmax}$                                                                  | 450      | A                  |
| $P_{tot}$                     | Total power dissipation               | $T_C = 95\text{ }^{\circ}\text{C}$                                                        | 200      | W                  |
| $T_{jmax}$                    | Maximum junction temperature          |                                                                                           | 175      | $^{\circ}\text{C}$ |

7. Module package thermal & insulation properties

Table 3. Thermal & Insulation properties

| Symbol             | Parameter                  | Test Condition                                                         | Value      | Unit |
|--------------------|----------------------------|------------------------------------------------------------------------|------------|------|
| V <sub>ISOL</sub>  | RMS isolation voltage      | T <sub>j</sub> = 25 °C, all terminals shorted,<br>f = 50 Hz, t = 1 min | 2500       | V    |
| d <sub>Creep</sub> | Creepage distance          | terminal to heatsink                                                   | 11.5       | mm   |
| d <sub>Clear</sub> | Clearance                  | terminal to heatsink                                                   | 10         | mm   |
| CTI                | Comperative tracking index |                                                                        | > 200      |      |
| T <sub>stg</sub>   | Storage temperature        |                                                                        | -40 to 125 | °C   |

## 8. Electrical characteristics

Table 4. Characteristics

| Symbol                                            | Parameter                            | Conditions                                                                                                             | Min | Typ  | Max | Unit               |
|---------------------------------------------------|--------------------------------------|------------------------------------------------------------------------------------------------------------------------|-----|------|-----|--------------------|
| <b>Outer IGBT characteristics, T1/T4</b>          |                                      |                                                                                                                        |     |      |     |                    |
| $V_{CEsat}$                                       | Collector-emitter saturation voltage | $V_{GE} = 15\text{ V}; I_C = 150\text{ A}; T_J = 25\text{ °C}$                                                         | -   | 1.5  | -   | V                  |
|                                                   |                                      | $V_{GE} = 15\text{ V}; I_C = 150\text{ A}; T_J = 150\text{ °C}$                                                        | -   | 1.85 | -   | V                  |
| $V_{GE(th)}$                                      | Gate-emitter threshold voltage       | $I_C = 1\text{ mA}; V_{CE} = V_{GE}; T_J = 25\text{ °C}$                                                               | 3.0 | 4.2  | 5.5 | V                  |
| $I_{CES}$                                         | Zero gate voltage collector current  | $V_{CE} = 650\text{ V}; V_{GE} = 0\text{ V}; T_J = 25\text{ °C}$                                                       | -   | -    | 20  | $\mu\text{A}$      |
| $I_{GES}$                                         | Gate leakage current                 | $V_{GE} = 20\text{ V}; V_{CE} = 0\text{ V}; T_J = 25\text{ °C}$                                                        | -   | -    | 250 | nA                 |
| $Q_G$                                             | Gate charge                          | $V_{CC} = 300\text{ V}; I_C = 150\text{ A}; V_{GE} = \pm 15\text{ V}$                                                  | -   | 626  | -   | nC                 |
| $C_{ies}$                                         | Input capacitance                    | $V_{CE} = 25\text{ V}; V_{GE} = 0\text{ V}; f = 1\text{ MHz}; T_J = 25\text{ °C}$                                      | -   | 9947 | -   | pF                 |
| $C_{oes}$                                         | Output capacitance                   |                                                                                                                        | -   | 490  | -   | pF                 |
| $C_{res}$                                         | Reverse transfer capacitance         |                                                                                                                        | -   | 84   | -   | pF                 |
| $t_{d(on)}$                                       | Turn-on delay time                   | $T_J = 25\text{ °C}$<br>$V_{CC} = 300\text{ V}; I_C = 150\text{ A}; V_{GE} = \pm 15\text{ V}; R_g = 10\text{ }\Omega$  | -   | 45   | -   | nS                 |
| $t_r$                                             | Rise time                            |                                                                                                                        | -   | 33   | -   | nS                 |
| $t_{d(off)}$                                      | Turn-off delay time                  |                                                                                                                        | -   | 199  | -   | nS                 |
| $t_f$                                             | Fall time                            |                                                                                                                        | -   | 51   | -   | nS                 |
| $E_{on}$                                          | Turn-on energy                       |                                                                                                                        | -   | 1.55 | -   | mJ                 |
| $E_{off}$                                         | Turn-off energy                      |                                                                                                                        | -   | 1.4  | -   | mJ                 |
| $t_{d(on)}$                                       | Turn-on delay time                   | $T_J = 150\text{ °C}$<br>$V_{CC} = 300\text{ V}; I_C = 150\text{ A}; V_{GE} = \pm 15\text{ V}; R_g = 10\text{ }\Omega$ | -   | 42   | -   | nS                 |
| $t_r$                                             | Rise time                            |                                                                                                                        | -   | 38   | -   | nS                 |
| $t_{d(off)}$                                      | Turn-off delay time                  |                                                                                                                        | -   | 218  | -   | nS                 |
| $t_f$                                             | Fall time                            |                                                                                                                        | -   | 59   | -   | nS                 |
| $E_{on}$                                          | Turn-on energy                       |                                                                                                                        | -   | 1.9  | -   | mJ                 |
| $E_{off}$                                         | Turn-off energy                      |                                                                                                                        | -   | 1.9  | -   | mJ                 |
| $R_{thJC}$                                        | Thermal resistance, junction to case |                                                                                                                        | -   | 0.24 | -   | K/W                |
| $T_{jop}$                                         | Operation temperature                |                                                                                                                        | -40 |      | 150 | $^{\circ}\text{C}$ |
| <b>Neutral point Diode characteristics, D5/D6</b> |                                      |                                                                                                                        |     |      |     |                    |
| $V_F$                                             | Diode forward voltage                | $I_F = 120\text{ A}; T_J = 25\text{ °C}$                                                                               | -   | 1.4  | -   | V                  |
|                                                   |                                      | $I_F = 120\text{ A}; T_J = 150\text{ °C}$                                                                              | -   | 1.6  | -   | V                  |
| $Q_{rr}$                                          | Reverse recovery charge              | $T_J = 25\text{ °C}$<br>$V_R = 300\text{ V}; I_F = 150\text{ A}; di/dt = 3500\text{ A}/\mu\text{s};$                   | -   | 218  | -   | nC                 |
| $I_{rrm}$                                         | Peak reverse recovery current        |                                                                                                                        | -   | 19   | -   | A                  |
| $E_{rr}$                                          | Reverse recovery energy              |                                                                                                                        | -   | 0.03 | -   | mJ                 |
| $Q_{rr}$                                          | Reverse recovery charge              | $T_J = 150\text{ °C}$<br>$V_R = 300\text{ V}; I_F = 150\text{ A}; di/dt = 3500\text{ A}/\mu\text{s};$                  | -   | 239  | -   | nC                 |
| $I_{rrm}$                                         | Peak reverse recovery current        |                                                                                                                        | -   | 19   | -   | A                  |
| $E_{rr}$                                          | Reverse recovery energy              |                                                                                                                        | -   | 0.03 | -   | mJ                 |
| $R_{thJC}$                                        | Thermal resistance, junction to case |                                                                                                                        | -   | 0.4  | -   | K/W                |
| $T_{jop}$                                         | Operation temperature                |                                                                                                                        | -40 |      | 150 | $^{\circ}\text{C}$ |

| Symbol                                      | Parameter                            | Conditions                                                                                                                           | Min | Typ   | Max | Unit               |
|---------------------------------------------|--------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------|-----|-------|-----|--------------------|
| <b>Inner IGBT characteristics, T2/T3</b>    |                                      |                                                                                                                                      |     |       |     |                    |
| $V_{CEsat}$                                 | Collector-emitter saturation voltage | $V_{GE} = 15\text{ V}; I_C = 150\text{ A}; T_j = 25\text{ }^{\circ}\text{C}$                                                         | -   | 1.6   | -   | V                  |
|                                             |                                      | $V_{GE} = 15\text{ V}; I_C = 150\text{ A}; T_j = 150\text{ }^{\circ}\text{C}$                                                        | -   | 2.0   | -   | V                  |
| $V_{GE(th)}$                                | Gate-emitter threshold voltage       | $I_C = 1\text{ mA}; V_{CE} = V_{GE}; T_j = 25\text{ }^{\circ}\text{C}$                                                               | 3.8 | 4.8   | 5.8 | V                  |
| $I_{CES}$                                   | Zero gate voltage collector current  | $V_{CE} = 650\text{ V}; V_{GE} = 0\text{ V}; T_j = 25\text{ }^{\circ}\text{C}$                                                       | -   | -     | 20  | $\mu\text{A}$      |
| $I_{GES}$                                   | Gate leakage current                 | $V_{GE} = 20\text{ V}; V_{CE} = 0\text{ V}; T_j = 25\text{ }^{\circ}\text{C}$                                                        | -   | -     | 250 | nA                 |
| $Q_G$                                       | Gate charge                          | $V_{CC} = 300\text{ V}; I_C = 150\text{ A}; V_{GE} = \pm 15\text{ V}$                                                                | -   | 626   | -   | nC                 |
| $C_{ies}$                                   | Input capacitance                    | $V_{CE} = 25\text{ V}; V_{GE} = 0\text{ V}; f = 1\text{ MHz}; T_j = 25\text{ }^{\circ}\text{C}$                                      | -   | 10917 | -   | pF                 |
| $C_{oes}$                                   | Output capacitance                   |                                                                                                                                      | -   | 519   | -   | pF                 |
| $C_{res}$                                   | Reverse transfer capacitance         |                                                                                                                                      | -   | 88    | -   | pF                 |
| $t_{d(on)}$                                 | Turn-on delay time                   | $T_j = 25\text{ }^{\circ}\text{C}$<br>$V_{CC} = 300\text{ V}; I_C = 150\text{ A}; V_{GE} = \pm 15\text{ V}; R_g = 10\text{ }\Omega$  | -   | 41    | -   | nS                 |
| $t_r$                                       | Rise time                            |                                                                                                                                      | -   | 29    | -   | nS                 |
| $t_{d(off)}$                                | Turn-off delay time                  |                                                                                                                                      | -   | 204   | -   | nS                 |
| $t_f$                                       | Fall time                            |                                                                                                                                      | -   | 56    | -   | nS                 |
| $E_{on}$                                    | Turn-on energy                       |                                                                                                                                      | -   | 1.1   | -   | mJ                 |
| $E_{off}$                                   | Turn-off energy                      |                                                                                                                                      | -   | 1.85  | -   | mJ                 |
| $t_{d(on)}$                                 | Turn-on delay time                   | $T_j = 150\text{ }^{\circ}\text{C}$<br>$V_{CC} = 300\text{ V}; I_C = 150\text{ A}; V_{GE} = \pm 15\text{ V}; R_g = 10\text{ }\Omega$ | -   | 36    | -   | nS                 |
| $t_r$                                       | Rise time                            |                                                                                                                                      | -   | 35    | -   | nS                 |
| $t_{d(off)}$                                | Turn-off delay time                  |                                                                                                                                      | -   | 218   | -   | nS                 |
| $t_f$                                       | Fall time                            |                                                                                                                                      | -   | 80    | -   | nS                 |
| $E_{on}$                                    | Turn-on energy                       |                                                                                                                                      | -   | 1.6   | -   | mJ                 |
| $E_{off}$                                   | Turn-off energy                      |                                                                                                                                      | -   | 2.45  | -   | mJ                 |
| $R_{thJC}$                                  | Thermal resistance, junction to case |                                                                                                                                      | -   | 0.24  | -   | K/W                |
| $T_{jop}$                                   | Operation temperature                |                                                                                                                                      | -40 |       | 150 | $^{\circ}\text{C}$ |
| <b>Inverse Diode characteristics, D1/D4</b> |                                      |                                                                                                                                      |     |       |     |                    |
| $V_F$                                       | Diode forward voltage                | $I_F = 120\text{ A}; T_j = 25\text{ }^{\circ}\text{C}$                                                                               | -   | 1.4   | -   | V                  |
|                                             |                                      | $I_F = 120\text{ A}; T_j = 150\text{ }^{\circ}\text{C}$                                                                              | -   | 1.6   | -   | V                  |
| $Q_{rr}$                                    | Reverse recovery charge              | $T_j = 25\text{ }^{\circ}\text{C}$<br>$V_R = 300\text{ V}; I_F = 150\text{ A}; di/dt = 3500\text{ A}/\mu\text{s};$                   | -   | 186   | -   | nC                 |
| $I_{rrm}$                                   | Peak reverse recovery current        |                                                                                                                                      | -   | 20.6  | -   | A                  |
| $E_{rr}$                                    | Reverse recovery energy              |                                                                                                                                      | -   | 0.03  | -   | mJ                 |
| $Q_{rr}$                                    | Reverse recovery charge              | $T_j = 150\text{ }^{\circ}\text{C}$<br>$V_R = 300\text{ V}; I_F = 150\text{ A}; di/dt = 3500\text{ A}/\mu\text{s};$                  | -   | 205   | -   | nC                 |
| $I_{rrm}$                                   | Peak reverse recovery current        |                                                                                                                                      | -   | 20.9  | -   | A                  |
| $E_{rr}$                                    | Reverse recovery energy              |                                                                                                                                      | -   | 0.03  | -   | mJ                 |
| $R_{thJC}$                                  | Thermal resistance, junction to case |                                                                                                                                      | -   | 0.4   | -   | K/W                |
| $T_{jop}$                                   | Operation temperature                |                                                                                                                                      | -40 |       | 150 | $^{\circ}\text{C}$ |

| Symbol                               | Parameter                            | Conditions                                      | Min | Typ  | Max | Unit |
|--------------------------------------|--------------------------------------|-------------------------------------------------|-----|------|-----|------|
| Inverse Diode characteristics, D2/D3 |                                      |                                                 |     |      |     |      |
| V <sub>F</sub>                       | Diode forward voltage                | I <sub>F</sub> = 150 A; T <sub>j</sub> = 25 °C  | -   | 1.75 | -   | V    |
|                                      |                                      | I <sub>F</sub> = 150 A; T <sub>j</sub> = 150 °C | -   | 1.5  | -   | V    |
| R <sub>thJC</sub>                    | Thermal resistance, junction to case |                                                 | -   | 0.4  | -   | K/W  |
| T <sub>jop</sub>                     | Operation temperature                |                                                 | -40 |      | 150 | °C   |

9. NTC - thermistor

Table 5. NTC - Thermistor

| Symbol             | Parameter        | Conditions                                      | Min     | Typ  | Max | Unit |
|--------------------|------------------|-------------------------------------------------|---------|------|-----|------|
| R <sub>25</sub>    | Rated resistance | T <sub>c</sub> = 25 °C                          | -       | 5000 | -   | Ω    |
| R <sub>100</sub>   |                  | T <sub>c</sub> = 100 °C                         | 465±5%  |      |     | Ω    |
| B <sub>25/50</sub> | B-value          | $R_2=R_{25} \exp[B_{25/50}(1/T_2-1/(298.15K))]$ | 3380±5% |      |     | K    |

Typical Characteristics - IGBT T1/T2/T3/T4 and Diode D1/D2/D3/D4/D5/D6

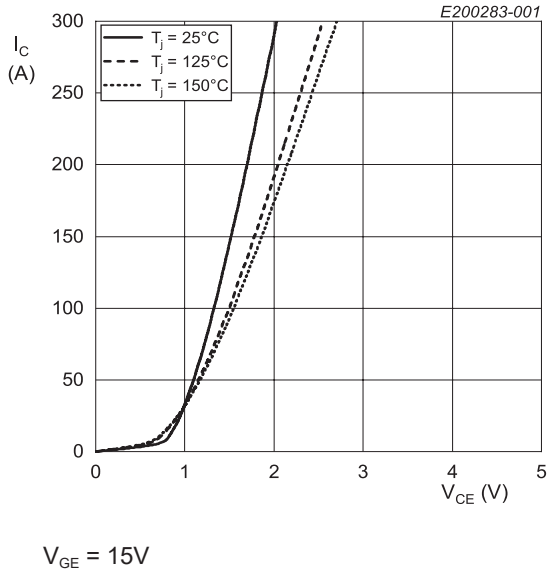


Fig. 1. IGBT typical output characteristics, T1/T4

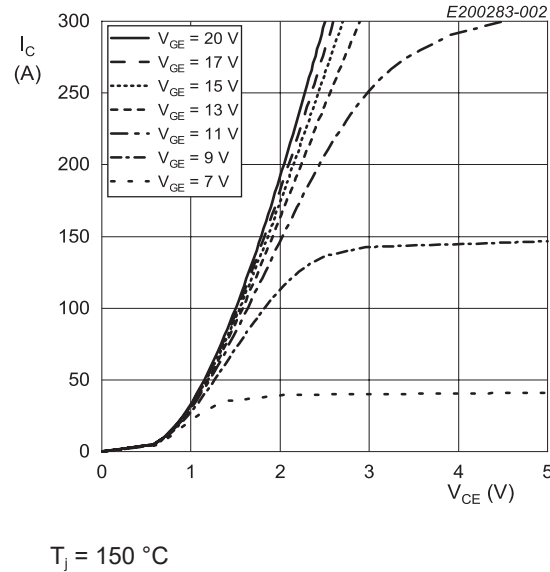


Fig. 2. IGBT typical output characteristics, T1/T4

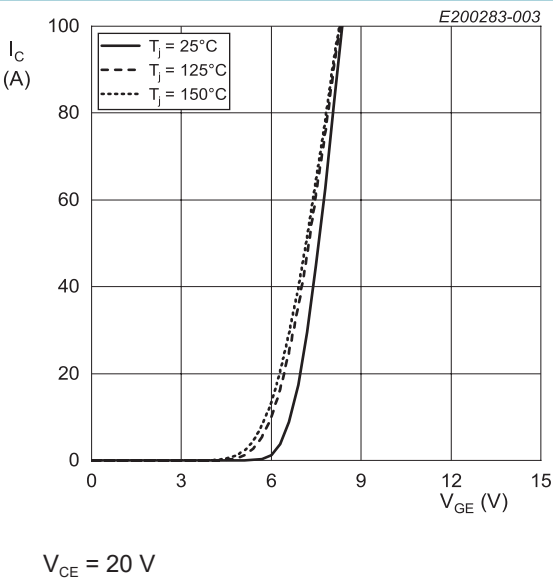


Fig. 3. IGBT typical transfer characteristics, T1/T4

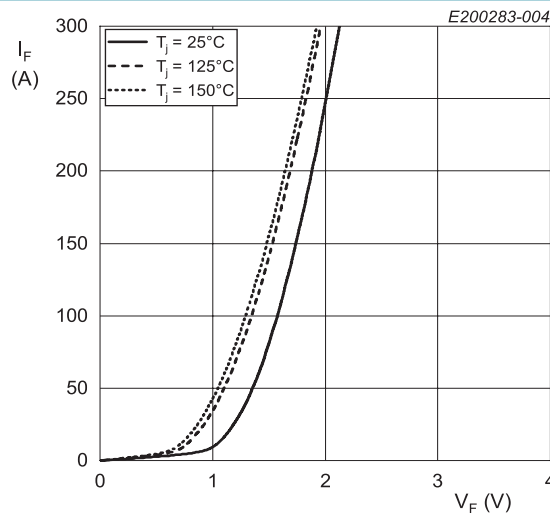
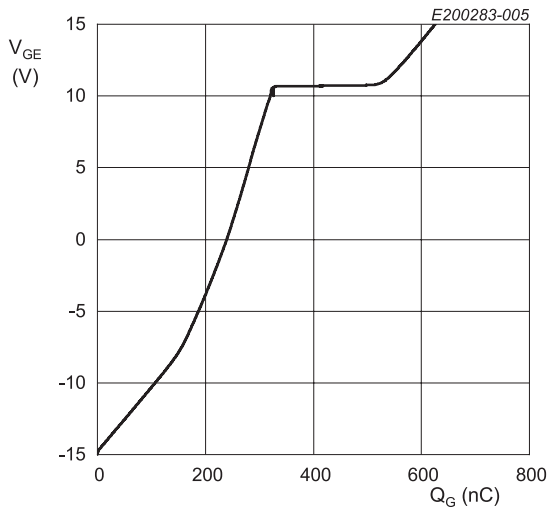


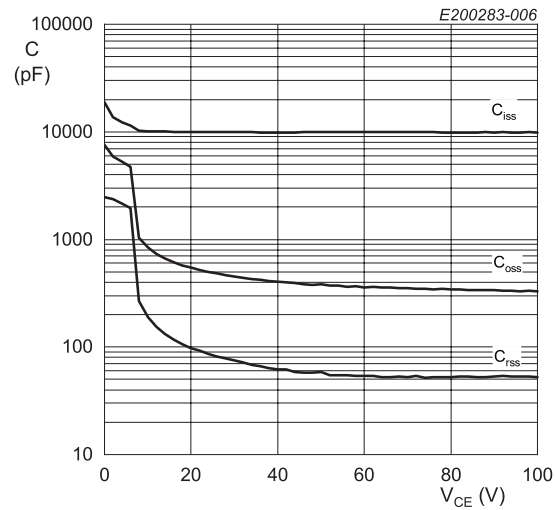
Fig. 4. Diode typical forward characteristics, D2/D3

Typical Characteristics - IGBT T1/T2/T3/T4 and Diode D1/D2/D3/D4/D5/D6



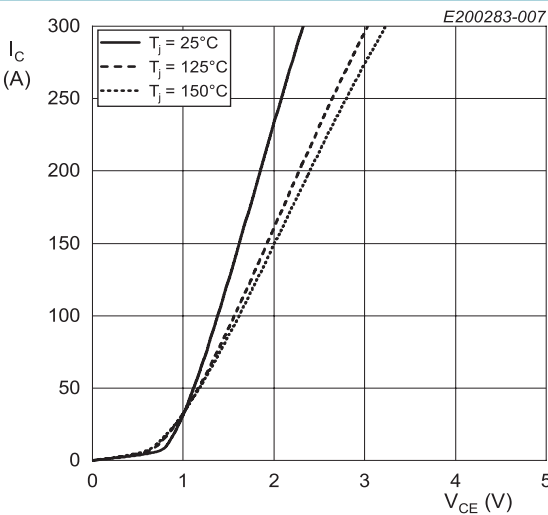
$I_C = 150\text{ A}$ ;  $V_{CC} = 300\text{ V}$ ;  $T_J = 25\text{ }^\circ\text{C}$

Fig. 5. Typical gate charge, T1/T4



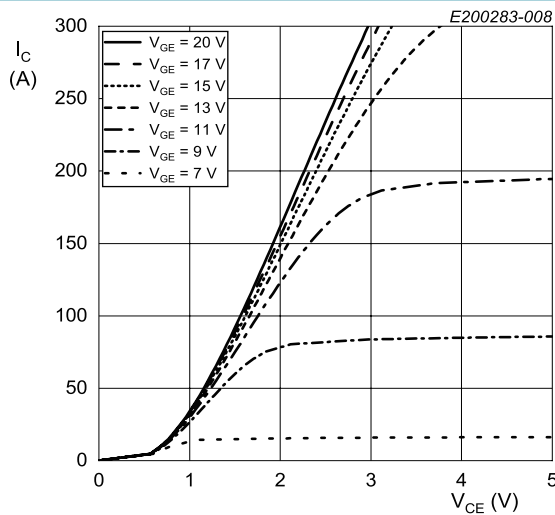
$V_{GE} = 0$ ;  $f = 1\text{ MHz}$ ;  $T_J = 25\text{ }^\circ\text{C}$

Fig. 6. Typical capacitance as a function of collector emitter voltage, T1/T4



$V_{GE} = 15\text{ V}$

Fig. 7. IGBT typical output characteristics, T2/T3



$T_J = 150\text{ }^\circ\text{C}$

Fig. 8. IGBT typical output characteristics, T2/T3



Typical Characteristics - IGBT T1/T2/T3/T4 and Diode D1/D2/D3/D4/D5/D6

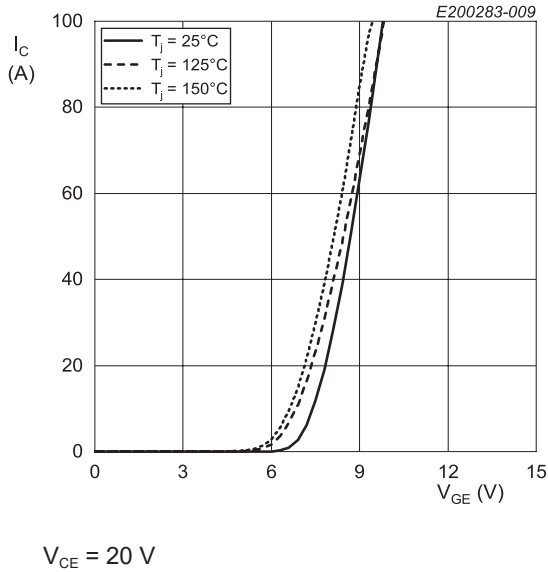


Fig. 9. IGBT typical transfer characteristics, T2/T3

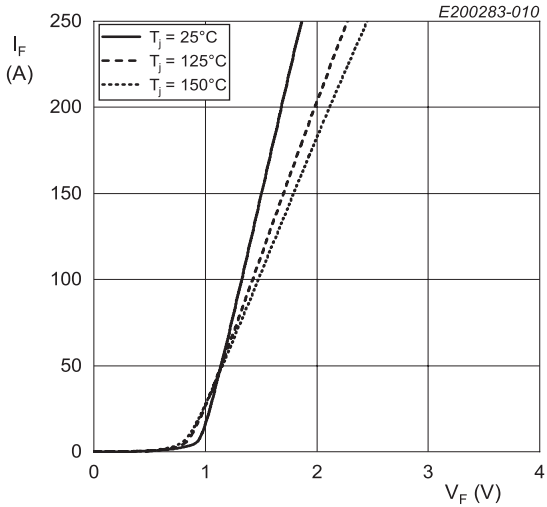


Fig. 10. Diode typical forward characteristics, D1/D4/D5/D6

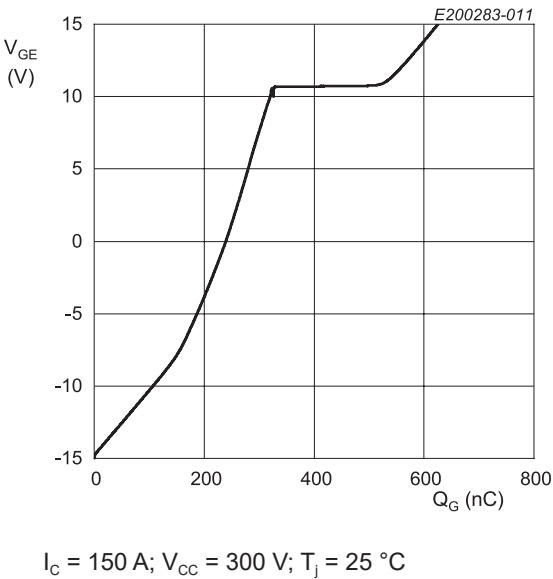


Fig. 11. Typical gate charge, T2/T3

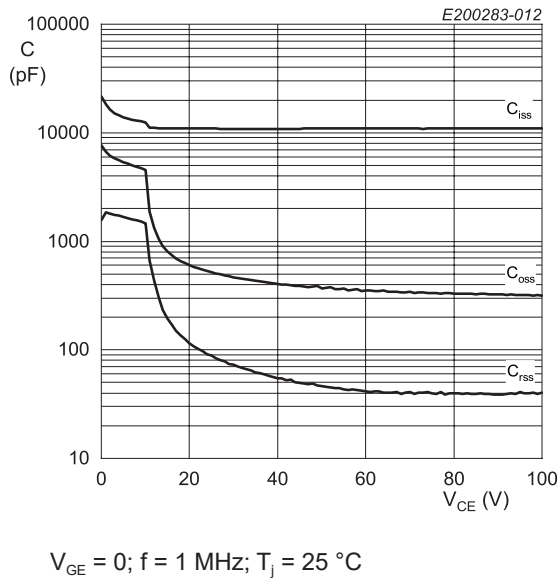
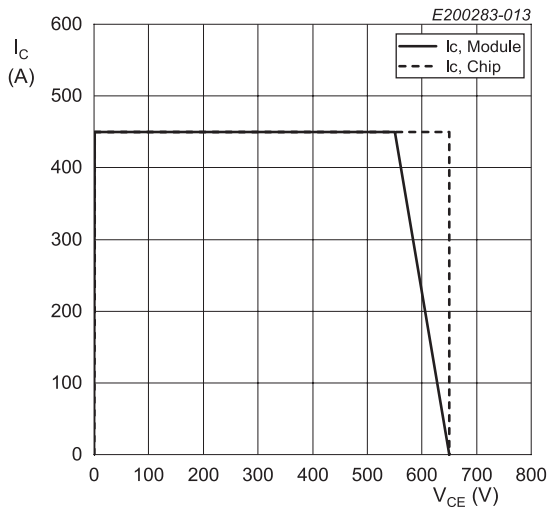


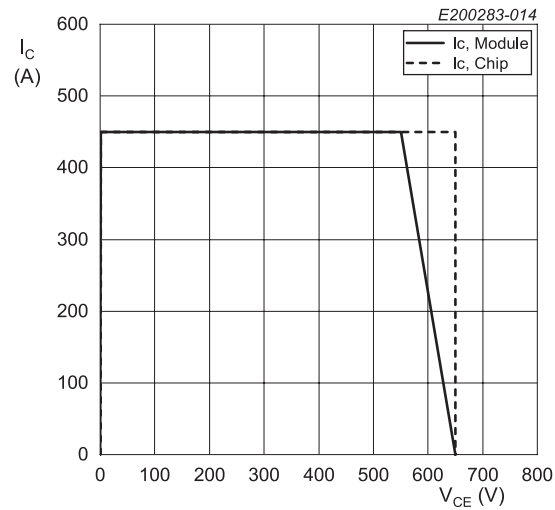
Fig. 12. Typical capacitance as a function of collector emitter voltage, T2/T3

Typical Characteristics - IGBT T1/T2/T3/T4 and Diode D1/D2/D3/D4/D5/D6



$R_g = 10 \Omega$ ;  $V_{GE} = \pm 15V$ ;  $T_j = 150^\circ C$

Fig. 13. Reverse bias safe operating area, T1/T4



$R_g = 10 \Omega$ ;  $V_{GE} = \pm 15V$ ;  $T_j = 150^\circ C$

Fig. 14. Reverse bias safe operating area, T2/T3

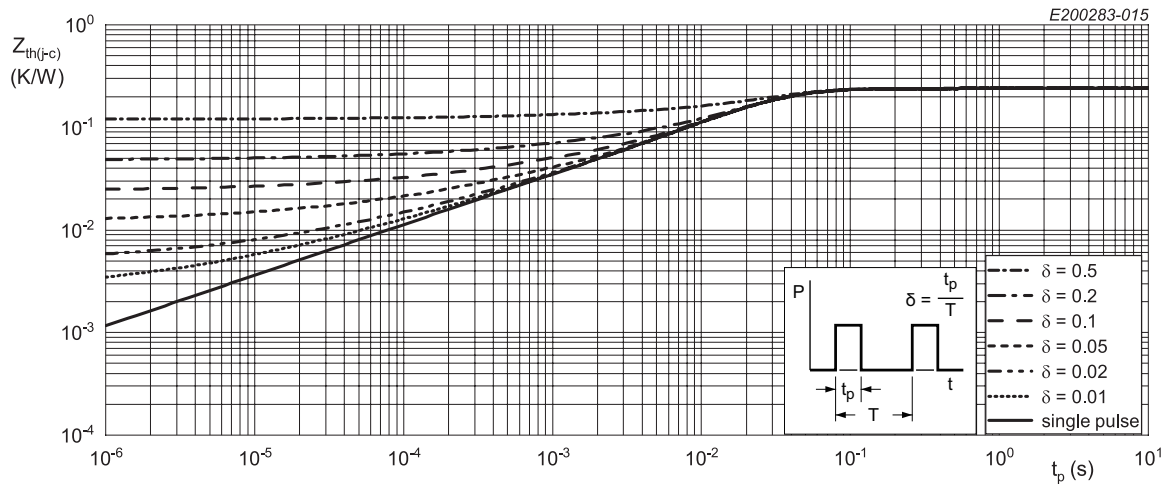


Fig. 15. Typical Transient thermal impedance IGBT, T1/T4

Typical Characteristics - IGBT T1/T2/T3/T4 and Diode D1/D2/D3/D4/D5/D6

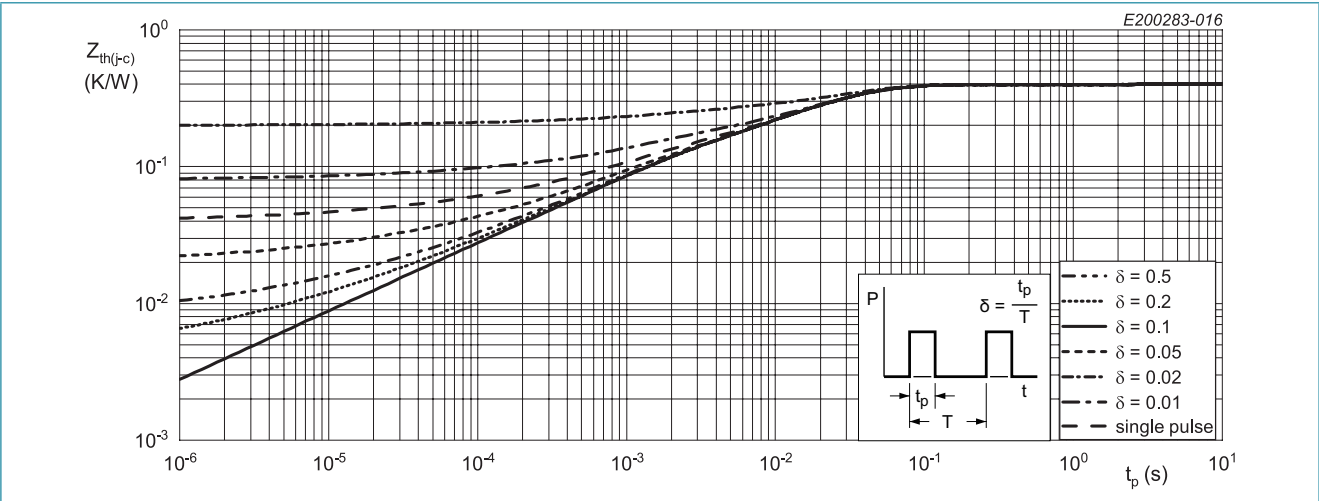


Fig. 16. Typical Transient thermal impedance Diode, D2/D3

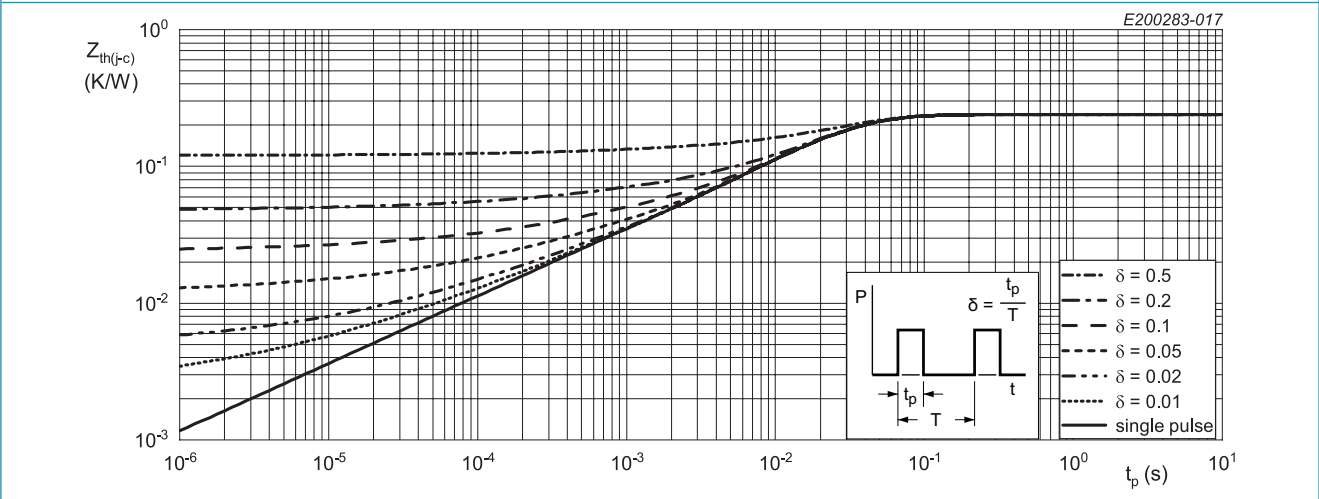


Fig. 17. Typical Transient thermal impedance IGBT, T2/T3

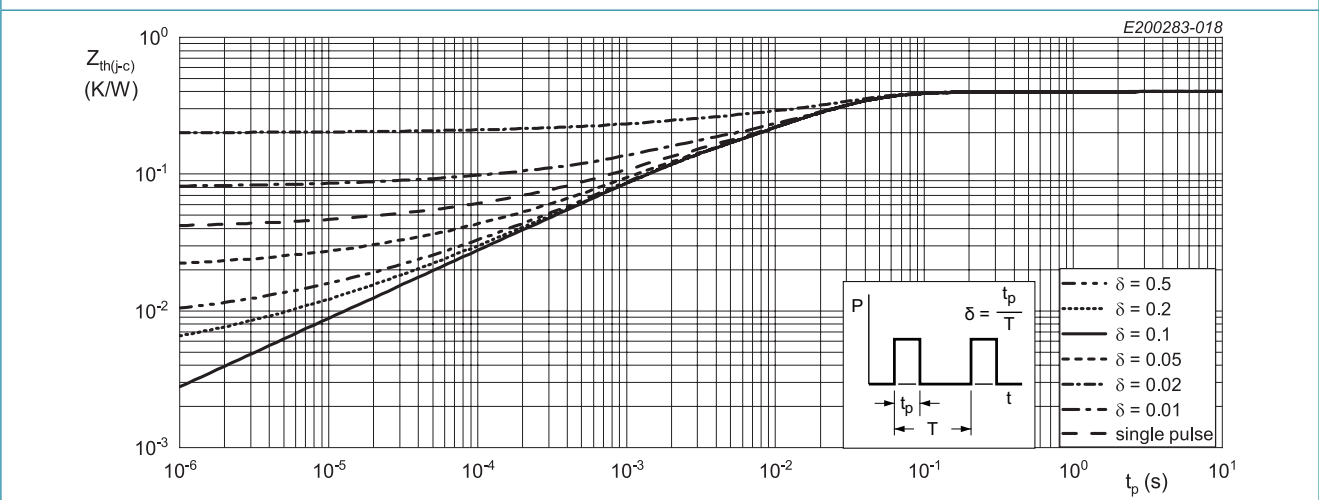
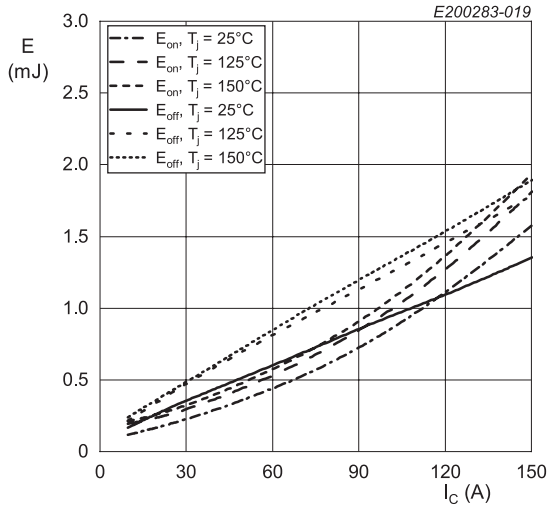


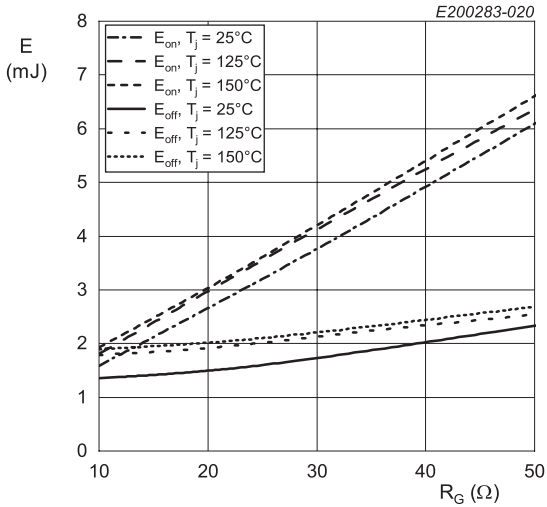
Fig. 18. Typical Transient thermal impedance Diode, D1/D4/D5/D6

Typical Characteristics - IGBT T1/T4 Comutates Diode D5/D6



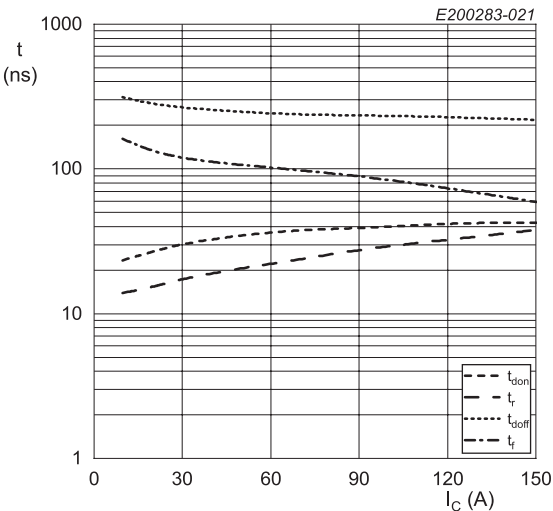
$R_g = 10 \Omega$ ;  $V_{GE} = \pm 15V$ ;  
 $V_{CE} = 300 V$ ; inductive load

Fig. 19. Typical switching energy loss as a function of collector current, T1/T4



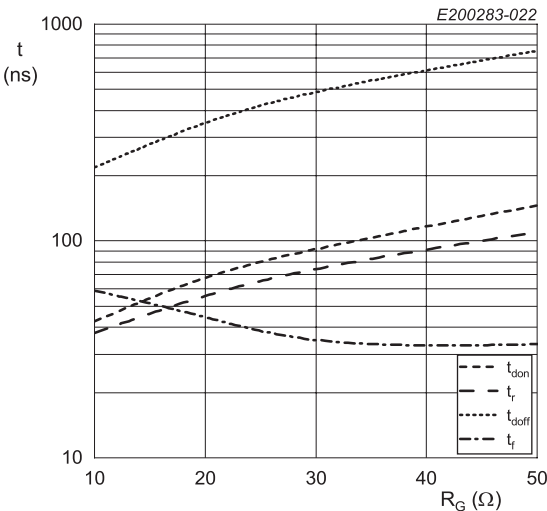
$I_C = 150 A$ ;  $V_{GE} = \pm 15V$ ;  
 $V_{CE} = 300 V$ ; inductive load

Fig. 20. Typical switching energy loss as a function of gate resistance, T1/T4



$R_g = 10 \Omega$ ;  $V_{GE} = \pm 15V$ ;  $T_j = 150 \text{ }^\circ\text{C}$ ;  
 $V_{CE} = 300 V$ ; inductive load

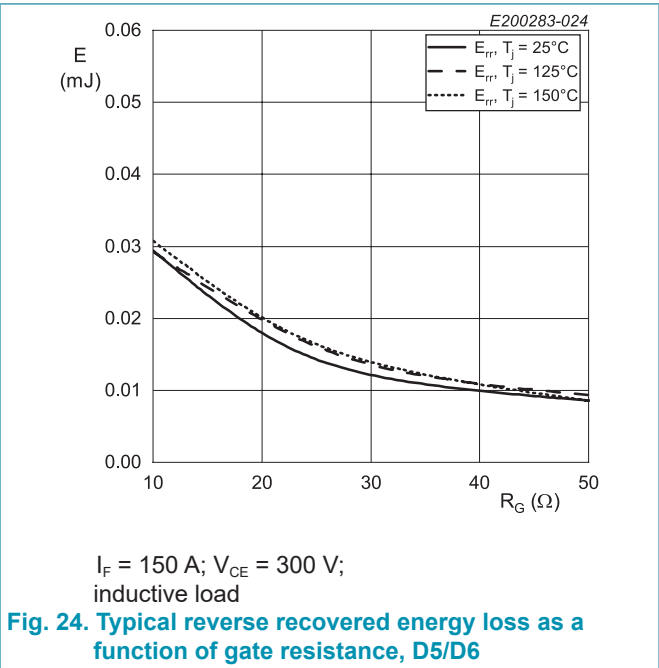
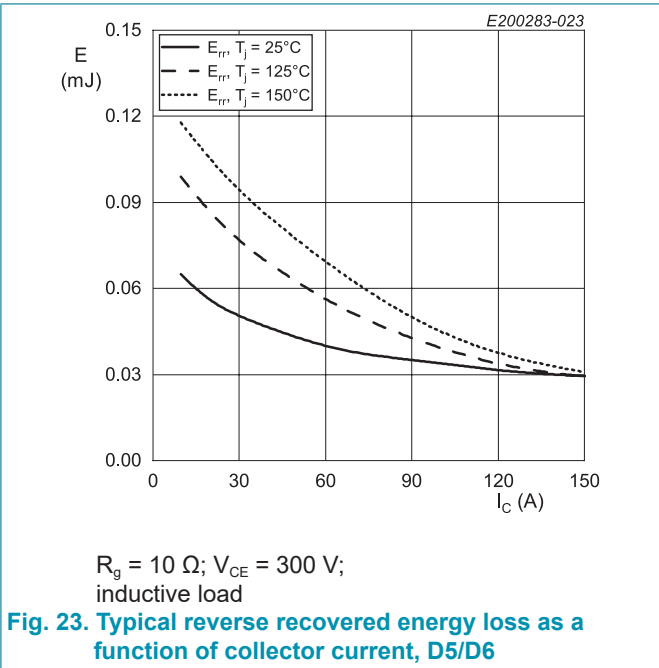
Fig. 21. Typical switching times as a function of collector current, T1/T4



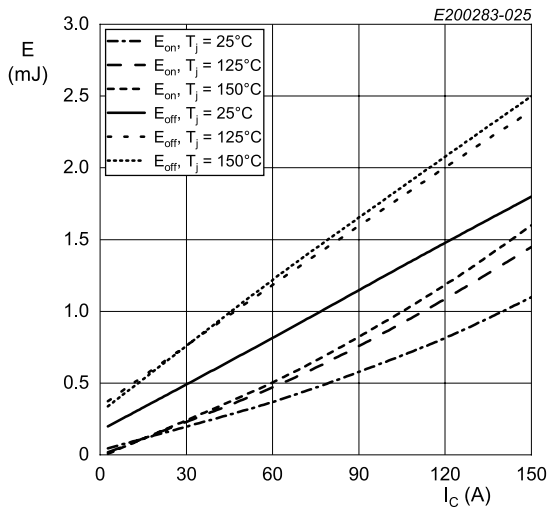
$I_C = 150 A$ ;  $V_{GE} = \pm 15V$ ;  $T_j = 150 \text{ }^\circ\text{C}$ ;  
 $V_{CE} = 300 V$ ; inductive load

Fig. 22. Typical switching times as a function of gate resistance, T1/T4

Typical Characteristics - IGBT T1/T4 Comutates Diode D5/D6

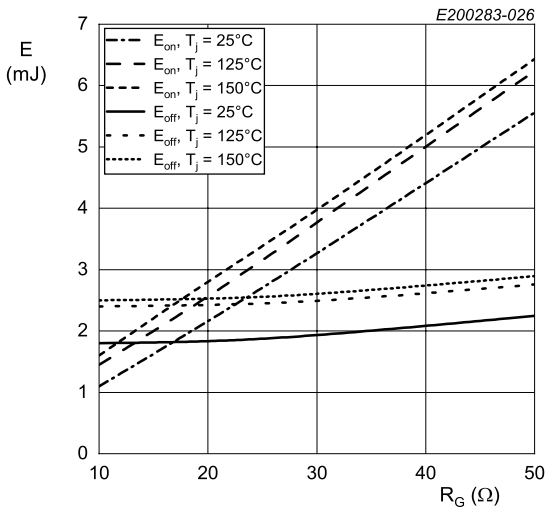


Typical Characteristics - IGBT T2/T3 Comutates Diode D1/D4



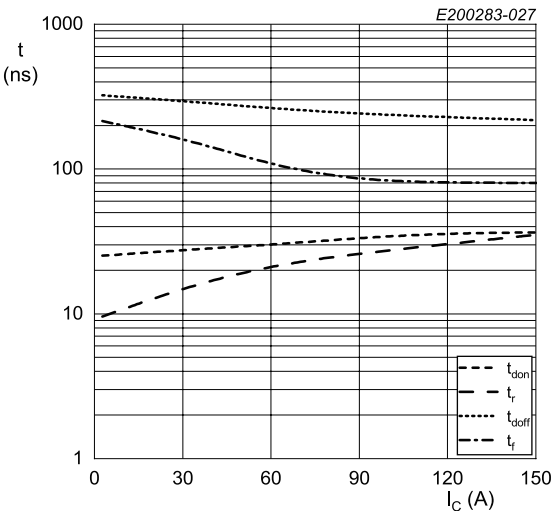
$R_g = 10\ \Omega$ ;  $V_{GE} = \pm 15V$ ;  
 $V_{CE} = 300\ V$ ; inductive load

Fig. 25. Typical switching energy loss as a function of collector current, T2/T3



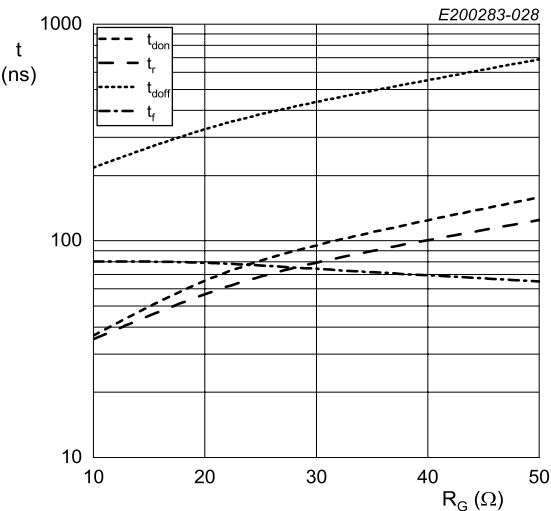
$I_C = 150\ A$ ;  $V_{GE} = \pm 15V$ ;  
 $V_{CE} = 300\ V$ ; inductive load

Fig. 26. Typical switching energy loss as a function of gate resistance, T2/T3



$R_g = 10\ \Omega$ ;  $V_{GE} = \pm 15V$ ;  $T_j = 150\ ^\circ C$ ;  
 $V_{CE} = 300\ V$ ; inductive load

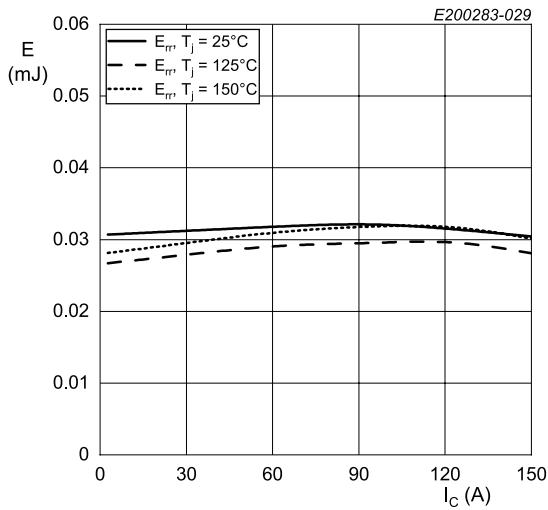
Fig. 27. Typical switching times as a function of collector current, T2/T3



$I_C = 150\ A$ ;  $V_{GE} = \pm 15V$ ;  $T_j = 150\ ^\circ C$ ;  
 $V_{CE} = 300\ V$ ; inductive load

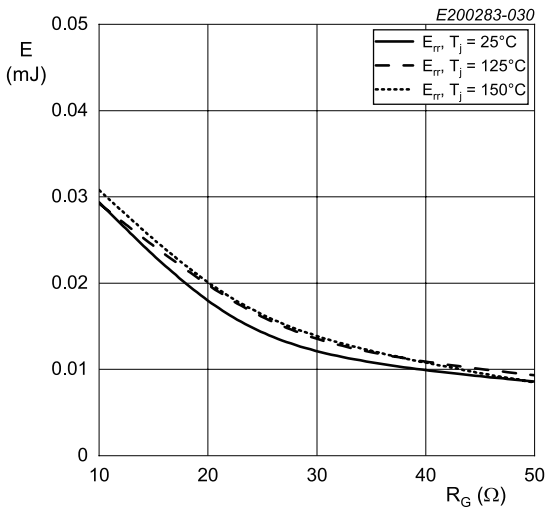
Fig. 28. Typical switching times as a function of gate resistance, T2/T3

Typical Characteristics - IGBT T2/T3 Comutates Diode D1/D4



$R_g = 10\ \Omega$ ;  $V_{CE} = 300\ \text{V}$ ;  
inductive load

Fig. 29. Typical reverse recovered energy loss as a function of collector current, D1/D4



$I_F = 150\ \text{A}$ ;  $V_{CE} = 300\ \text{V}$ ;  
inductive load

Fig. 30. Typical reverse recovered energy loss as a function of gate resistance, D1/D4

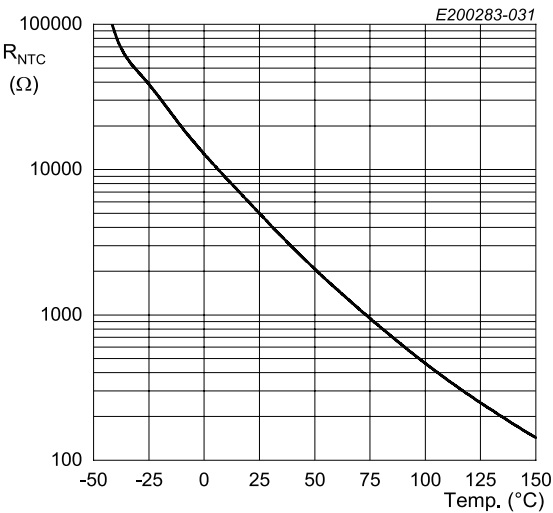
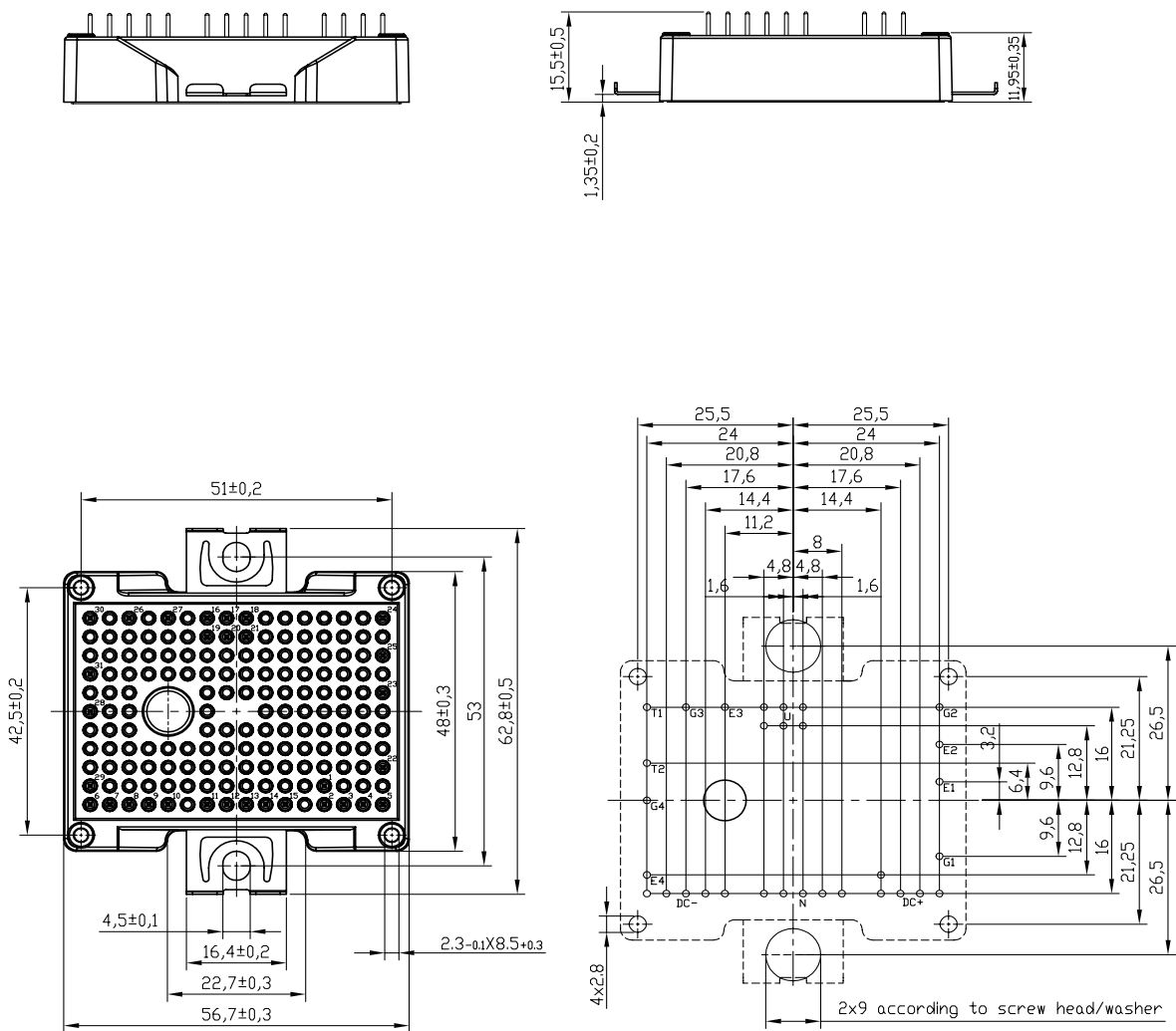


Fig. 31. Typical NTC characteristic as a function of temperature

10. Package outline

Package Outline

Dimensions in mm





## 11. Legal information

### Data sheet status

| Document status [1][2]         | Product status [3] | Definition                                                                            |
|--------------------------------|--------------------|---------------------------------------------------------------------------------------|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production         | This document contains the product specification.                                     |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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