Product data sheet

1. General description

WMG50S06B1S is a six-pack module consisting of six 50A, 650 V IGBTs with inverse diodes and an NTC thermistor, which excels in providing high current density. The integrated field stop trench IGBTs and FRDs provide lower conduction losses and switching losses, enabling designers to achieve high efficiency and superior reliability.



2. Features and benefits

- Six-pack topology
- Low switching losses
- Low Vcesat
- Compact design
- Solder pin
- · Integrated NTC temperature sensor
- Al₂O₃ substrate with low thermal resistance

3. Applications

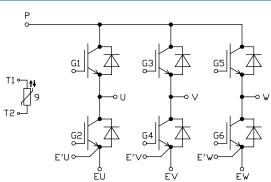
- Air Conditioning
- Motor Drives
- · Servo Drives
- UPS Systems

4. Ordering information

Table 1. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WMG50S06B1S	WeEnPACK-B1	WMG50S06B1ST	Tray	_	WeEnPACK- B1PSB-A	28-Jun-2024

5. Circuit diagram



6. Limiting values

Table 2. Limiting values

Symbol	Parameter	Test Condition	Value	Unit
IGBT				
V _{CE}	Collector-emitter voltage		650	V
V_{GE}	Gate-emitter voltage		±20	V
I _C	Continous collector current	T_C = 80 °C, limited by T_{jmax}	50	Α
Cpulse	Pulsed collector current	tp limited by T _{jmax}	150	А
P _{tot}	Total power dissipation	T _C = 80 °C	152	W
t _{sc}	Short circuit withstand time	$V_{GE} = 15 \text{ V}; V_{CC} = 400 \text{ V};$ $T_j = 150 \text{ °C}$	5	μs
T _{jmax}	Maximum junction temperature		175	°C
Diode				
V_{RRM}	Diode repetitive peak reverse voltage		650	V
I _F	Diode continous forward current	T_C = 80 °C, limited by T_{jmax}	42	А
I _{FRM}	Diode repetitive reak forward current	tp limited by T _{jmax}	126	А
P _{tot}	Total power dissipation	T _C = 80 °C	64	W
T _{jmax}	Maximum junction temperature		175	°C

7. Module package thermal & insulation properties

Table 3. Thermal & Insulation properties

Symbol	Parameter	Test Condition	Value	Unit
V _{ISOL}	RMS isolation voltage	T _j = 25 °C, all terminals shorted, f = 50 Hz, t = 1 min	2500	V
d _{Creep}	Creepage distance	terminal to heatsink	11.5	mm
d _{Clear}	Clearance	terminal to heatsink	10	mm
CTI	Comperative tracking index		> 200	
T _{stg}	Storage temperature		-40 to 125	°C

8. Electrical characteristics

Table 4. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
IGBT cha	racteristics					
V _{CEsat} Collector-emitter saturation volt		$V_{GE} = 15 \text{ V}; I_{C} = 50 \text{ A}; T_{j} = 25 \text{ °C}$	-	1.55	-	V
	$V_{GE} = 15 \text{ V}; I_{C} = 50 \text{ A}; T_{j} = 150 \text{ °C}$	-	2.0	-	V	
$V_{\text{GE(th)}}$	Gate-emitter threhold voltage	$I_{C} = 0.5 \text{ mA}; V_{CE} = V_{GE}; T_{j} = 25 \text{ °C}$	4	5	6	V
I _{CES}	Zero gate voltage collector current	$V_{CE} = 650 \text{ V}; V_{GE} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	40	μA
I _{GES}	Gate leakage current	$V_{GE} = 20 \text{ V}; V_{CE} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	400	nA
Q_G	Gate charge	$V_{CC} = 300 \text{ V}; I_C = 50 \text{ A}; V_{GE} = \pm 15 \text{ V}$	-	204	-	nC
C _{ies}	Input capacitance	$V_{CE} = 25 \text{ V}; V_{GE} = 0 \text{V}; f = 1 \text{ MHz};$	-	3477	-	pF
C _{oes}	Output capacitance	T _j = 25 °C	-	184	-	pF
C _{res}	Reverse transfer capacitance		-	44	-	pF
t _{d(on)}	Turn-on delay time	T _j = 25 °C	-	30	-	nS
t _r	Rise time	$V_{CC} = 300 \text{ V}; I_C = 50 \text{ A}; V_{GE} = \pm 15 \text{ V};$ $R_a = 10 \Omega$	-	29	-	nS
$t_{\text{d(off)}}$	Turn-off delay time	. Trg = 10 22	-	107	-	nS
t _f	Fall time		-	54	-	nS
E _{on}	Turn-on energy		-	0.56	-	mJ
E _{off}	Turn-off energy		-	0.69	-	mJ
t _{d(on)}	Turn-on delay time	T _i =150 °C	-	30	-	nS
t _r	Rise time	$V_{CC} = 300 \text{ V}; I_C = 50 \text{ A}; V_{GE} = \pm 15 \text{ V};$ $R_a = 10 \Omega$	-	33	-	nS
$t_{d(off)}$	Turn-off delay time	1 Ng - 10 22	-	120	-	nS
t _f	Fall time		-	81	-	nS
E _{on}	Turn-on energy		-	1	-	mJ
E _{off}	Turn-off energy		-	0.9	-	mJ
R _{thJC}	Thermal resistance, junction to case	per IGBT	-	0.46	-	K/W
T _{jop}	Operation temperature		-40	-	150	°C
Inverter D	liode characteristics					
V _F	Diode forward voltage	I _F = 50 A; T _j = 25 °C	-	2.3	-	V
		I _F = 50 A; T _j = 150 °C	-	1.9	-	V
Q _{rr}	Reverse recovery charge	T _j = 25 °C	-	341	-	nC
I _{rrm}	Peak reverse recovery current	$V_R = 300 \text{ V}; I_F = 50 \text{ A};$ di/dt = 500 A/µs;	-	10.5	-	Α
E _{rr}	Reverse recovery energy		-	0.04	-	mJ
Q _{rr}	Reverse recovery charge	T _i = 150 °C	-	1488	-	nC
I _{rrm}	Peak reverse recovery current	$V_R = 300 \text{ V; } I_F = 50 \text{ A;}$ di/dt = 500 A/µs;	-	18	-	А
E _{rr}	Reverse recovery energy	αι/αι	-	0.18	-	mJ
R _{thJC}	Thermal resistance, junction to case	per diode	-	0.9	-	K/W
T _{jop}	Operation temperature		-40	_	150	°C

9. NTC - thermistor

Table 5. NTC - Thermistor

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R ₂₅	Rated resistance	T _c = 25 °C	-	5000	-	Ω
R ₁₀₀		T _c = 100 °C		465±5%		Ω
B _{25/50}	B-value	$R_2 = R_{25} \exp[B_{25/50}(1/T_2-1(298.15K))]$;	3380±5%)	K

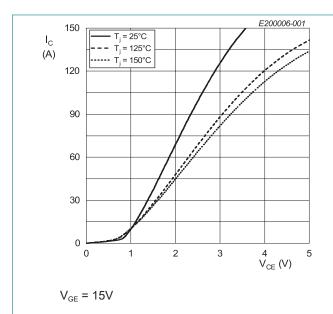


Fig. 1. IGBT typical output characteristics

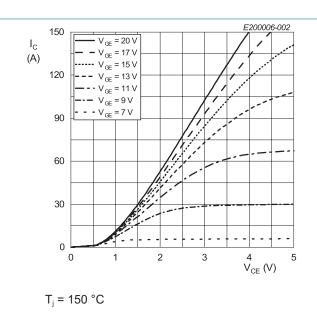


Fig. 2. IGBT typical output characteristics

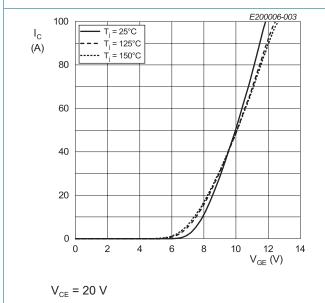


Fig. 3. IGBT typical transfer characteristics

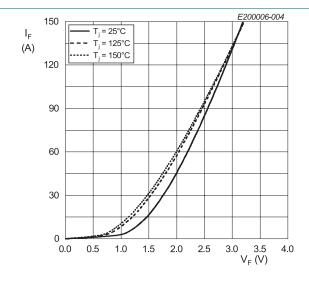
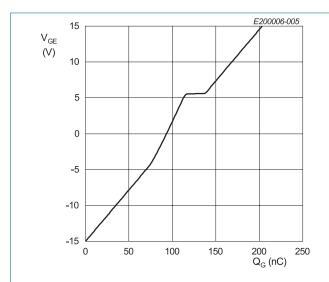
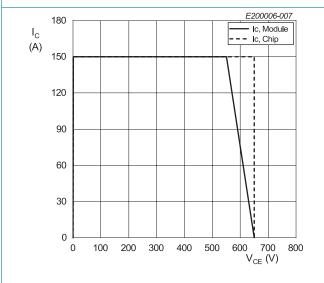


Fig. 4. Diode typical forward characteristics



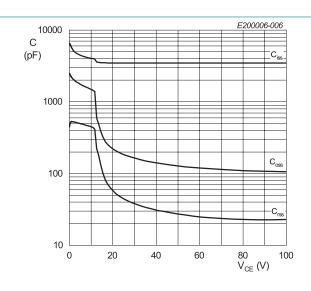
$$I_{C}$$
 = 50 A; V_{CC} = 300 V; T_{j} = 25 °C

Fig. 5. Typical gate charge



 $R_q = 10 \Omega; V_{GE} = \pm 15V; T_j = 150 °C$

Fig. 7. Reverse bias safe operating area



 $V_{GE} = 0$; f = 1 MHz; $T_i = 25$ °C

Fig. 6. Typical capacitance as a function of collector emitter voltage

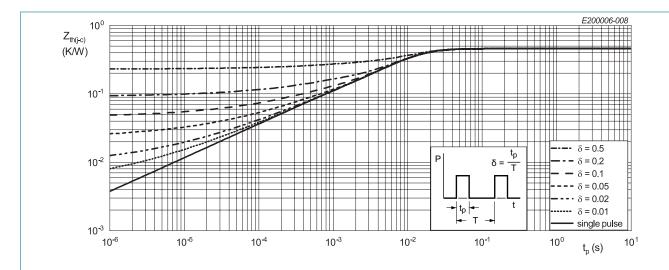


Fig. 8. Typical Transient thermal impedance IGBT

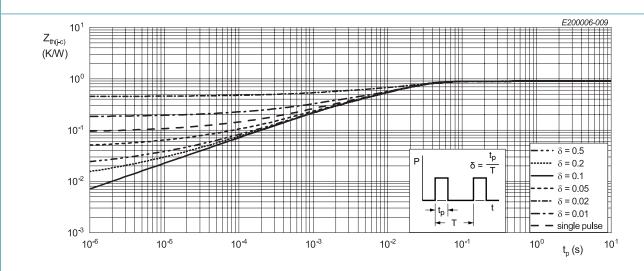
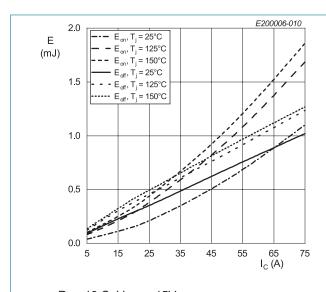
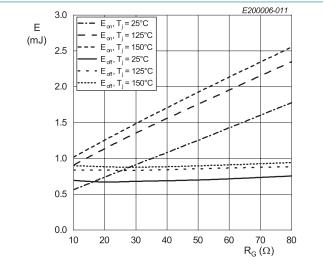


Fig. 9. Typical Transient thermal impedance Diode



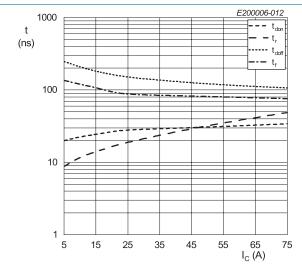
 R_g = 10 Ω ; V_{GE} = ±15V; V_{CE} = 300 V; inductive load

Fig. 10. Typical switching energy loss as a function of collector current



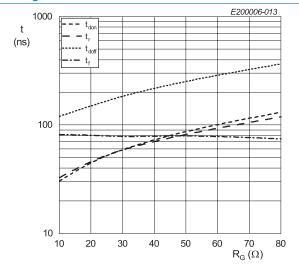
 I_C = 50 A; V_{GE} = ±15V; V_{CE} = 300 V; inductive load

Fig. 11. Typical switching energy loss as a function of gate resistance



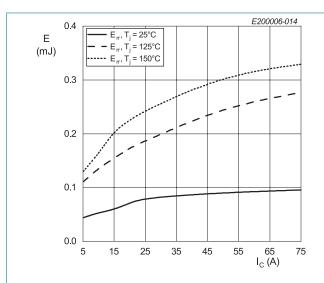
 R_g = 10 Ω ; V_{GE} = ±15V; T_j = 150 °C; V_{CE} = 300 V; inductive load

Fig. 12. Typical switching times as a function of collector current



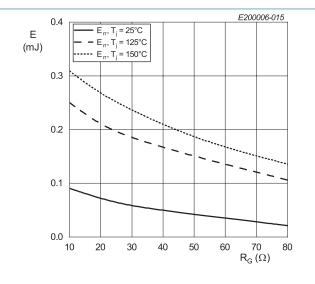
 I_{C} = 50 A; V_{GE} = ±15V; T_{j} = 150 °C; V_{CE} = 300 V; inductive load

Fig. 13. Typical switching times as a function of gate resistance



 R_g = 10 Ω ; V_{CE} = 300 V; inductive load

Fig. 14. Typical reverse recovered energy loss as a function of collector current



 $I_F = 50 \text{ A}; V_{CE} = 300 \text{ V};$ inductive load

Fig. 15. Typical reverse recovered energy loss as a function of gate resistance

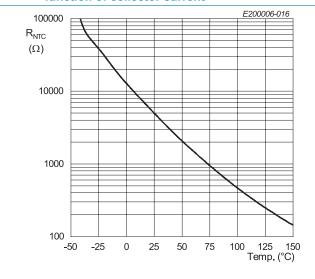
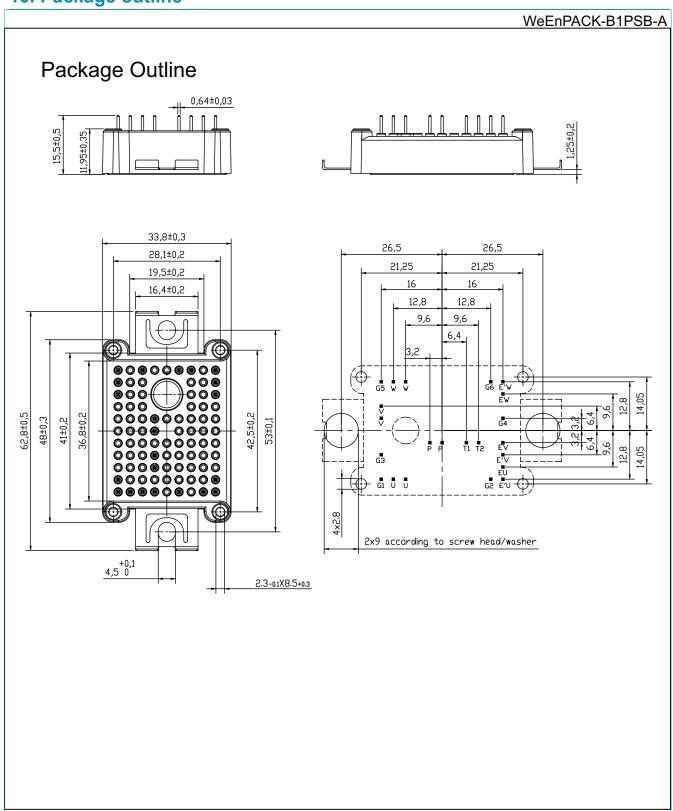


Fig. 16. Typical NTC characteristic as a function of temperature

10. Package outline



11. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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