**Product data sheet** 

## 1. General description

WMG200H12T2S is a Half Bridge module consisting of two 200A, 1200 V IGBTs with inverse diodes, which excels in providing high current density. The integrated field stop trench IGBTs and FRDs provide lower conduction losses and switching losses, enabling designers to achieve high efficiency and superior reliability.



### 2. Features and benefits

- · Half Bridge topology
- Low switching losses
- Low Vcesat
- Compact design
- · Fast & soft reverse recovery anti-paralle FWD
- Isolated copper baseplate using DBC technology

## 3. Applications

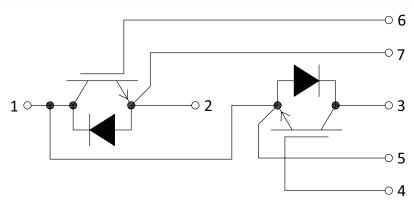
- Motor Drives
- Servo Drives
- · Electronic welder Systems

## 4. Ordering information

### Table 1. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WMG200H12T2S	WeEnPACK- 62mm	WMG200H12T2ST	Tray	10	WeEnPACK- 62mmPHB-A	10-Feb-2025

# 5. Circuit diagram



# 6. Limiting values

#### Table 2. Limiting values

Symbol	Parameter	Test Condition	Value	Unit
IGBT				
V <sub>CE</sub>	Collector-emitter voltage		1200	V
$V_{\sf GE}$	Gate-emitter voltage		±20	V
I <sub>CN</sub>	Implemented collector current		400	А
I <sub>C</sub>	Continous collector current	$T_C$ = 80 °C, limited by $T_{jmax}$	200	Α
Cpulse	Pulsed collector current	tp limited by T <sub>jmax</sub>	600	А
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> = 80 °C	950	W
t <sub>sc</sub>	Short circuit withstand time	$V_{GE} = 15 \text{ V}; V_{CC} = 600 \text{ V};$ $T_j = 150 \text{ °C}$	10	μs
T <sub>jmax</sub>	Maximum junction temperature		175	°C
Diode				
$V_{RRM}$	Diode repetitive peak reverse voltage		1200	V
I <sub>FN</sub>	Diode Implemented collector current		400	Α
I <sub>F</sub>	Diode continous forward current	$T_C$ = 80 °C, limited by $T_{jmax}$	200	А
I <sub>FRM</sub>	Diode repetitive reak forward current	tp limited by T <sub>jmax</sub>	600	А
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> = 80 °C	559	W
T <sub>jmax</sub>	Maximum junction temperature		175	°C

# 7. Module package thermal & insulation properties

### **Table 3. Thermal & Insulation properties**

Symbol	Parameter	Test Condition	Value	Unit
V <sub>ISOL</sub>	RMS isolation voltage	T <sub>j</sub> = 25 °C, all terminals shorted, f = 50 Hz, t = 1 min	2500	V
d <sub>Creep</sub>	Creepage distance	terminal to heatsink	11.5	mm
d <sub>Clear</sub>	Clearance	terminal to heatsink	10	mm
СТІ	Comperative tracking index		> 200	
T <sub>stg</sub>	Storage temperature		-40 to 125	°C

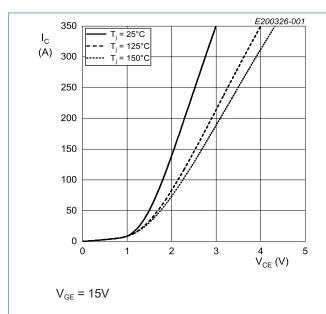
## 8. Electrical characteristics

**Table 4. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
IGBT cha	racteristics					
$V_{\text{CEsat}}$	Collector-emitter saturation voltage	$V_{GE} = 15 \text{ V}; I_C = 200 \text{ A}; T_j = 25 \text{ °C}$	-	2.3	-	V
		$V_{GE} = 15 \text{ V}; I_{C} = 200 \text{ A}; T_{j} = 150 ^{\circ}\text{C}$	-	3.1	-	V
$V_{\text{GE(th)}}$	Gate-emitter threhold voltage	$I_{\rm C}$ = 2.5 mA; $V_{\rm CE}$ = $V_{\rm GE}$ ; $T_{\rm j}$ = 25 °C	4.2	5.3	6.4	V
I <sub>CES</sub>	Zero gate voltage collector current	$V_{CE} = 1200 \text{ V}; V_{GE} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	-	1	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = 20 \text{ V}; V_{CE} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	400	nA
$Q_{G}$	Gate charge	$V_{CC}$ = 600 V; $I_{C}$ = 200 A; $V_{GE}$ = ±15 V	-	1	-	μC
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25 V; V <sub>GE</sub> = 0V; f = 1 MHz;	-	33	-	nF
C <sub>oes</sub>	Output capacitance	$T_j = 25 ^{\circ}\text{C}$	-	0.5	-	nF
C <sub>res</sub>	Reverse transfer capacitance		-	0.1	-	nF
t <sub>d(on)</sub>	Turn-on delay time	T <sub>j</sub> = 25 °C	-	31	-	nS
t <sub>r</sub>	Rise time	$V_{CC} = 600 \text{ V}; I_{C} = 200 \text{ A}; V_{GE} = \pm 15 \text{ V};$ $R_{g} = 1.8 \Omega$	-	24	-	nS
$t_{d(off)}$	Turn-off delay time		-	180	-	nS
t <sub>f</sub>	Fall time		-	35	-	nS
E <sub>on</sub>	Turn-on energy		-	5.05	-	mJ
E <sub>off</sub>	Turn-off energy		-	5.5	-	mJ
t <sub>d(on)</sub>	Turn-on delay time	T <sub>j</sub> =150 °C	-	30	-	nS
t <sub>r</sub>	Rise time	$V_{CC} = 600 \text{ V}; I_C = 200 \text{ A}; V_{GE} = \pm 15 \text{ V};$ $R_g = 1.8 \Omega$	-	28	-	nS
$t_{d(off)}$	Turn-off delay time		-	203	-	nS
t <sub>f</sub>	Fall time		-	103	-	nS
E <sub>on</sub>	Turn-on energy		-	9.15	-	mJ
E <sub>off</sub>	Turn-off energy		-	8.25	-	mJ
R <sub>thJC</sub>	Thermal resistance, junction to case		-	0.1	-	K/W
T <sub>jop</sub>	Operation temperature		-40	-	150	°C
Inverter D	liode characteristics		1		l	
V <sub>F</sub>	Diode forward voltage	I <sub>F</sub> = 200 A; T <sub>j</sub> = 25 °C	-	2.4	-	V
		I <sub>F</sub> = 200 A; T <sub>j</sub> = 150 °C	-	2.2	-	V
Q <sub>rr</sub>	Reverse recovery charge	T <sub>j</sub> = 25 °C	-	12364	-	nC
I <sub>rrm</sub>	Peak reverse recovery current	$\dot{V}_{R} = 600 \text{ V}; I_{F} = 200 \text{ A};$ di/dt = 7000 A/µs;	-	270	-	А
E <sub>rr</sub>	Reverse recovery energy		-	6.5	-	mJ
Q <sub>rr</sub>	Reverse recovery charge	T <sub>i</sub> = 150 °C	-	21121	-	nC
I <sub>rrm</sub>	Peak reverse recovery current	V <sub>R</sub> = 600 V; I <sub>F</sub> = 200 A; di/dt = 6000 A/µs;	-	313	-	А
E <sub>rr</sub>	Reverse recovery energy	,	-	14.2	-	mJ
R <sub>thJC</sub>	Thermal resistance, junction to case		-	0.17	-	K/W
$T_jop$	Operation temperature		-40	_	150	°C

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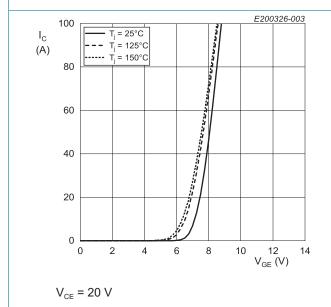
**Module** 



V<sub>GE</sub> = 20 V V<sub>GE</sub> = 17 V (A) 300 V<sub>GE</sub> = 15 V  $V_{GE}$  = 13 VV<sub>GE</sub> = 11 V 250 V<sub>GE</sub> = 9 V - - V<sub>GE</sub> = 7 V 200 150 100 50 0 4 V<sub>CE</sub> (V) T<sub>i</sub> = 150 °C

Fig. 1. IGBT typical output characteristics





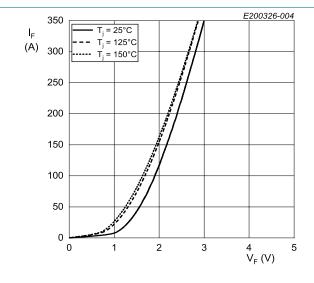
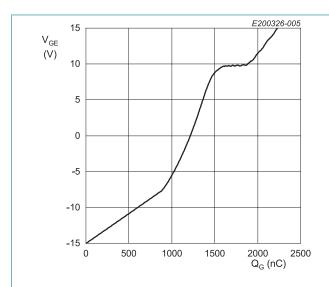


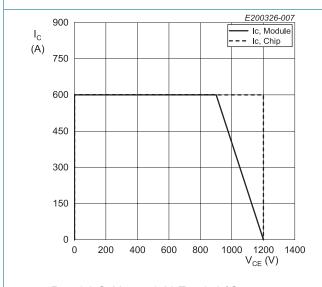
Fig. 3. IGBT typical transfer characteristics

Fig. 4. Diode typical forward characteristics



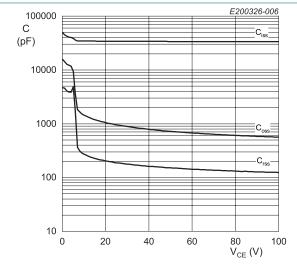
 $I_{\text{C}}$  = 200 A;  $V_{\text{CC}}$  = 600 V;  $T_{j}$  = 25 °C

Fig. 5. Typical gate charge



 $R_q = 1.8 \Omega; V_{GE} = \pm 15V; T_i = 150 °C$ 

Fig. 7. Reverse bias safe operating area



 $V_{GE} = 0V$ ; f = 1 MHz;  $T_i = 25 \, ^{\circ}C$ 

Fig. 6. Typical capacitance as a function of collector emitter voltage

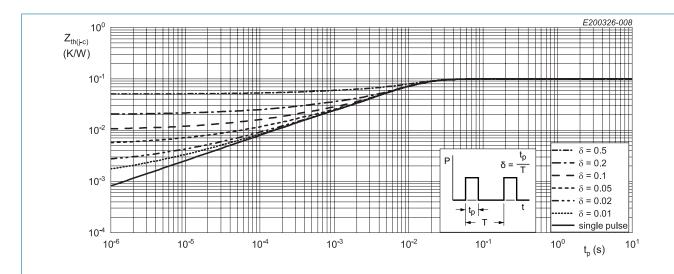


Fig. 8. Typical Transient thermal impedance IGBT

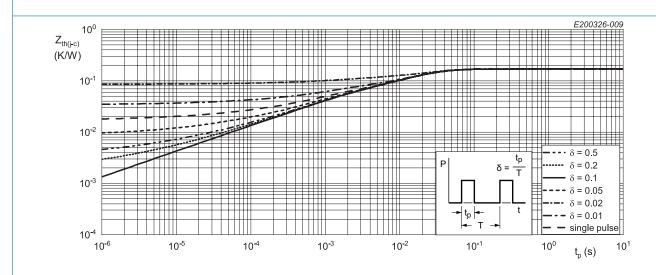
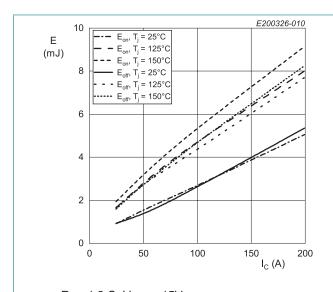
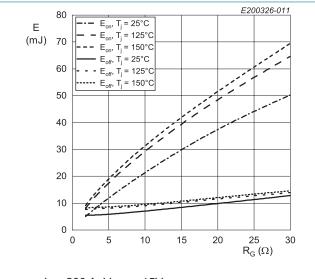


Fig. 9. Typical Transient thermal impedance Diode



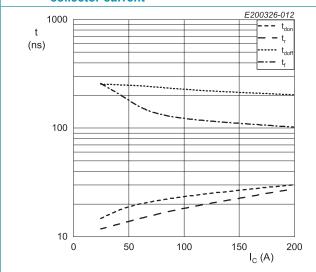
 $R_g$  = 1.8  $\Omega$ ;  $V_{GE}$  = ±15V;  $V_{CE}$  = 600 V; inductive load

Fig. 10. Typical switching energy loss as a function of collector current



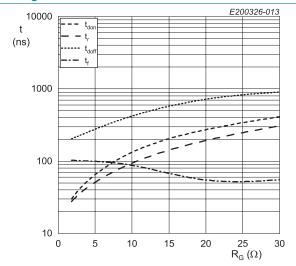
 $I_C = 200 \text{ A}$ ;  $V_{GE} = \pm 15 \text{V}$ ;  $V_{CE} = 600 \text{ V}$ ; inductive load

Fig. 11. Typical switching energy loss as a function of gate resistance



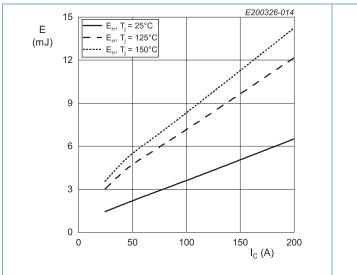
 $R_g$  = 1.8  $\Omega$ ;  $V_{GE}$  = ±15V;  $T_j$  = 150 °C;  $V_{CE}$  = 600 V; inductive load

Fig. 12. Typical switching times as a function of collector current



 $I_{C}$  = 200 A;  $V_{GE}$  = ±15V;  $T_{j}$  = 150 °C;  $V_{CE}$  = 600 V; inductive load

Fig. 13. Typical switching times as a function of gate resistance



inductive load

Fig. 14. Typical reverse recovered energy loss as a function of collector current

 $R_g = 1.8 \Omega$ ;  $V_{CE} = 600 V$ ;

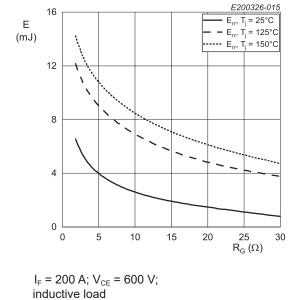
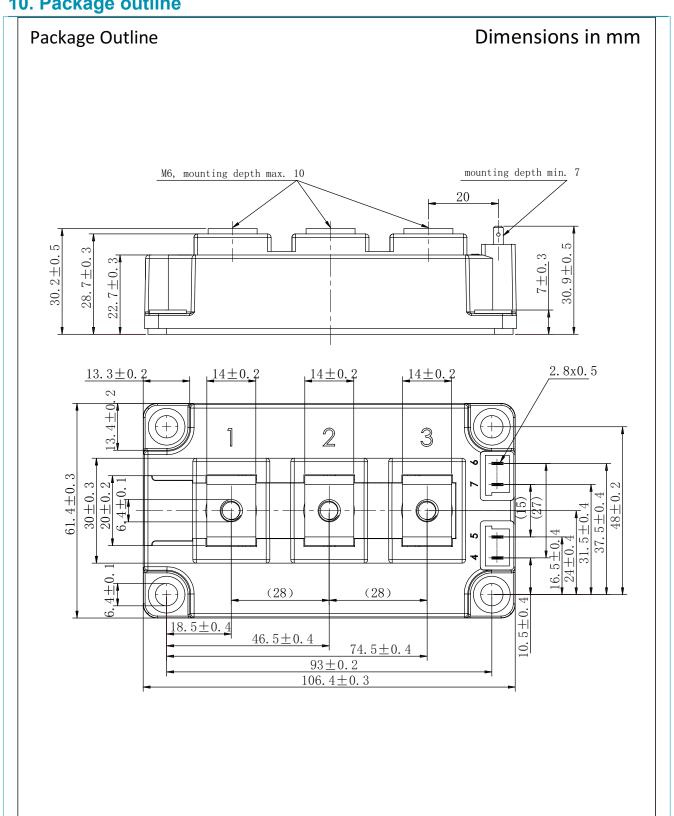


Fig. 15. Typical reverse recovered energy loss as a function of gate resistance

# 10. Package outline



### 11. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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