Product data sheet

1. General description

WMG100H12T2S is a Half Bridge module consisting of two 100A, 1200V IGBTs with inverse diodes, which excels in providing high current density. The integrated field stop trench IGBTs and FRDs provide lower conduction losses and switching losses, enabling designers to achieve high efficiency and superior reliability.



2. Features and benefits

- · Half Bridge topology
- Low switching losses
- Low Vcesat
- Compact design
- · Fast & soft reverse recovery anti-paralle FWD
- Isolated copper baseplate using DBC technology

3. Applications

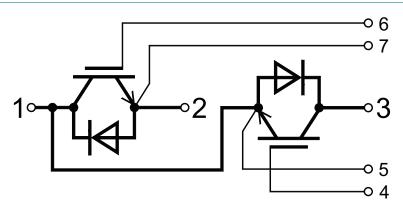
- Motor Drives
- Servo Drives
- · Electronic welder Systems

4. Ordering information

Table 1. Ordering information

Type number	Package	Orderable part number	Packing	Small packing	Package	Package
	Name		method	quantity	version	issue date
WMG100H12T2S	WeEnPACK-	WMG100H12T2ST	Tray	10	WeEnPACK-	10-Apr-2025
	62mm				62mmPHB-A	

5. Circuit diagram



6. Limiting values

Table 2. Limiting values

Symbol	Parameter	Test Condition	Value	Unit
IGBT				
V _{CE}	Collector-emitter voltage		1200	V
$V_{\sf GE}$	Gate-emitter voltage		±20	V
I _{CN}	Implemented collector current		200	А
I _C	Continous collector current	T_C = 80 °C, limited by T_{jmax}	100	А
Cpulse	Pulsed collector current	tp limited by T _{jmax}	300	А
P _{tot}	Total power dissipation	T _C = 80 °C	594	W
t _{sc}	Short circuit withstand time	$V_{GE} = 15 \text{ V}; V_{CC} = 600 \text{ V};$ $T_j = 150 ^{\circ}\text{C}$	10	μs
T_{jmax}	Maximum junction temperature		175	°C
Diode				
V_{RRM}	Diode repetitive peak reverse voltage		1200	V
I _{FN}	Diode Implemented collector current		200	А
I _F	Diode continous forward current	T_C = 80 °C, limited by T_{jmax}	100	А
I _{FRM}	Diode repetitive reak forward current	tp limited by T _{jmax}	300	Α
P _{tot}	Total power dissipation	T _C = 80 °C	365	W
T _{jmax}	Maximum junction temperature		175	°C

7. Module package thermal & insulation properties

Table 3. Thermal & Insulation properties

Symbol	Parameter	Test Condition	Value	Unit
V _{ISOL}	RMS isolation voltage	T _j = 25 °C, all terminals shorted, f = 50 Hz, t = 1 min	2500	V
d _{Creep}	Creepage distance	terminal to heatsink	11.5	mm
d _{Clear}	Clearance	terminal to heatsink	10	mm
CTI	Comperative tracking index		> 200	
T _{stg}	Storage temperature		-40 to 125	°C

8. Electrical characteristics

Table 4. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
IGBT cha	racteristics					
V_{CEsat}	Collector-emitter saturation voltage	$V_{GE} = 15 \text{ V}; I_{C} = 100 \text{ A}; T_{j} = 25 ^{\circ}\text{C}$	-	2.2	-	V
		$V_{GE} = 15 \text{ V}; I_{C} = 100 \text{ A}; T_{j} = 150 ^{\circ}\text{C}$	-	2.95	-	V
$V_{\text{GE(th)}}$	Gate-emitter threhold voltage	$I_{C} = 1.5 \text{ mA}; V_{CE} = V_{GE}; T_{j} = 25 \text{ °C}$	4.2	5.3	6.4	V
I _{CES}	Zero gate voltage collector current	V _{CE} = 1200 V; V _{GE} = 0 V; T _j = 25 °C	-	-	1	mA
I _{GES}	Gate leakage current	$V_{GE} = 20 \text{ V}; V_{CE} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	400	nA
Q_G	Gate charge	$V_{CC} = 600 \text{ V}; I_C = 100 \text{ A}; V_{GE} = \pm 15 \text{ V}$	-	1.33	-	μC
C _{ies}	Input capacitance	$V_{CE} = 25 \text{ V}; V_{GE} = 0 \text{V}; f = 1 \text{ MHz};$	-	22	-	nF
C _{oes}	Output capacitance	T _j = 25 °C	-	0.6	-	nF
C _{res}	Reverse transfer capacitance		-	0.12	-	nF
t _{d(on)}	Turn-on delay time	T _j = 25 °C	-	15	-	nS
t _r	Rise time	$V_{CC} = 600 \text{ V}; I_{C} = 100 \text{ A}; V_{GE} = \pm 15 \text{ V};$ $R_{\alpha} = 1.8 \Omega$	-	14	-	nS
$t_{\text{d(off)}}$	Turn-off delay time	9 -	-	106	-	nS
t _f	Fall time		-	46	-	nS
E _{on}	Turn-on energy		-	2.4	-	mJ
E _{off}	Turn-off energy		-	2.8	-	mJ
t _{d(on)}	Turn-on delay time	$T_j = 150 ^{\circ}\text{C}$ $V_{CC} = 600 ^{\circ}\text{V}; I_C = 100 ^{\circ}\text{A}; V_{GE} = \pm 15 ^{\circ}\text{V};$ $R_a = 1.8 ^{\circ}\Omega$	-	15	-	nS
t _r	Rise time		-	16	-	nS
$t_{\text{d(off)}}$	Turn-off delay time		-	123	-	nS
t _f	Fall time		-	82	-	nS
E _{on}	Turn-on energy		-	4.0	-	mJ
E _{off}	Turn-off energy		-	4.4	-	mJ
R _{thJC}	Thermal resistance, junction to case		-	0.16	-	K/W
T _{jop}	Operation temperature		-40	-	150	°C
Inverter D	liode characteristics					
V _F	Diode forward voltage	I _F = 100 A; T _j = 25 °C	-	1.95	-	V
		I _F = 100 A; T _j = 150 °C	-	1.8	-	V
Q _{rr}	Reverse recovery charge	T _j = 25 °C	-	6138	-	nC
I _{rrm}	Peak reverse recovery current	V _R = 600 V; I _F = 100 A; di/dt = 5000 A/µs;	-	148	-	Α
E _{rr}	Reverse recovery energy	1 /	-	3.17	-	mJ
Q _{rr}	Reverse recovery charge	T _j = 150 °C	-	10422	-	nC
I _{rrm}	Peak reverse recovery current	V _R = 600 V; I _F = 100 A; di/dt = 5000 A/µs;	-	173	-	Α
E _{rr}	Reverse recovery energy	, ω. συσυνημο,	-	6.7	-	mJ
R _{thJC}	Thermal resistance, junction to case		-	0.26	-	K/W
T _{jop}	Operation temperature		-40	_	150	°C

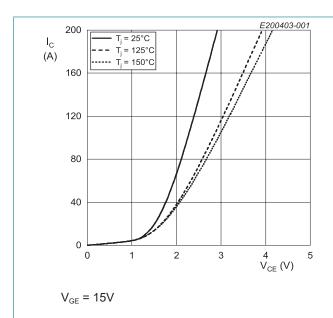


Fig. 1. IGBT typical output characteristics

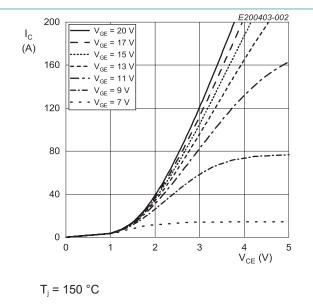


Fig. 2. IGBT typical output characteristics

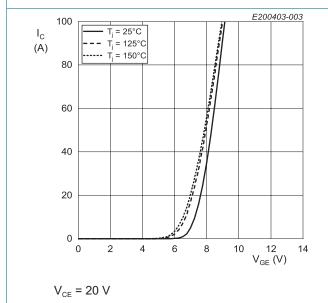


Fig. 3. IGBT typical transfer characteristics

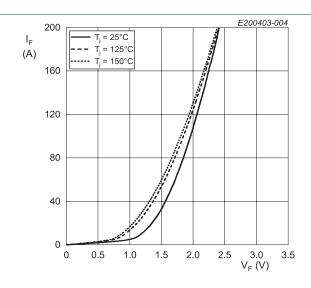
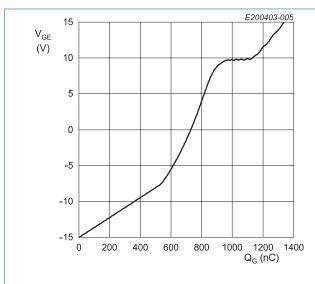
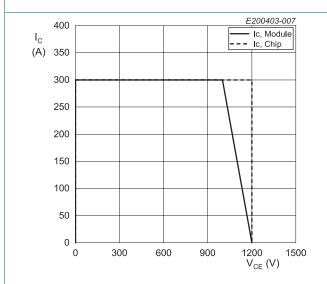


Fig. 4. Diode typical forward characteristics



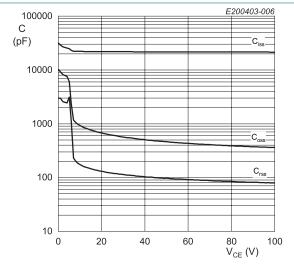
 $I_{\rm C}$ = 100 A; $V_{\rm CC}$ = 600 V; $T_{\rm j}$ = 25 °C

Fig. 5. Typical gate charge



 R_g = 1.8 Ω ; V_{GE} = ±15V; T_j = 150 °C

Fig. 7. Reverse bias safe operating area



 $V_{GE} = 0V; f = 1 \text{ MHz}; T_i = 25 ^{\circ}\text{C}$

Fig. 6. Typical capacitance as a function of collector emitter voltage

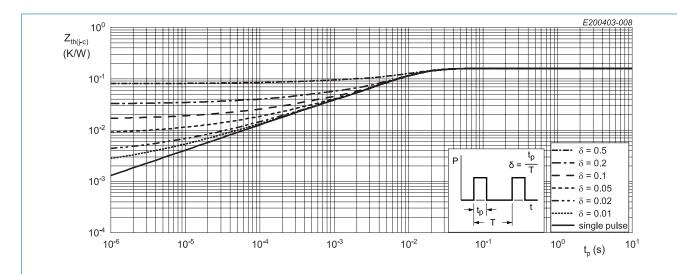


Fig. 8. Typical Transient thermal impedance IGBT

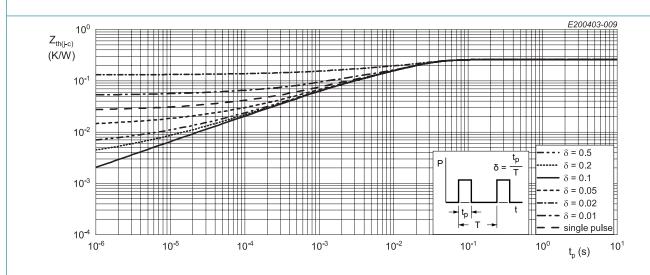
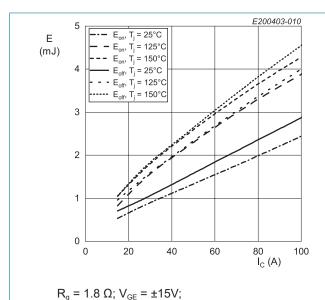
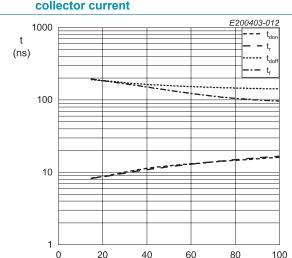


Fig. 9. Typical Transient thermal impedance Diode



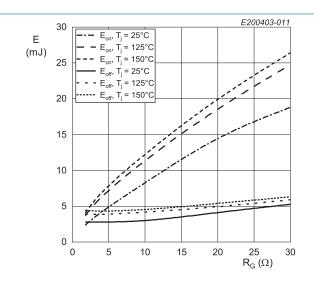
 $V_{CE} = 600 \text{ V}$; inductive load

Fig. 10. Typical switching energy loss as a function of collector current



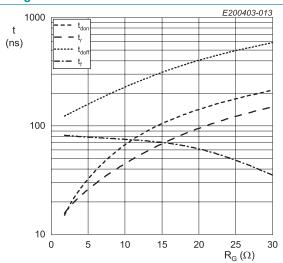
 R_g = 1.8 Ω ; V_{GE} = ±15V; T_j = 150 °C; V_{CE} = 600 V; inductive load

Fig. 12. Typical switching times as a function of collector current



 $I_C = 100 \text{ A}; V_{GE} = \pm 15 \text{V};$ $V_{CE} = 600 \text{ V}; \text{ inductive load}$

Fig. 11. Typical switching energy loss as a function of gate resistance



 I_{C} = 100 A; V_{GE} = ±15V; T_{j} = 150 °C; V_{CE} = 600 V; inductive load

Fig. 13. Typical switching times as a function of gate resistance

 $I_{C}(A)$

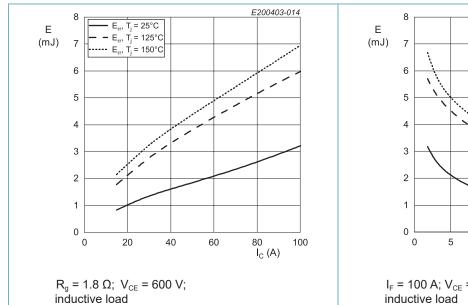
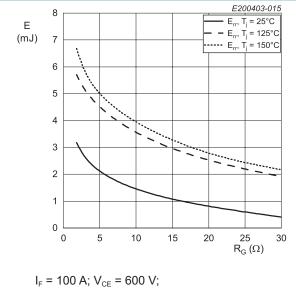


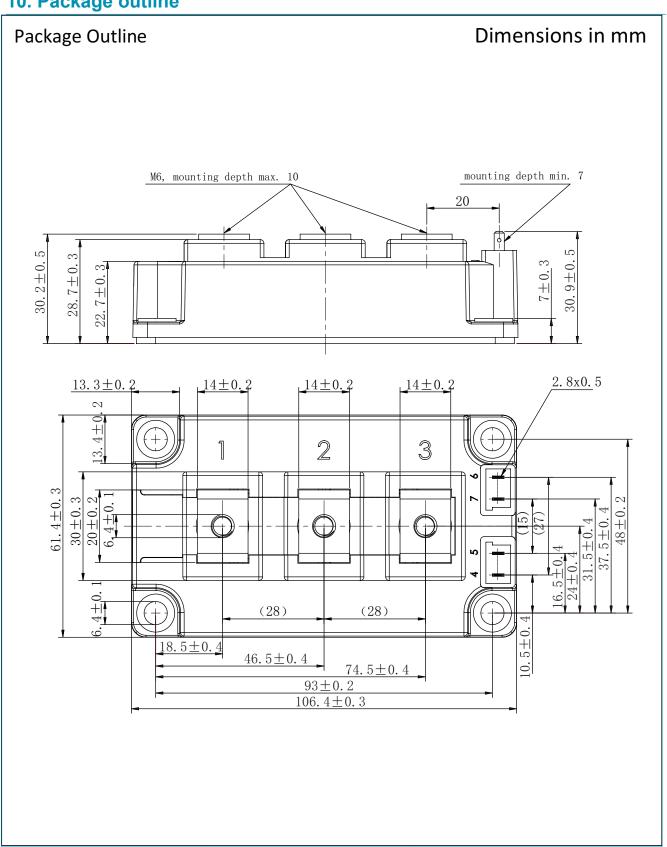
Fig. 14. Typical reverse recovered energy loss as a function of collector current



inductive load

Fig. 15. Typical reverse recovered energy loss as a function of gate resistance

10. Package outline



11. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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