W3MS100N019TL

Split Gate Trench Power MOSFET

Rev.01 - 26 May 2026 **Product data sheet**

1. General description

W3MS100N019TL is a low voltage N-channel MOSFET in TOLL package, which utilizes the split gate technology to provide superior FOM $R_{DS(on)}$*Q_g among silicon based MOSFETs. It is particularly suitable for applications require extreme high efficiency and power density.

RoHS Halogen-Free

2. Features and benefits

- High-speed switching
- Excellent gate charge x $R_{DS(on)}$ product (FOM)
- 100% avalanche tested
- Qualified according to JEDEC criteria

3. Applications

- High-Efficiency DC-DC Converters
- Switching Voltage Regulators
- Motor Drivers

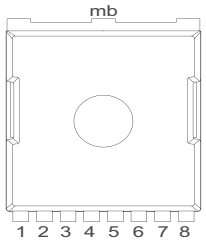
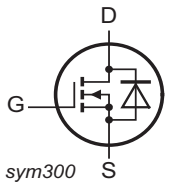
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute maximum rating							
V_{DS}	drain-source voltage			100			V
V_{GS}	gate-source voltage	static		±20			V
I_D	continuous drain current	T_C = 25 °C		295			A
P_{tot}	power dissipation	T_C = 25 °C		290			W
T_j	junction temperature			-55 to 150			°C
Symbol	**Parameter**	**Conditions**	**Notes**	**Min**	**Typ**	**Max**	**Unit**
Static characteristics							
$R_{DS(on)}$	drain-source on-state resistance	V_{GS} = 10 V, I_D = 50 A		-	1.60	1.90	mΩ
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	I_D = 20 A; V_{DS} = 50 V; V_{GS} = 10 V		-	103	-	nC

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2-8	S	source		
mb	D	mounting base; connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
W3MS100N019TL	TOLL	W3MS100N019TLJ	Reel	1200	TOLLO	11-Feb-2026

7. Marking

Table 4. Marking codes

Type number	Marking codes
W3MS100N019TL	W3MS 100N019TL

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V_{DS}	drain-source voltage			100	V
V_{GS}	gate-source voltage	static		±20	V
I_D	continuous drain current	T_C = 25 °C		295	A
		T_C = 100 °C		186	A
I_{DM}	pulsed drain current	T_C = 25 °C		1180	A
P_{tot}	power dissipation	T_C = 25 °C		290	W
E_{AS}	single pulse drain−to-source avalanche	I_{AS} = 34 A; R_{GS} = 25 Ω; V_{DD} = 50 V; T_j = 25 °C		578	mJ
I_{AS}	avalanche current, single pulse			34	A
T_{stg}	storage temperature			-55 to 150	°C
T_j	junction temperature			-55 to 150	°C

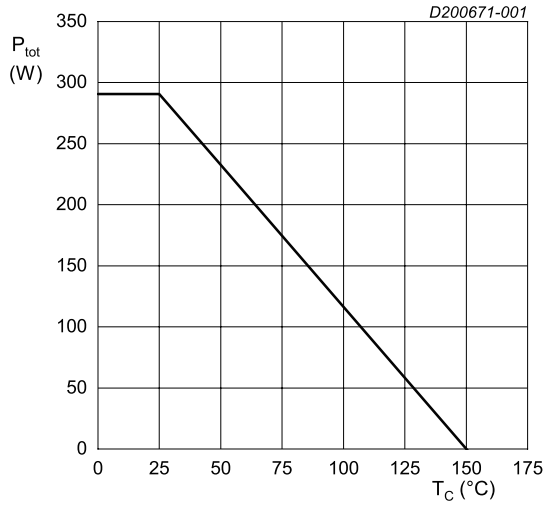


Fig. 1. Total power dissipation as a function of case temperature

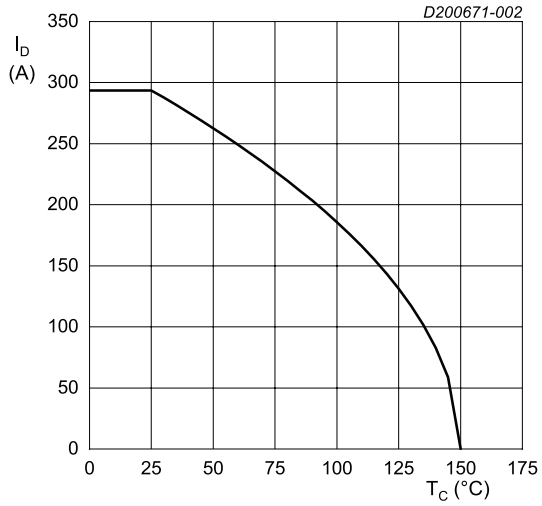


Fig. 2. Continuous Drain Current as a function of case temperature

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9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-c)}$	thermal resistance from junction to case			-	0.33	0.43	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air		-	-	50	K/W

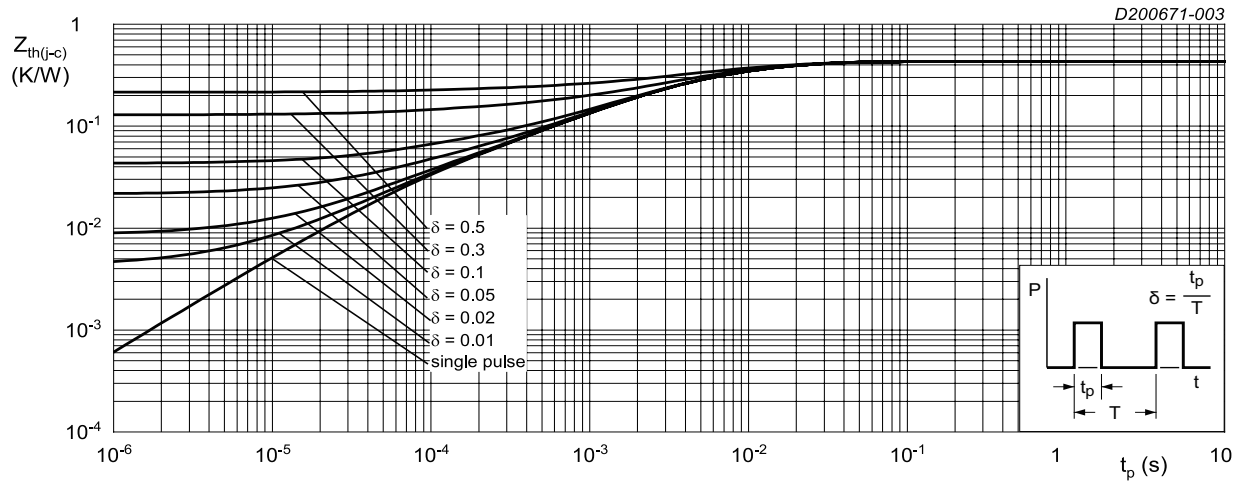


Fig. 3. Transient thermal impedance from junction to case as a function of pulse duration; maximum values

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10. Characteristics

Table 7. Characteristics

T_j = 25 °C unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V		100	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	I_D = 250 µA; V_{DS} = V_{GS}		2.0	3.0	4.0	V
I_{DSS}	drain leakage current	V_{DS} = 100 V; V_{GS} = 0 V		-	-	1	µA
		V_{DS} = 100 V; V_{GS} = 0 V; T_j = 125 °C		-	-	100	µA
I_{GSS}	gate leakage current	V_{GS} = ±20 V; V_{DS} = 0 V		-	-	±100	nA
$R_{DS(on)}$	drain-source on-state resistance	V_{GS} = 10 V; I_D = 50 A		-	1.60	1.90	mΩ
R_G	gate resistance	f = 1 MHz		-	1.4	-	Ω
g_{fs}	transconductance	V_{DS} = 5 V; I_D = 20 A; T_j = 25 °C		-	60	-	S
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	I_D = 20 A; V_{DS} = 50 V; V_{GS} = 10 V		-	103	-	nC
Q_{GS}	gate-source charge			-	32	-	nC
Q_{GD}	gate-drain charge			-	24	-	nC
C_{iss}	input capacitance	V_{DS} = 50 V; V_{GS} = 0 V; f = 100 kHz		-	6871	-	pF
C_{oss}	output capacitance			-	2718	-	pF
C_{rss}	reverse transfer capacitance			-	60	-	pF
$t_{d(on)}$	turn-on delay time	V_{DS} = 50 V; V_{GS} = 10 V; R_G = 3 Ω; I_D = 80 A		-	16	-	ns
t_r	rise time			-	25	-	ns
$t_{d(off)}$	turn-off delay time			-	45	-	ns
t_f	fall time			-	28	-	ns
Source-drain diode							
V_{SD}	source-drain voltage	V_{GS} = 0 V; I_S = 20 A		-	0.7	1.2	V
I_S	body-diode continuous current	T_C = 25 °C		-	-	295	A
t_{rr}	reverse recovery time	V_R = 50 V; I_F = 80 A; dI_F/dt = 100 A/µs		-	70	-	ns
Q_{rr}	reverse recovered charge			-	135	-	nC

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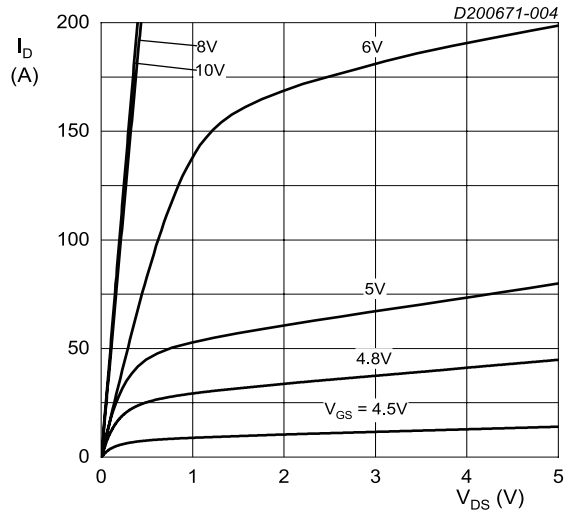
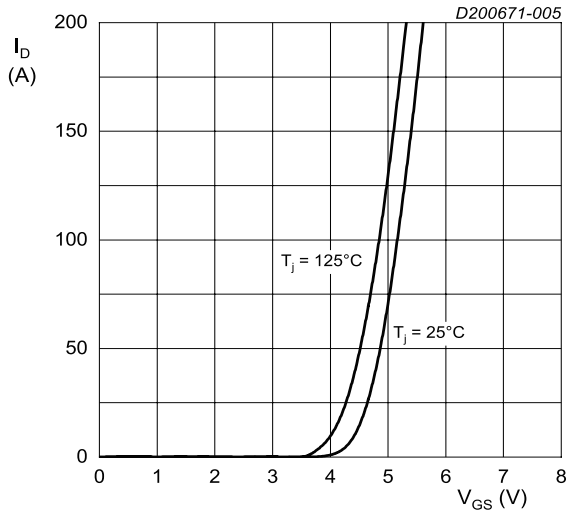


Fig. 4. Drain current as a function of drain-source voltage; typical values



V_{DS} = 5 V

Fig. 5. Drain current as a function of gate-source voltage; typical values

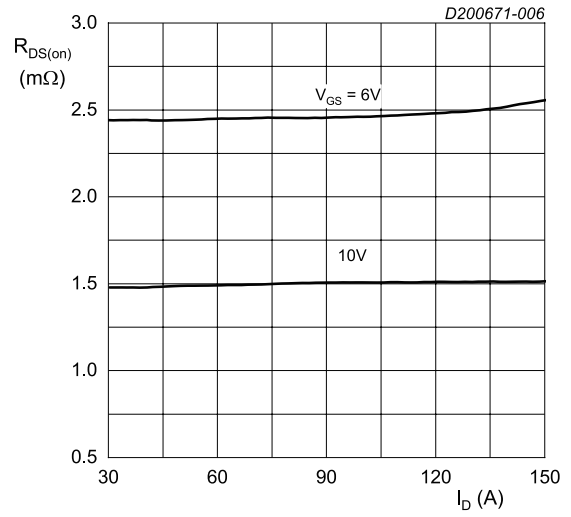
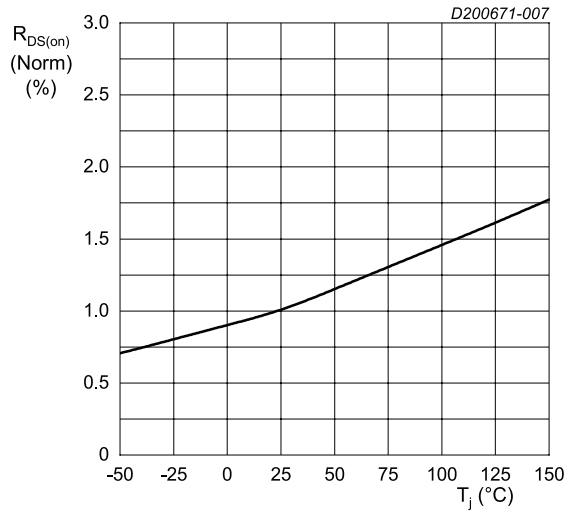


Fig. 6. Drain-source on-state resistance as a function of drain current; typical values

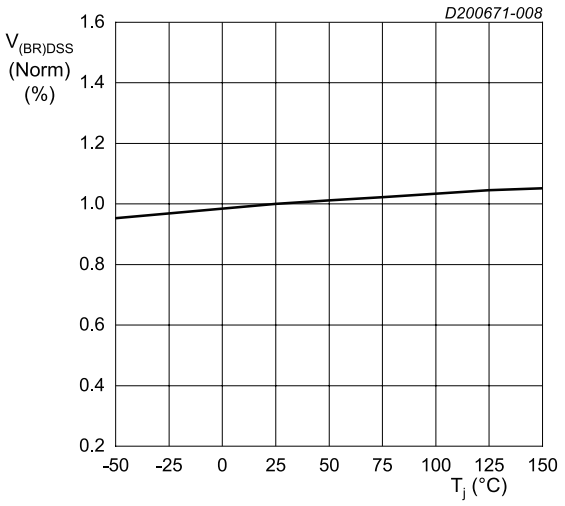


V_{GS} = 10 V; I_D = 50 A

Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature

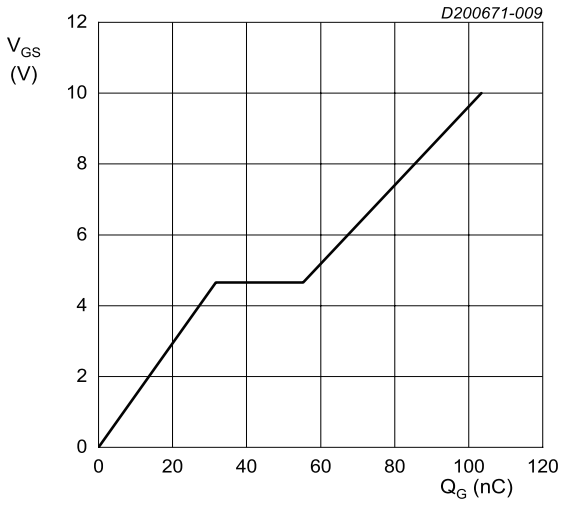
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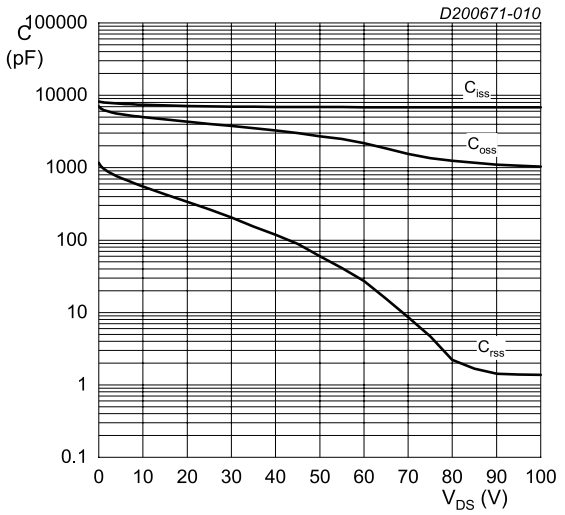
I_D = 1 mA

Fig. 8. Normalized drain-source breakdown voltage as a function of junction temperature



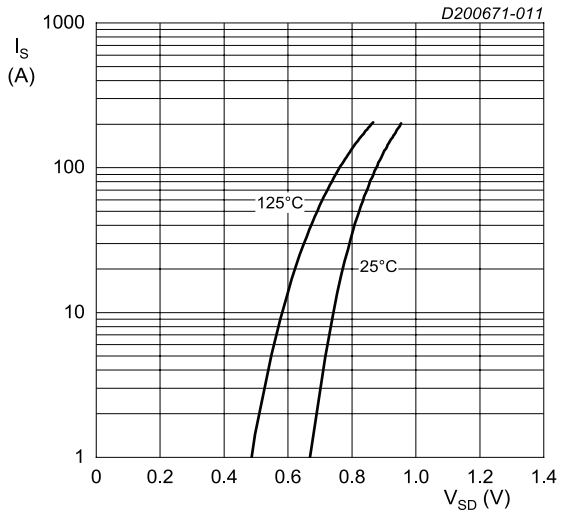
I_D = 20 A; V_{DS} = 50 V

Fig. 9. Gate-source voltage as a function of gate charge; typical values



V_{GS} = 0 V; f = 100 kHz

Fig 10. Capacitances as a function of drain-source voltage; typical values



V_{GS} = 0 V

Fig 11. Source current as a function of source-drain voltage; typical values

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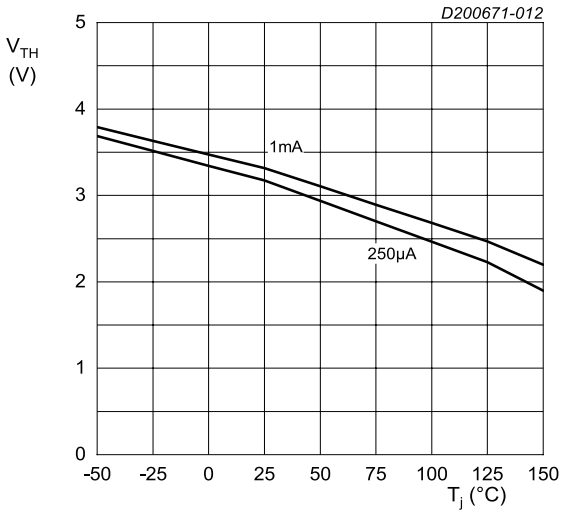
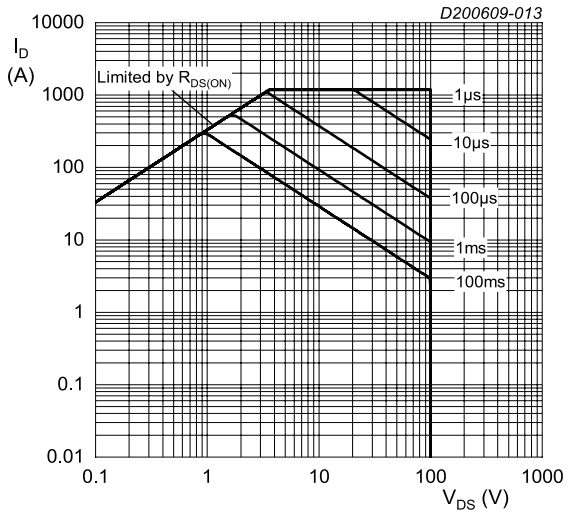


Fig. 12. Gate threshold voltage as a function of junction temperature



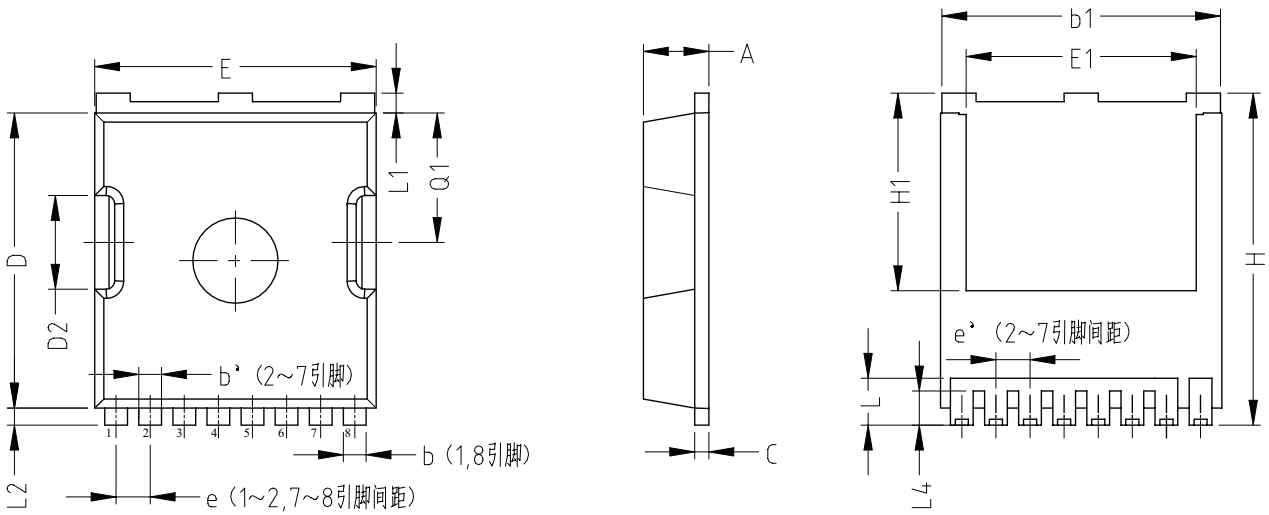
T_C = 25 °C

Fig. 13. Safe operating area

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11. Package outline



COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
A	2.15	2.30	2.45
b	0.70	0.75	0.85
b'	0.65	0.70	0.80
b1	9.65	9.80	9.95
C	0.45	0.50	0.60
D	10.18	10.38	10.58
D2	3.15	3.30	3.45
E	9.70	9.90	10.10
E1	7.95	8.10	8.25
e	BSC 1.225		
e'	BSC 1.20		
Q1	4.40	4.55	4.70
H	11.48	11.68	11.88
H1	6.80	6.95	7.10
L	1.60	1.80	2.00
L1	0.50	0.70	0.90
L2	0.48	0.60	0.72
L4	1.00	1.15	1.30

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