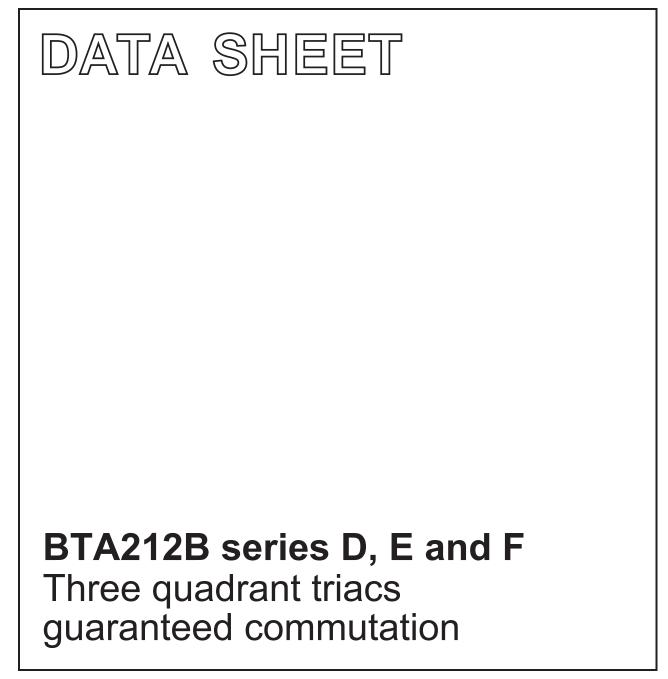
DISCRETE SEMICONDUCTORS



Product specification

September 2023



MAX

800E

800

12

95

UNIT

V

А

А

## Three quadrant triacs guaranteed commutation

### BTA212B series D, E and F

MAX.

600D

600E

600F

600

12

95

#### **GENERAL DESCRIPTION**

Passivated guaranteed commutation triacs in a plastic envelope suitable for surface mounting intended for use in motor control circuits or with other highly inductive loads. These devices balance the requirements of commutation performance and gate sensitivity. The "sensitive gate" E series and "logic level" D series are intended for interfacing with low power drivers, including micro controllers.

DESCRIPTION

main terminal 1

main terminal 2

main terminal 2

#### **PINNING - TO263**

PIN

1

2

3

mb

# 

**PIN CONFIGURATION** 

QUICK REFERENCE DATA

voltages

current

PARAMETER

Repetitive peak off-state

Non-repetitive peak on-state

RMS on-state current

**SYMBOL** 

VDRM

I<sub>T(RMS)</sub>

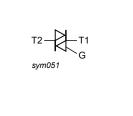
ITSM

#### SYMBOL

**BTA212B-**

BTA212B-

**BTA212B-**



#### **LIMITING VALUES**

gate

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	M	UNIT	
V <sub>DRM</sub>	Repetitive peak off-state voltages		-	<b>-600</b> 600 <sup>1</sup>	<b>-800</b> 800	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 99 °C	-		12	A
I <sub>TSM</sub>	Non-repetitive peak on-state current	full sine wave; $T_j = 25 °C$ prior to surge				
		t = 20 ms t = 16.7 ms	-		95 05	A
l²t dl <sub>⊤</sub> /dt	I <sup>2</sup> t for fusing Repetitive rate of rise of on-state current after	t = 10  ms $I_{TM} = 20 \text{ A}; I_G = 0.2 \text{ A};$ $dI_G/dt = 0.2 \text{ A}/\mu\text{s}$	-	4	45 00	A A²s A/µs
$\begin{matrix} I_{GM} \\ P_{GM} \\ P_{G(AV)} \end{matrix}$	triggering Peak gate current Peak gate power Average gate power	over any 20 ms	-		2 5 0.5	A W W
T <sub>stg</sub> T <sub>j</sub>	Storage temperature Operating junction temperature	period	-40 -	1	50 25	° C C

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15  $A/\mu s$ .

### BTA212B series D, E and F

#### THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R <sub>th j-mb</sub> R <sub>th j-a</sub>	Thermal resistance junction to mounting base Thermal resistance junction to ambient	full cycle half cycle in free air	-	- - 55	1.5 2.0 -	K/W K/W K/W

#### STATIC CHARACTERISTICS

 $T_i = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
		BTA212B-	D	D	E	F	
I <sub>GT</sub>	Gate trigger current <sup>2</sup>	$V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}$		_			
		T2+ G+   T2+ G-	-	5 5 5	10 10	25 25	mA mA
		T2- G-	-	5	10	25	mA
IL.	Latching current	$V_{D} = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$		4.5	0.5		
		T2+ G+ T2+ G-	-	15 25	25 30	30 40	mA mA
		T2- G-	-	25	30	40	mA
I <sub>H</sub>	Holding current	V <sub>D</sub> = 12 V; I <sub>GT</sub> = 0.1 A	-	15	25	30	mA
			D, E, F				
V <sub>T</sub> V <sub>GT</sub>	On-state voltage	$I_{T} = 17 \text{ A}$	-		1.6		V
V <sub>GT</sub>	Gate trigger voltage	$\dot{V}_{\rm D} = 12 \text{ V}; \text{ I}_{\rm T} = 0.1 \text{ A}$	0.05		1.5		VV
		$V_{\rm D} = 400 \text{ V}; I_{\rm T} = 0.1 \text{ A};$ $T_{\rm i} = 125 \text{ °C}$	0.25		-		V
I <sub>D</sub>	Off-state leakage current	$V_{\rm D} = V_{\rm DRM(max)}; T_{\rm j} = 125 ^{\circ}{\rm C}$	-		0.5		mA

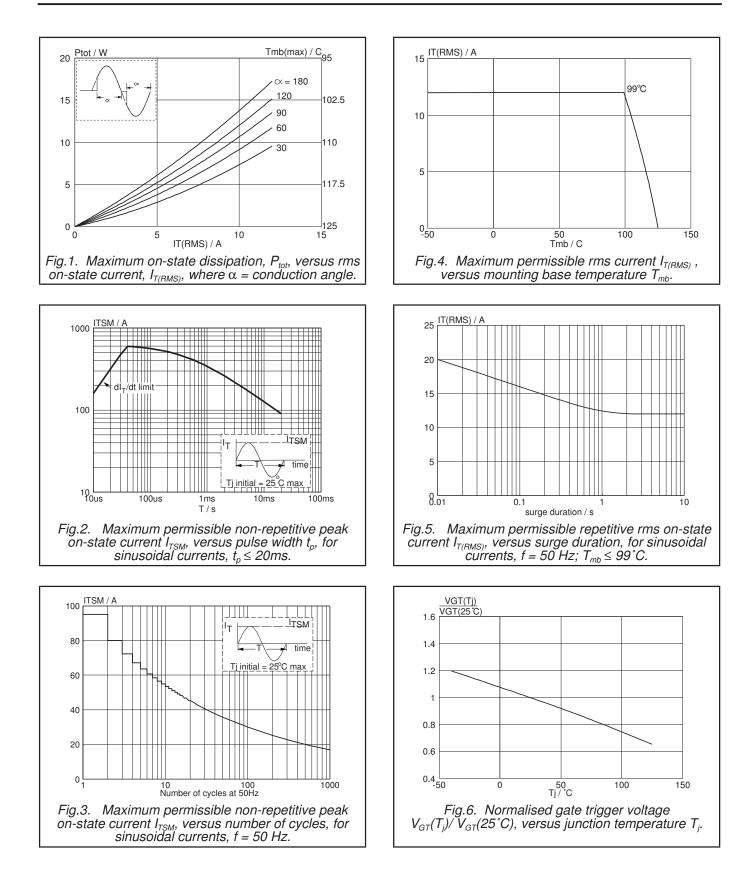
#### **DYNAMIC CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise stated

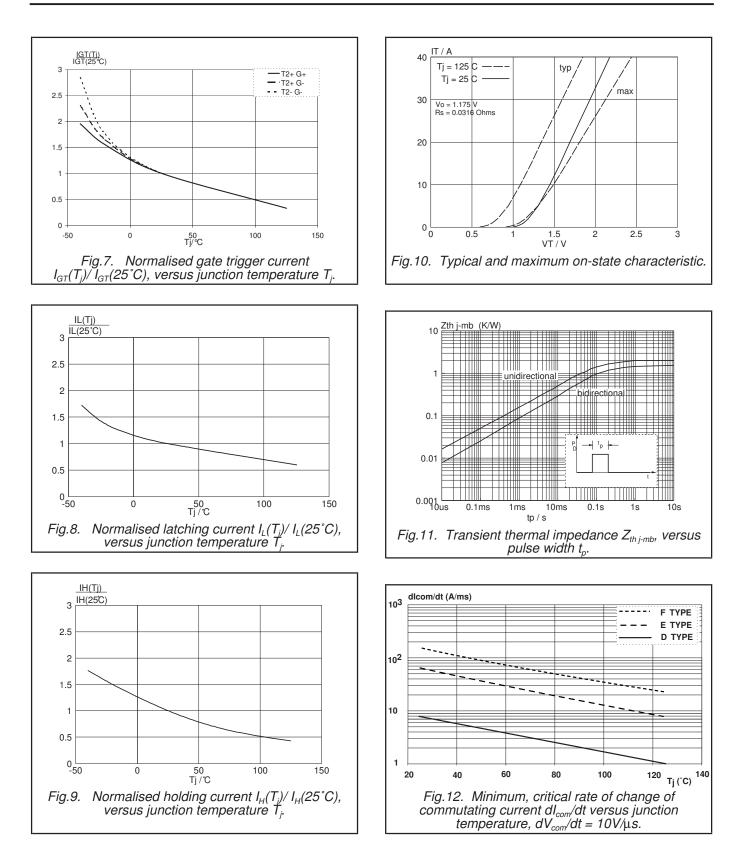
SYMBOL	PARAMETER	CONDITIONS		MIN.		MAX.	UNIT
		BTA212B-	D	E	F		
dV <sub>D</sub> /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)};$ $T_j = 110 °C;$ exponential waveform; gate open circuit	20	60	70	-	V/µs
dl <sub>com</sub> /dt	Critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; \text{T}_{j} = 125 \text{ °C};$ $I_{T(RMS)} = 12 \text{ A};$ $dV_{com}/dt = 10 V/\mu \text{s}; \text{ gate}$ open circuit	1.0	8	21	-	A/ms
dl <sub>com</sub> /dt	Critical rate of change of commutating current	$ \begin{array}{l} V_{\text{DM}} = 400 \; V; \; T_{j} = 125 \; ^{\circ}\text{C}; \\ I_{\text{T(RMS)}} = 12 \; \text{A}; \\ dV_{\text{com}}/dt = 0.1 \; V/\mu\text{s}; \; \text{gate} \\ \text{open circuit} \end{array} $	3.5	16	32	-	A/ms

**<sup>2</sup>** Device does not trigger in the T2-, G+ quadrant.

### BTA212B series D, E and F

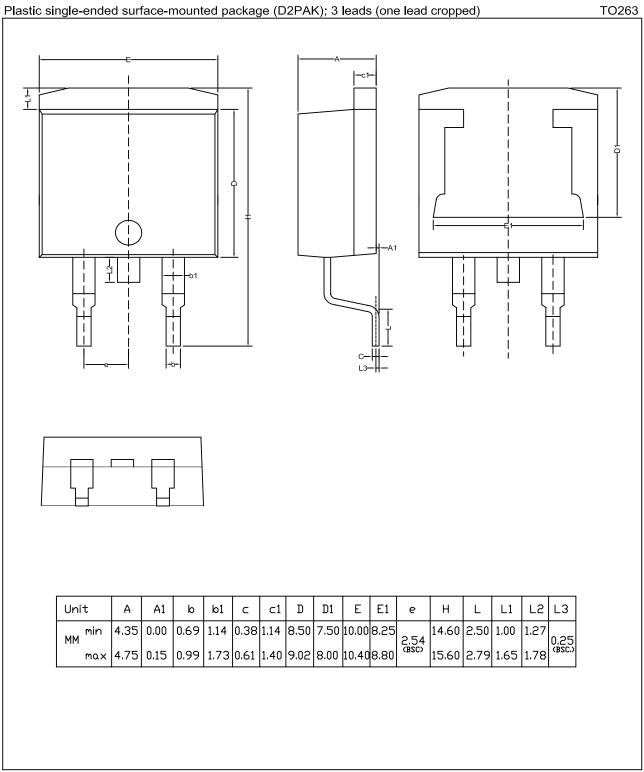


### BTA212B series D, E and F



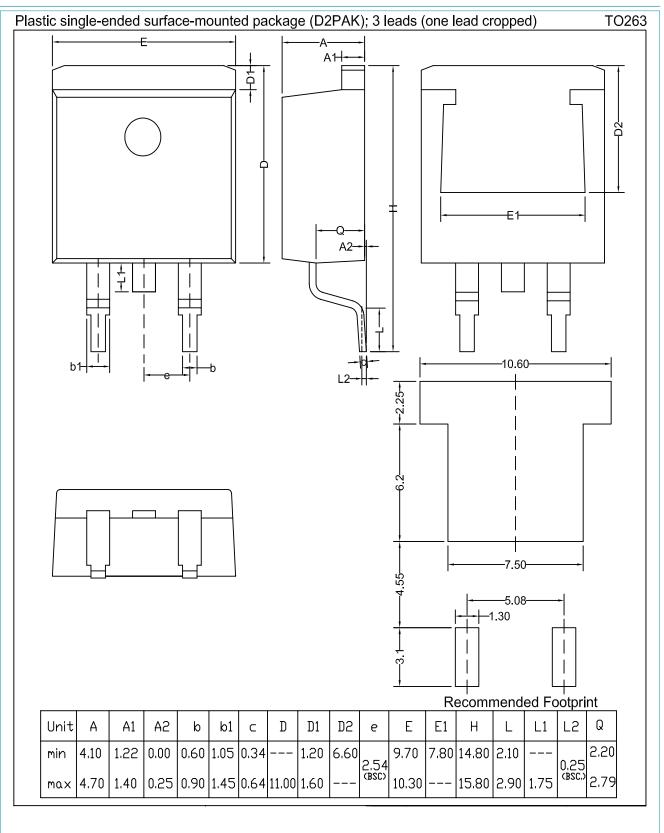
### BTA212B series D, E and F

#### Assembly factory: E



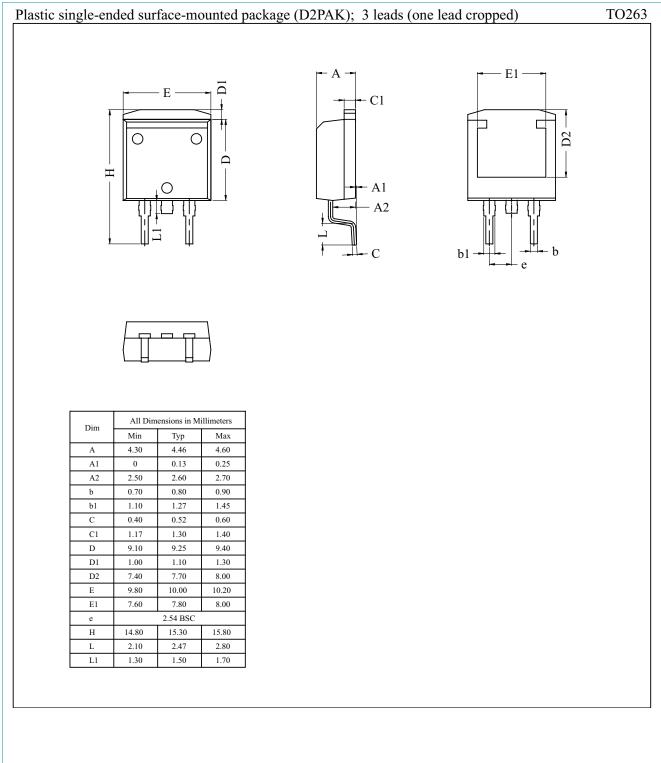
### BTA212B series D, E and F

Assembly factory: N



#### BTA212B series D, E and F

Assembly factory: P



#### Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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