**Product data sheet** 

## 1. General description

The ESDALD05UD4 is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time. The ESDALD05UD4 suited for use in USB 2.0, Firewire, DVI, and Gigabit Ethernet Interfaces.

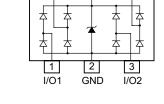


VDD

5

### 2. Features and benefits

- Peak pulse power 88W @ 8/20µs waveform
- Protects four I/O lines and one V<sub>cc</sub> line
- IEC 61000-4-2 (ESD) ±15kV(air), ±8kV(contact)
- IEC 61000-4-5 (Lightning) 5.5A (8/20µs)
- Low capacitance
- Low leakage current
- · Low clamping voltage
- Meet MSL level1
- Halogen free and RoHS compliant



1/04

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## 3. Applications

- USB 2.0
- Digital Visual Interface (DVI)
- IEEE 1394 Firewire Ports
- Notebooks & Handhelds
- Projection TV & Monitors
- Set-top box
- Flat Panel Displays
- PCI Express





I/O3

4

# 4. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Marking	Package issue date
ESDALD05UD4	SOT23-6L	ESDALD05UD4X	Tape and reel	3000	V05	13-Oct-2020

# 5. Absolute maximum ratings

In accordance with the Absolute Maximum Rating System (IEC 60134).

 $T_i = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions	Values	Unit		
Absolute	Absolute maximum rating					
P <sub>PPM</sub>	peak pulse power	t <sub>p</sub> = 8/20 μs	88	W		
I <sub>PP</sub>	peak pulse current	t <sub>p</sub> = 8/20 μs	5.5	Α		
V <sub>ESD</sub>	ESD per IEC 61000-4-2 (air) ESD per IEC 61000-4-2 (contact)		±15 ±8	kV kV		
T <sub>stg</sub>	storage temperature range		-55 to 150	°C		
T <sub>j</sub>	operating temperature range		-55 to 150	°C		

### 6. Characteristics

 $T_i$  = 25 °C unless otherwise specified.

Symbol	Parameter	Condition	Min	Тур	Max	Unit
$V_{RWM}$	Reverse Working Voltage	Any I/O pin to GND	-	-	5	V
$V_{BR}$	Reverse Breakdown Voltage	I <sub>T</sub> = 1 mA; Any I/O pin to GND	6	-	-	V
I <sub>R</sub>	Reverse Leakage Current	V <sub>RWM</sub> = 5 V; Any I/O pin to GND	-	-	100	nA
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 15 mA	-	-	1.2	V
V <sub>C</sub>	Clamping Voltage	$I_{pp}$ = 1 A; $t_p$ = 8/20 µs; Any I/O pin to GND	-	-	10	V
		$I_{pp}$ = 5.5 A; $t_p$ = 8/20 µs; Any I/O pin to GND	-	-	16	V
CJ	Junction Capacitance	V <sub>R</sub> = 0 V; f = 1 MHz; Between I/O pins	-	0.3	0.5	pF
		$V_R = 0$ V; f = 1 MHz; Any I/O pin to GND	-	0.6	1	pF

120

80

40

20

0

25

 $\mathsf{P}_{\mathsf{P}\!\underline{\mathsf{PM}}}$ 

(%)

P<sub>PPM(25°C)</sub> 100

Note: I/O pins are pin 1,3,4,6

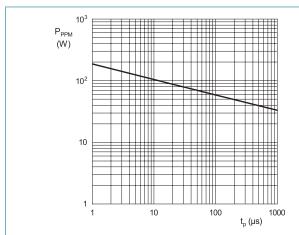


Fig. 1. Pulse rating curve Fig. 2. Peak pulse power derating curve

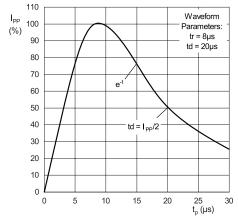


Fig. 3. Pulse waveform

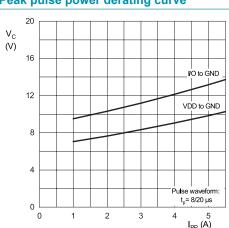
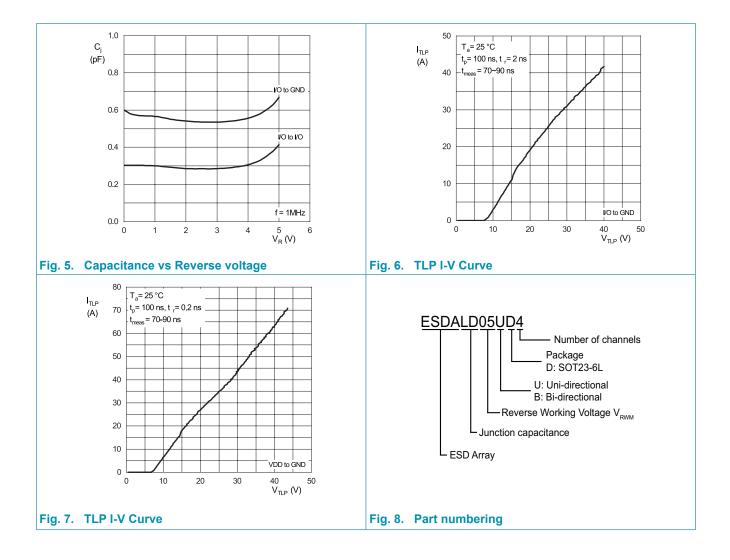


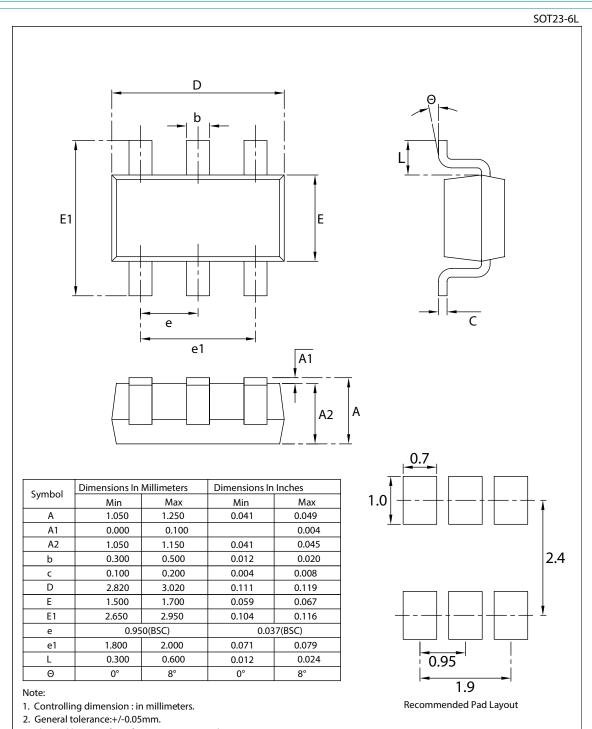
Fig. 4. Clamping voltage vs Peak pulse current

150

### **ESD Protection Diodes Array**



# 7. Package outline



3. The pad layout is for reference purposes only.

### **ESD Protection Diodes Array**

## 8. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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**ESD Protection Diodes Array** 

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