DISCRETE SEMICONDUCTORS

DATA SHEET

BUJ105ABSilicon Diffused Power Transistor

Product specification

October 2018



Silicon Diffused Power Transistor

BUJ105AB

GENERAL DESCRIPTION

High-voltage, high-speed planar-passivated npn power switching transistor in SOT404 (D²-PAK) surface-mount package intended for use in high frequency electronic lighting ballast applications, converters, inverters, switching regulators, motor control systems, etc.

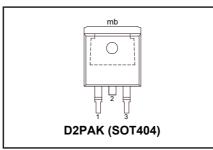
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 \text{ V}$	-	700	V
V _{CBO}	Collector-Base voltage (open emitter)		-	700	V
V _{CEO}	Collector-emitter voltage (open base)		-	400	V
I _C	Collector current (DC)		-	8	Α
1 1	Collector current peak value		-	16	Α
P _{tot}	Total power dissipation	$T_{mb} \le 25$ °C	-	125	W
V _{CEsat}	Collector-emitter saturation voltage	$I_{\rm C} = 4.0 \text{ A}; I_{\rm B} = 0.8 \text{ A}$	0.3	1.0	V
h _{FEsat}		$I_{\rm C} = 4.0 \text{ A}; V_{\rm CE} = 5 \text{ V}$	11	15	
t _f	Fall time	$I_{\rm C} = 5 \text{A}; I_{\rm B1} = 1 \text{A}$	20	50	ns

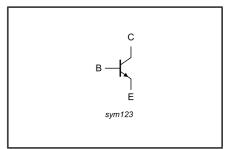
PINNING - SOT404

PIN	DESCRIPTION	
1	base	
2	collector	
3	emitter	
mb	collector	

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CESM}	Collector to emitter voltage	$V_{BF} = 0 \text{ V}$	-	700	V
V _{CEO}	Collector to emitter voltage (open base)		-	400	V
V _{CBO}	Collector to base voltage (open emitter)		-	700	V
I _C	Collector current (DC)		-	8	Α
I _{CM}	Collector current peak value		-	16	Α
I _B	Base current (DC)		-	4	Α
ا ا	Base current peak value		-	8	Α
P _{tot}	Total power dissipation	$T_{mb} \le 25 ^{\circ}C$	-	125	W
T _{stq}	Storage temperature		-65	150	°C
T _i	Junction temperature		-	150	°C

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
R _{th j-mb}	Thermal resistance junction to mounting base		1	1.0	K/W
$R_{th i-a}$	Thermal resistance junction to ambient	minimum footprint, FR4 board	55	-	K/W

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STATIC CHARACTERISTICS

 T_{mb} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CES} ,I _{CBO}	Collector cut-off current ¹		-	-	0.2 0.5	mA mA
I _{CEO} I _{EBO} V _{CEOsust}	Collector cut-off current Emitter cut-off current Collector-emitter sustaining voltage	$V_{CEO} = V_{CEOMmax} (400V)$ $V_{EB} = 9 \text{ V; } I_{C} = 0 \text{ A}$ $I_{B} = 0 \text{ A; } I_{C} = 10 \text{ mA;}$ $I_{C} = 25 \text{ mH}$	- - 400	- - -	0.1 1 -	mA mA V
V _{CEsat} V _{BEsat} h _{FE} h _{FE} h _{FEsat}	Collector-emitter saturation voltage Base-emitter saturation voltage DC current gain	$\begin{aligned} &I_{C} = 4.0 \text{ A;} I_{B} = 0.8 \text{ A} \\ &I_{C} = 4.0 \text{ A;} I_{B} = 0.8 \text{ A} \\ &I_{C} = 1 \text{ mA;} \text{ V}_{CE} = 5 \text{ V} \\ &I_{C} = 500 \text{ mA;} \text{ V}_{CE} = 5 \text{ V} \\ &I_{C} = 4.0 \text{ A;} \text{ V}_{CE} = 5 \text{ V} \end{aligned}$	- 10 13 8	0.3 1.0 14 23 11	1.0 1.5 34 36 15	>>

DYNAMIC CHARACTERISTICS

 T_{mb} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
	Switching times (resistive load)	$I_{Con} = 5 \text{ A}; I_{Bon} = -I_{Boff} = 1 \text{ A}; R_L = 75 \text{ ohms}; V_{BB2} = 4 \text{ V};$			
t _{on}	Turn-on time		0.65	1	μs
t _s	Turn-off storage time Turn-off fall time		1.8 0.3	2.5 0.5	μs
Lf			0.3	0.5	μs
	Switching times (inductive load)	$I_{Con} = 5 \text{ A}; I_{Bon} = 1 \text{ A}; L_{B} = 1 \mu\text{H}; $ - $V_{BB} = 5 \text{ V}$			
t _s	Turn-off storage time Turn-off fall time	PB V	1.2 20	1.7 50	μs ns
	Switching times (inductive load)	$I_{Con} = 5 \text{ A}; I_{Bon} = 1 \text{ A}; L_{B} = 1 \mu\text{H}; \\ -V_{BB} = 5 \text{ V}; T_{i} = 100 ^{\circ}\text{C}$			
t _s	Turn-off storage time	, , , -	1.4	1.9	μs
t _f	Turn-off fall time		25	100	ns

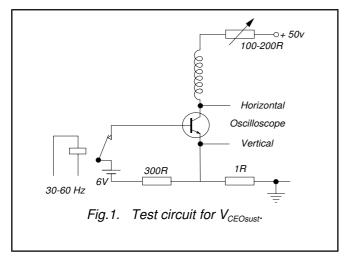
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¹ Measured with half sine-wave voltage (curve tracer).

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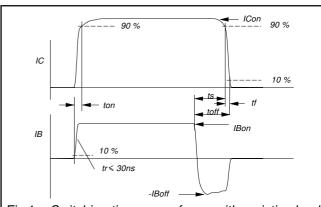
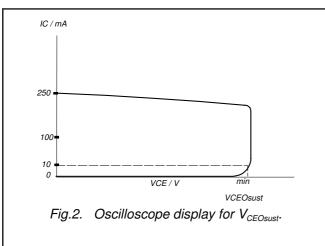
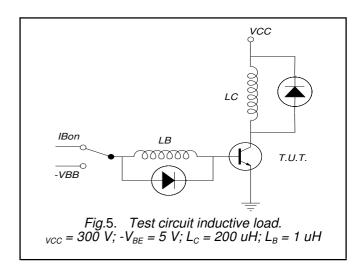
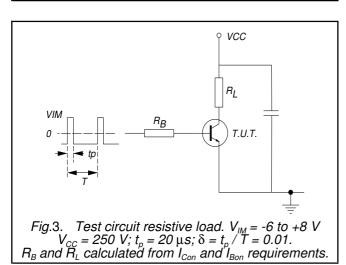
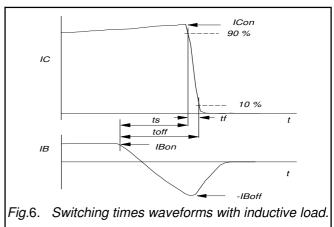


Fig.4. Switching times waveforms with resistive load.



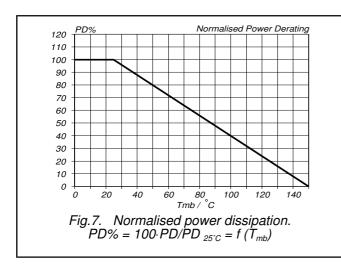


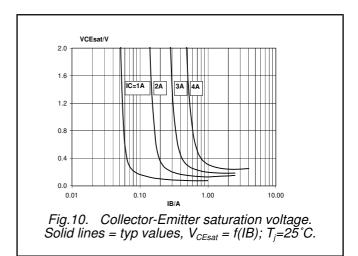


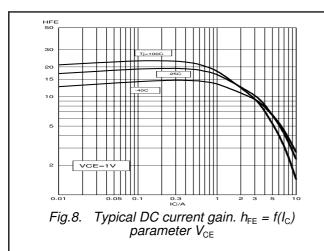


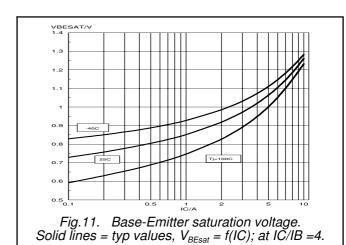
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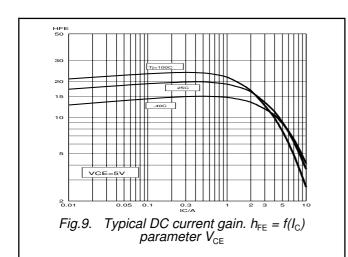
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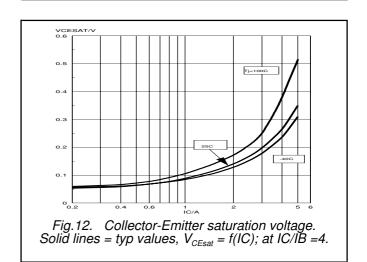








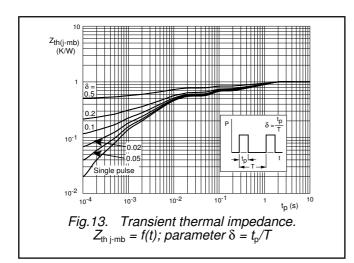




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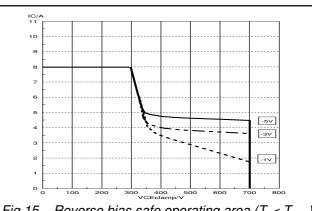


Fig.15. Reverse bias safe operating area $(T_j < T_{jmax})$ for $-V_{BE} = 5V, 3V \& 1V$.

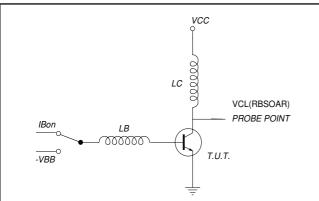


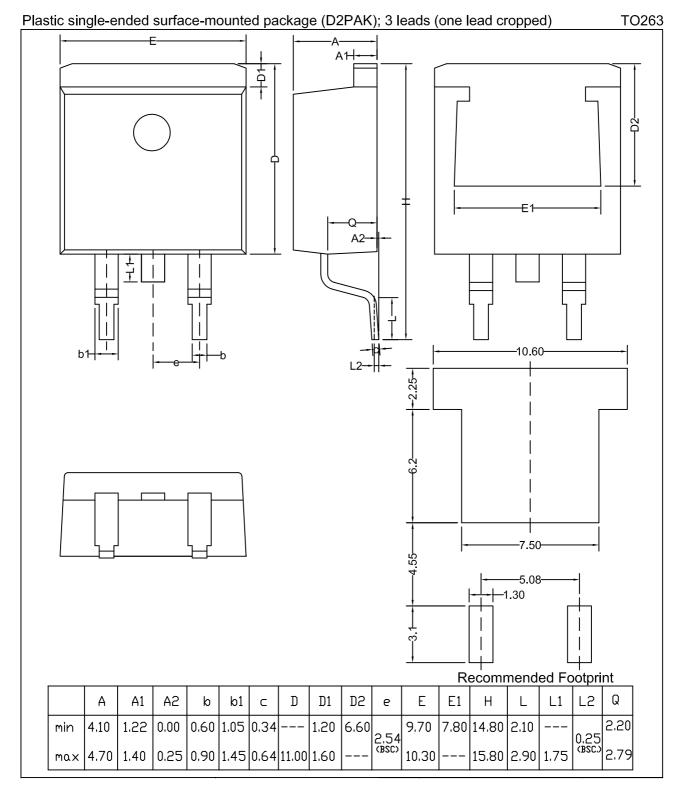
Fig.14. Test circuit for reverse bias safe operating area.

$$\begin{split} V_{clamp} < 700V; \ V_{cc} = 150V; \ -V_{be} = 5V, 3V \ \& \ 1V; \\ L_B = 1 \mu H; \ L_C = 200 \mu H. \end{split}$$

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MECHANICAL DATA



Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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